

# GUDLA MADHAVI

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## LINKS

<https://www.linkedin.com/in/gudla-madhavi-816261227>

## PROFILE

Hardworking and enthusiastic student looking forward to work in a company where I can expand my skills while working for the company goals.

## EDUCATION

Aug 2021 — Present	B.Tech, Rajiv Gandhi University of Knowledge Technologies	Kadapa, Andhra Pradesh
Aug 2019 — Aug 2021	PUC, Rajiv Gandhi University of Knowledge Technologies CGPA : 9.86	Kadapa, Andhra Pradesh

## SKILLS

Matlab	Analog Layout
Digital Electronics	Basic C
Arduino	Xilinx

## LANGUAGES

English	Telugu
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## COURSES AND PROJECTS

Sep 2023 — Nov 2023	Learned Ripple carry adder, sequence detector, PWM of a signal- using verilog in Quartus, implemented path planner algorithm in C language while participating in E-Yantra Robotics Competition.
Aug 2023 — Nov 2023	Designed Layouts of NAND, NOR, D flipflop using custom compiler in synopsis and learned basics of VLSI Backend, SURE TRUST  Worked on project 'Verilog Program to interface an ADC' in 2nd year of B.Tech
Feb 2024 — Present	VLSI for Beginners, NIELIT Calicut

## CERTIFICATES AND HONOURS

Apr 2023 — Apr 2023	participation Certificate in Astronic 4.0 workshop on NodeMCU and IoT in Abhiyanth 2K23	RGUKT
	Won 2nd prize in Grandtest	espark 2K23, RGUKT
	Matlab OnRamp	

## EXTRA-CURRICULAR ACTIVITIES

Volunteering - as student coordinator at Alumni cell, HHO, Department etc.,, RGUKT

## YOGA

[https://drive.google.com/drive/folders/1eUV8d\\_R0ByMj1WDiNwtwn\\_4WiCjflOuy?usp=sharing](https://drive.google.com/drive/folders/1eUV8d_R0ByMj1WDiNwtwn_4WiCjflOuy?usp=sharing)