

Kuntumalla Kusumalatha

Dorasanipalli,proddatur,Kadapa
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Objective

A meticulous and organized individual seeking an entry level position in the field of VLSI. Creative and willing to come up with new ideas and curious to learn new things.

Experience

- Fresher

Education

- RGUKT RK valley, Idupulapaya** Persuing
Btech (ECE)
9.0
- RGUKT RK Valley, Idupulapaya** 2021
Intermediate
9.86
- Z.P.H.School** 2019
SSC
10

Skills

- Basics of C language
- Basics of Verilog
- Digital Electronics

Projects

- Implementation of voting machine**
Implementation of electronic voting machine in verilog by using xilinx ISE.
Developed a finite state machine for voting machine.
Implementation of digital clock using verilog and dumped into FPGA

Interests

- Reading books
- Drawing
- Listening music

Languages

- English
- Telugu