AI on Chip 2024

LAB Ⅳ Processing Elements

REPORT

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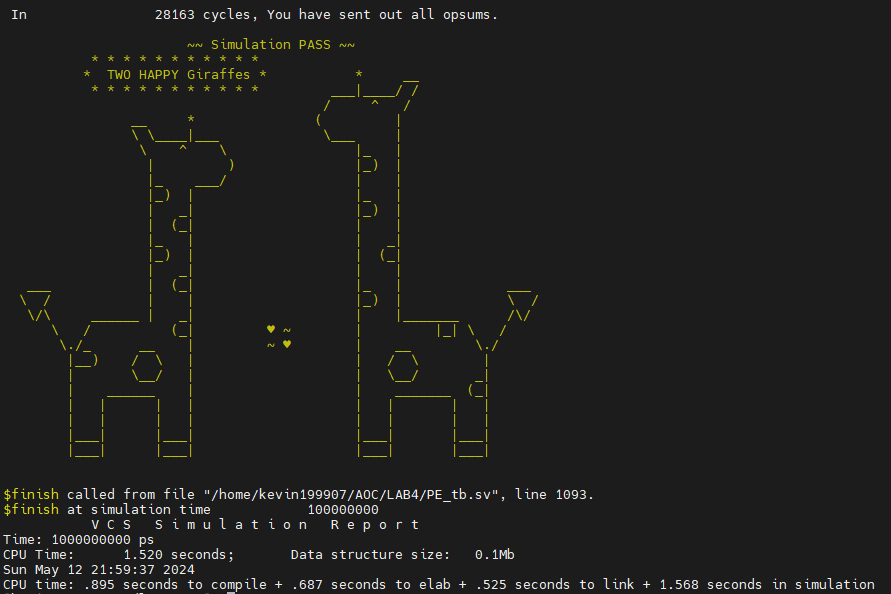
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1. Implementation Result
2. TB.0

一張含有 文字, 螢幕擷取畫面, 圖表, 字型 的圖片

自動產生的描述

1. TB.1



1. TB.2

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自動產生的描述

1. Implementation Cycle Result

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Testbench | Tb0 | Tb1 | Tb2 | Tb3 |
| Clock Cycle | 922 | 28163 | 8109 | - |

1. PE architecture and Design ideas
2. PE architecture
3. Spad storage space and functionality
4. FSM state and functionality
5. Waveform Result
6. Demonstrate Scenario B dataflow
7. Compare scenario A,B,C from different aspects
8. Share your thoughts