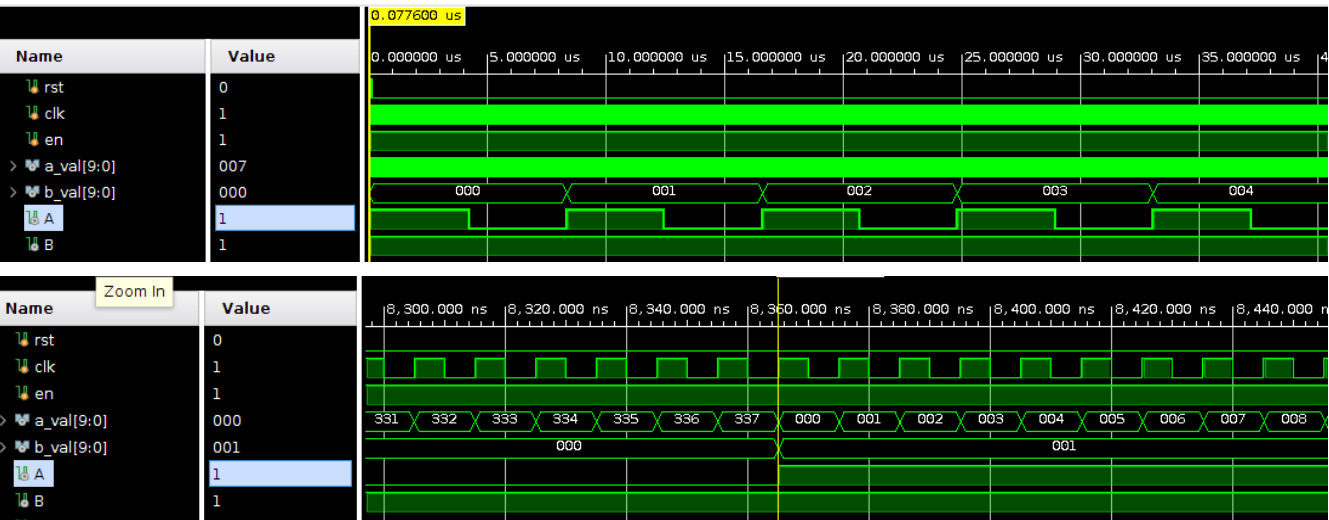


Requirement 1: count_wrap testbench simulation

Testing out the basic enable and sync reset functionality:



Showing the b-increment behavior:



Requirement 2: Using the clocking wizard

The output of the clocking wizard is called clkslow here:

