

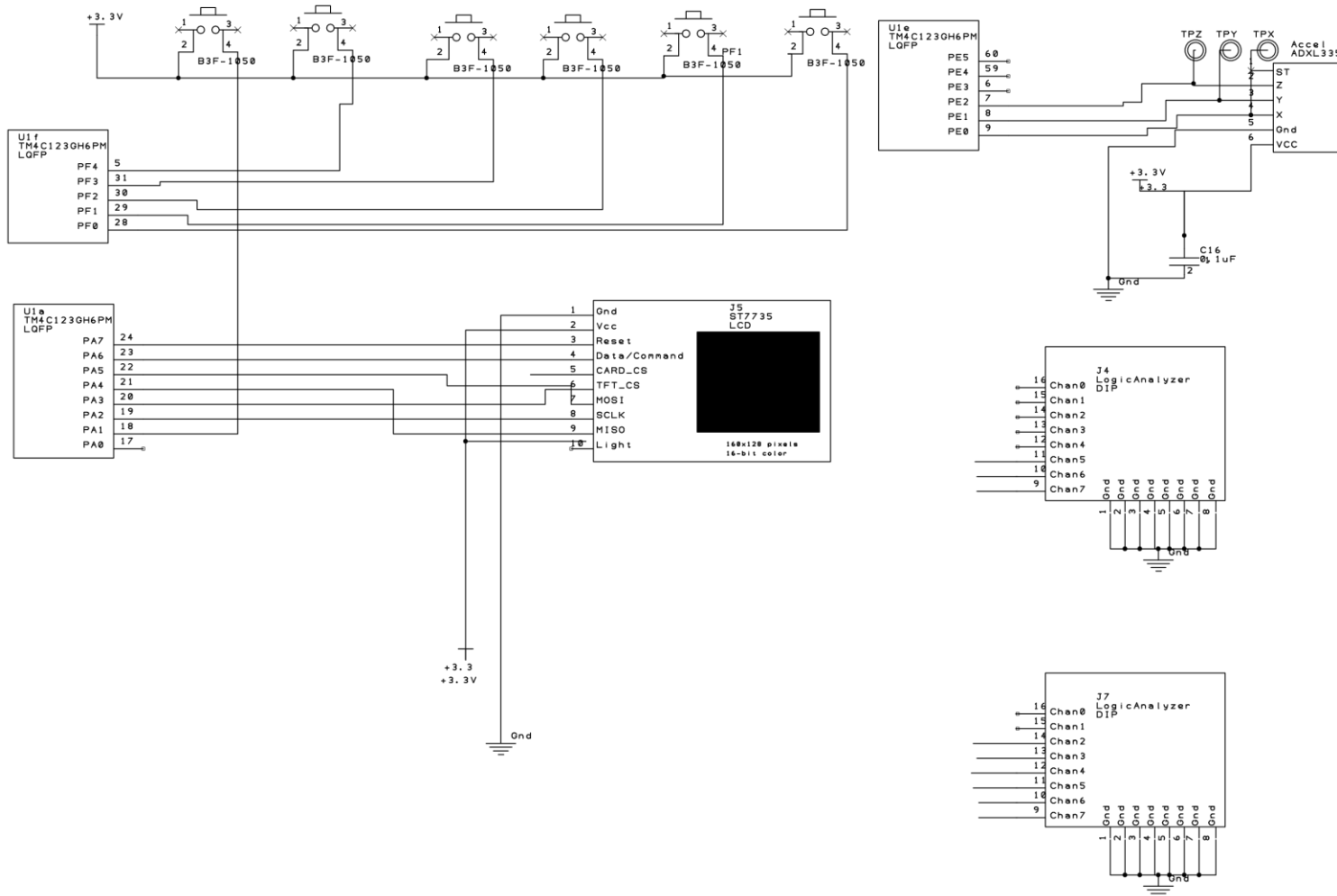
University Of Texas At Austin

Schematic Name: Lab 7: Soundtrack Device

Name(s): Kevin Yee and Stefan Bordovsky

Date: 3/19/2017

Semester: Spring 2017



University Of Texas At Austin

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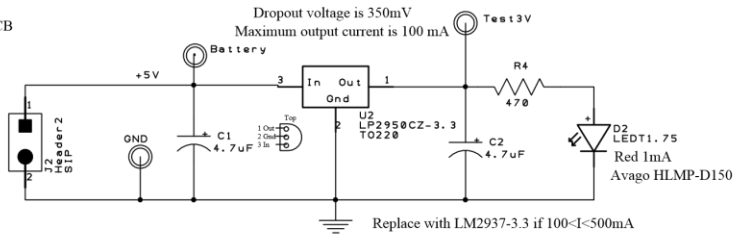
Name(s): Kevin Yee and Stefan Bordovsky

Date: 3/19/2017

Semester: Spring 2017

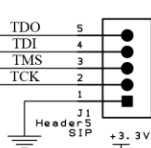
Battery external to PCB

+5V battery



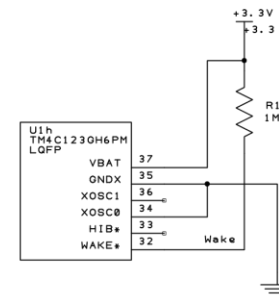
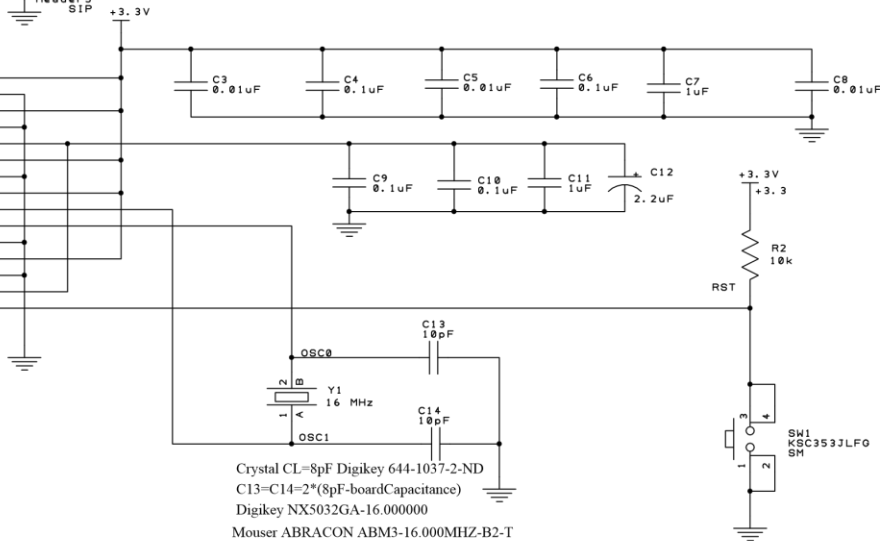
U1c
TM4C1230H6PM
LGFP

PC7 13
PC6 14
PC5 15
PC4 16
PC3 49
PC2 50
PC1 51
PC0 52



U1g
TM4C1230H6PM
LGFP

VDDA 2
GND 3
VDD 11
GND 12
VDDC 25
VDD 26
GND 27
VDD 42
OSC1 41
OSC0 40
GND 39
VDD 54
GND 55
VDDC 56
RST* 38



University Of Texas At Austin

Schematic Name: Lab 7: Power Clock JTAG

Name(s): Kevin Yee and Stefan Bordovsky

Date: 3/19/2017

Semester: Spring 2017

