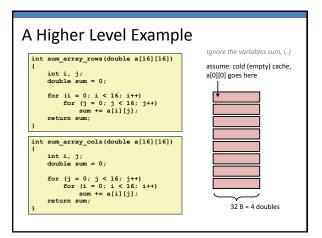
Lecture 22

# **Writing Cache**friendly Code

**CPSC 275** Introduction to Computer Systems



## Writing Cache Friendly Code

- Make the common case go fast
  - Focus on the inner loops of the core functions
- Minimize the misses in the inner loops
  - Repeated references to variables are good (temporal locality)
  - Stride-1 reference patterns are good (spatial locality)

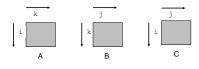
# Matrix Multiplication Example

- Description:
  - Multiply N x N matrices
  - O(N<sup>3</sup>) total operations
  - N reads per source element
  - N values summed per destination
    - but may be able to hold in register

```
/* ijk */
                      held in register
for (i=0; i<n; i++)
  for (j=0; j<n; j++) {
    sum = 0.0; +
    for (k=0; k<n; k++)
      sum += a[i][k] * b[k][j];
    c[i][j] = sum;
```

### Miss Rate Analysis for Matrix Multiply

- Assume:
  - Line size = 32 bytes (big enough for 4 doubles)
  - Matrix dimension (N) is very large
  - Cache is not even big enough to hold multiple rows
- Analysis Method:
  - Look at access pattern of inner loop



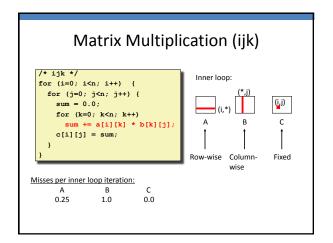
### Layout of C Arrays in Memory

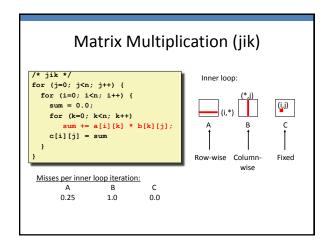
- C arrays allocated in row-major order
  - each row in contiguous memory locations
- Stepping through columns in one row:

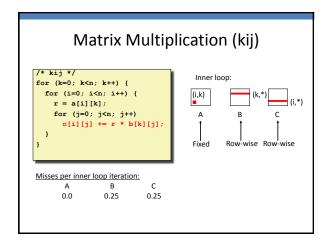
```
for (i = 0; i < N; i++)
  sum += a[0][i];
```

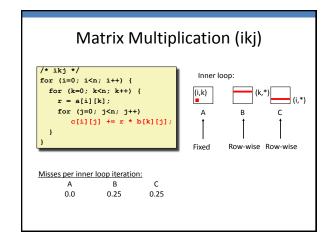
- accesses successive elements
- exploits spatial locality
- Stepping through rows in one column:
   for (i = 0; i < n; i++)
   sum += a[i][0];</pre>

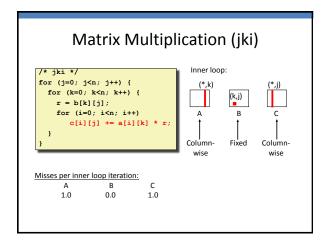
- accesses distant elements
- no spatial locality!

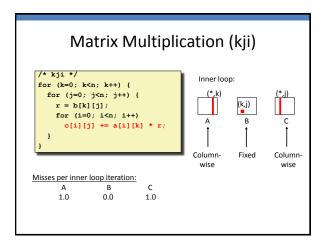


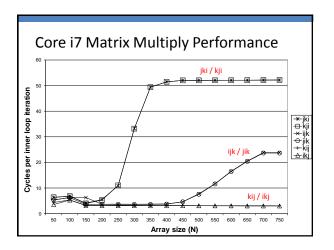






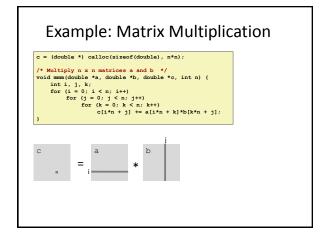


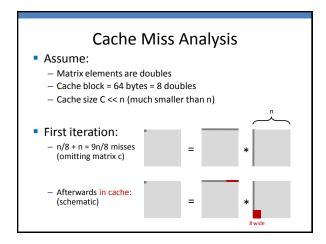


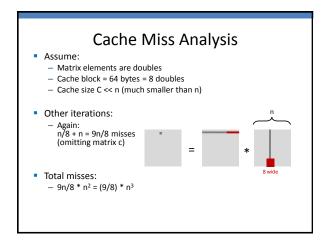


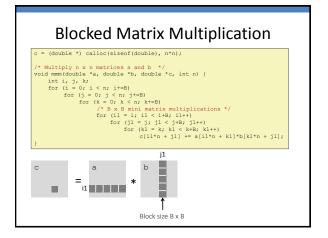
# **Optimizations for Memory Hierarchy**

- Write code that has locality
  - Spatial: access data contiguously
  - Temporal: make sure access to the same data is not too far apart in time
- How to achieve?
  - Proper choice of algorithm
  - Loop transformations

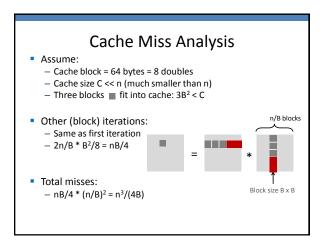








### Cache Miss Analysis Assume: - Cache block = 64 bytes = 8 doubles Cache size C << n (much smaller than n)</li> First (block) iteration: n/B blocks - B2/8 misses for each block $-2n/B * B^2/8 = nB/4$ (omitting matrix c) - Afterwards in cache Block size B x B (schematic) = \*



### Summary

No blocking: (9/8)\* n³
 Blocking: 1/(4B)\* n³

• If B = 8 difference is 4 \* 8 \* 9 / 8 = 36x

If B = 16 difference is 4 \* 16 \* 9 / 8 = 72x

Suggests largest possible block size B, but limit 3B<sup>2</sup> < C!</li>

Reason for dramatic difference:

Matrix multiplication has inherent temporal locality:

• Input data:  $3n^2$ , computation  $2n^3$ 

• Every array element used O(n) times!

 $-\,$  But program has to be written properly

# Cache-Friendly Code

- Programmer can optimize for cache performance
  - How data structures are organized
  - How data are accessed
    - Nested loop structure
    - Blocking is a general technique
- All systems favor "cache-friendly code"
  - Getting absolute optimum performance is very platform specific
    - · Cache sizes, line sizes, associativities, etc.
  - Can get most of the advantage with generic code
    - Keep working set reasonably small (temporal locality)
    - Use small strides (spatial locality)
    - Focus on inner loop code

#### **Practice Problems**

Read CSaPP Sec. 6.4 and try 6.18 and 6.19