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Design Overview

TBD

Disclaimer:

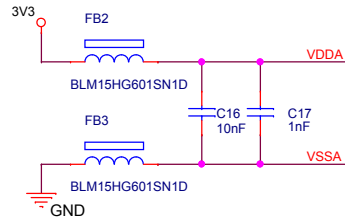
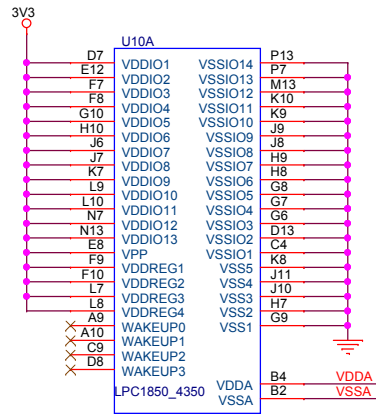
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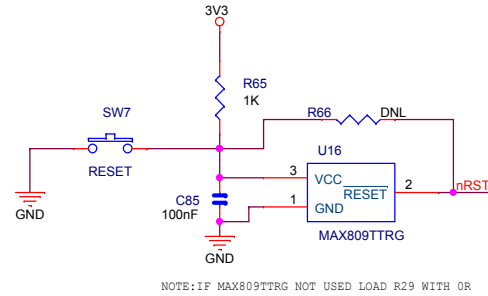
REV 1.1

- 1) Global Interrupt for Joystick and Accelerometer (G_INT, GPIO PD_7 pin T6)
- 2) LCD Header changed (I2C1 Lines have been brought to header for Touch screen)
- 3) SWD/JTAG symbol changed
- 4) SD/MMC Card Error solved

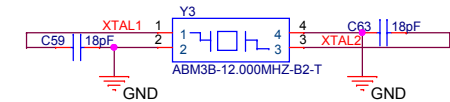
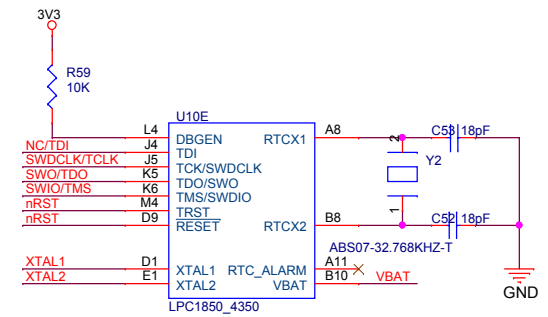
LPC4350 POWER



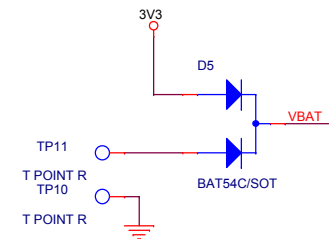
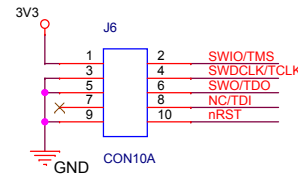
RESET SWITCH



LPC4350 CRYSTAL, JTAG, RTC

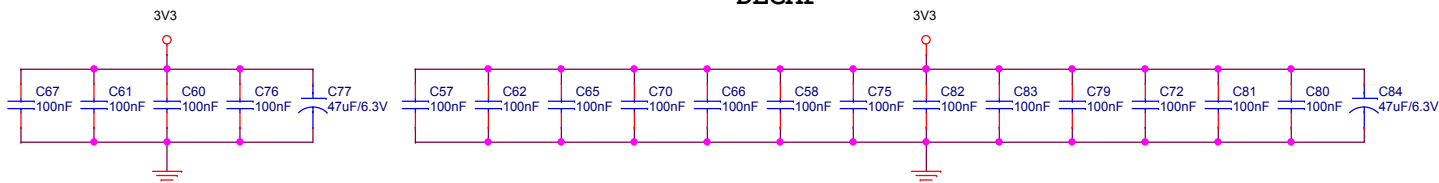


JTAG



NOTE: Place DECAP close to processor 3V3 pins.

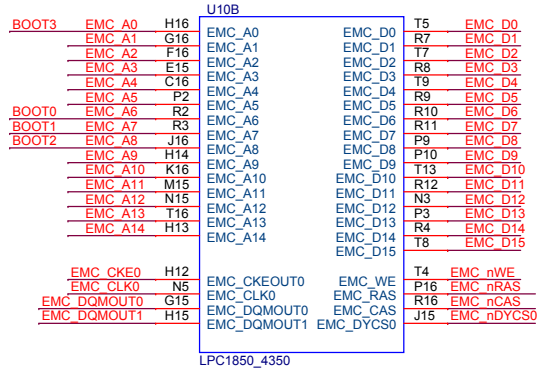
DECAP



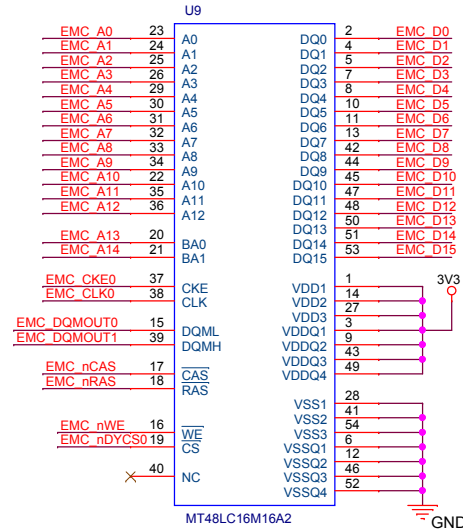
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MEMORY

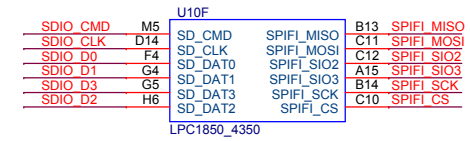
LPC4350 RAM



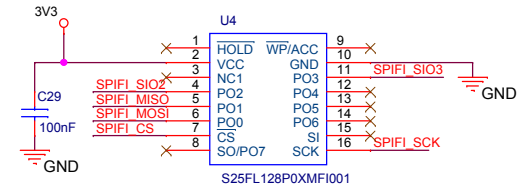
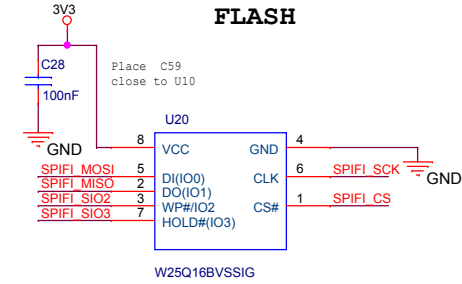
SDRAM



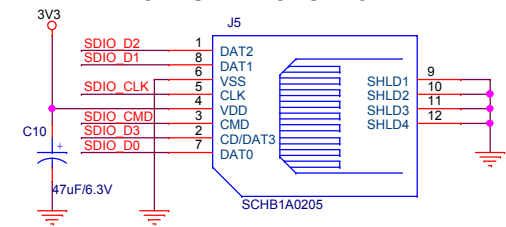
LPC4350 FLASH



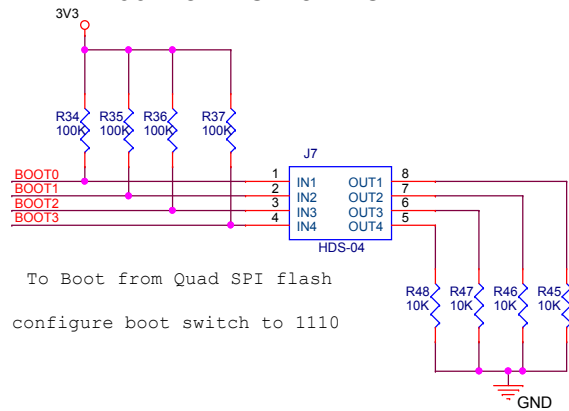
FLASH



SD CARD SECTION



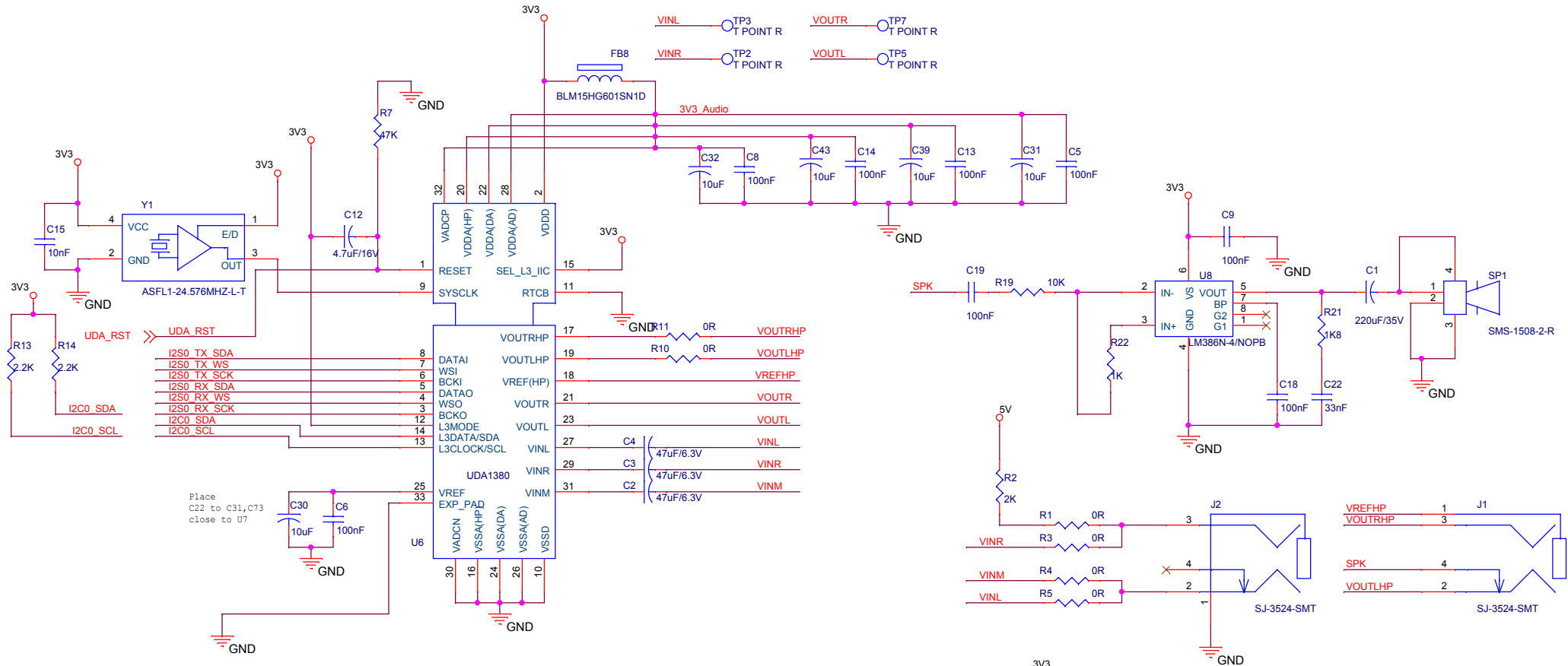
BOOT SELECT SWITCH



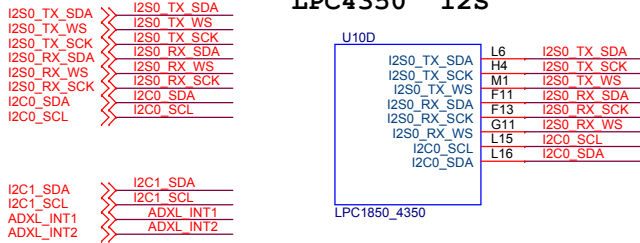
To Boot from Quad SPI flash
configure boot switch to 1110

NOTE: Place DECAP close to SRAM 3V3 pins.

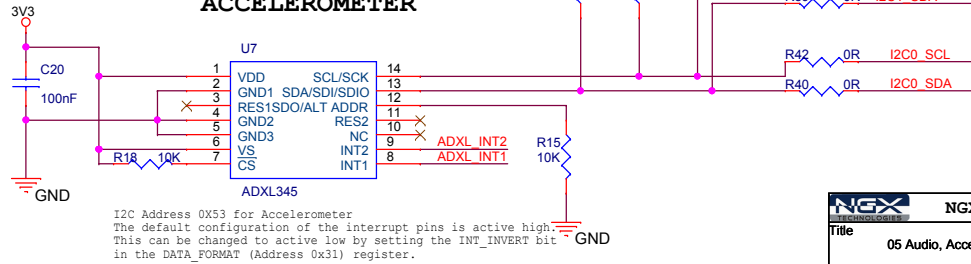
UDA CODEC




LPC4350 I2S

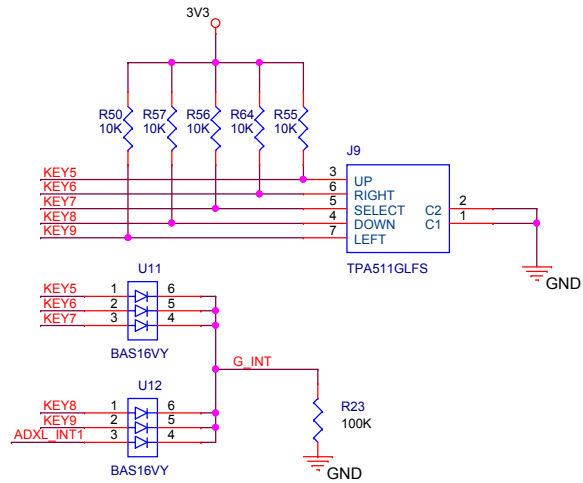


ACCELEROMETER

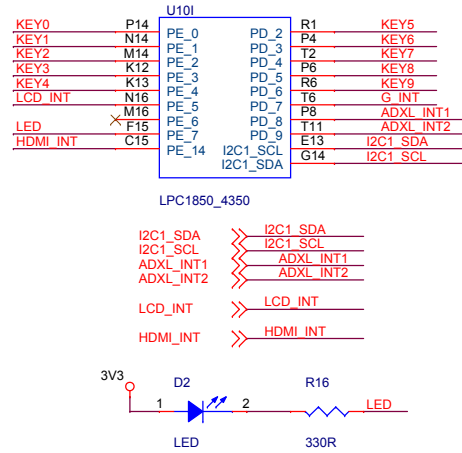


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05 Audio, Accelerometer.			
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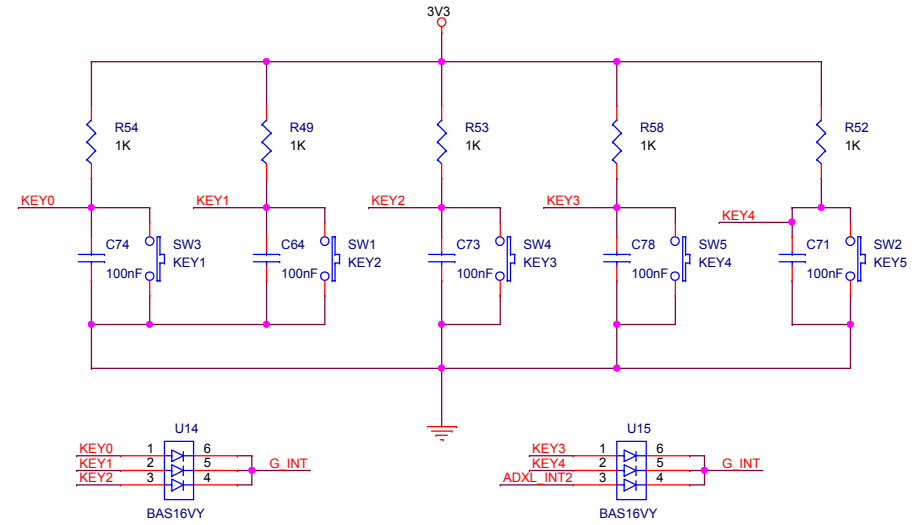
JOYSTICK



LPC4350 GPIO

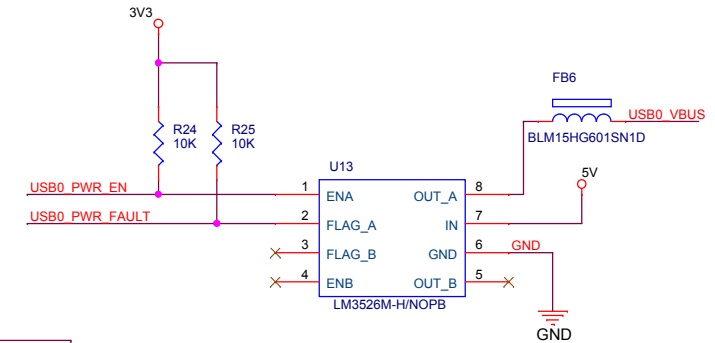
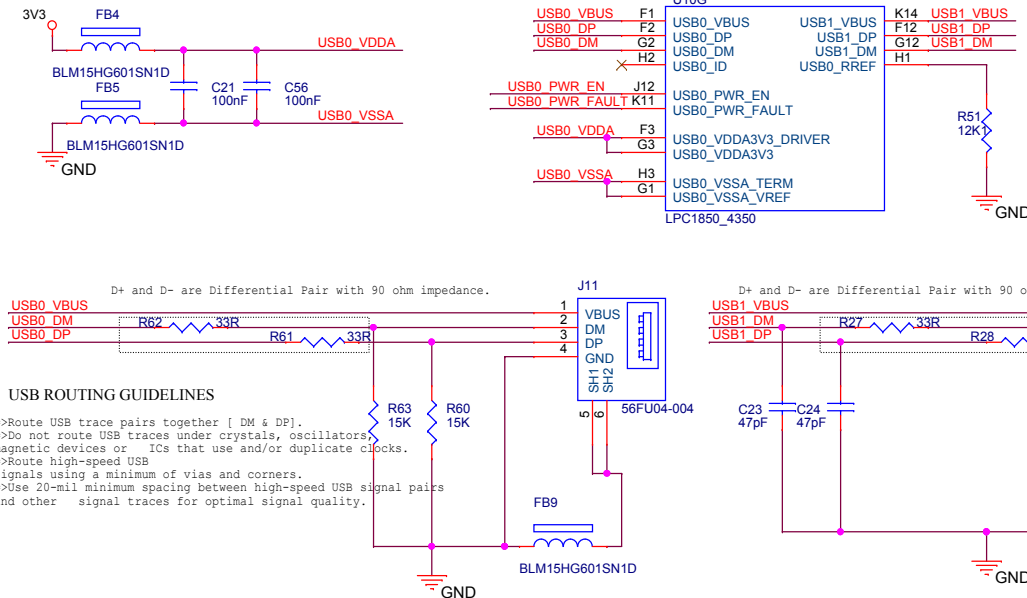


USER KEY

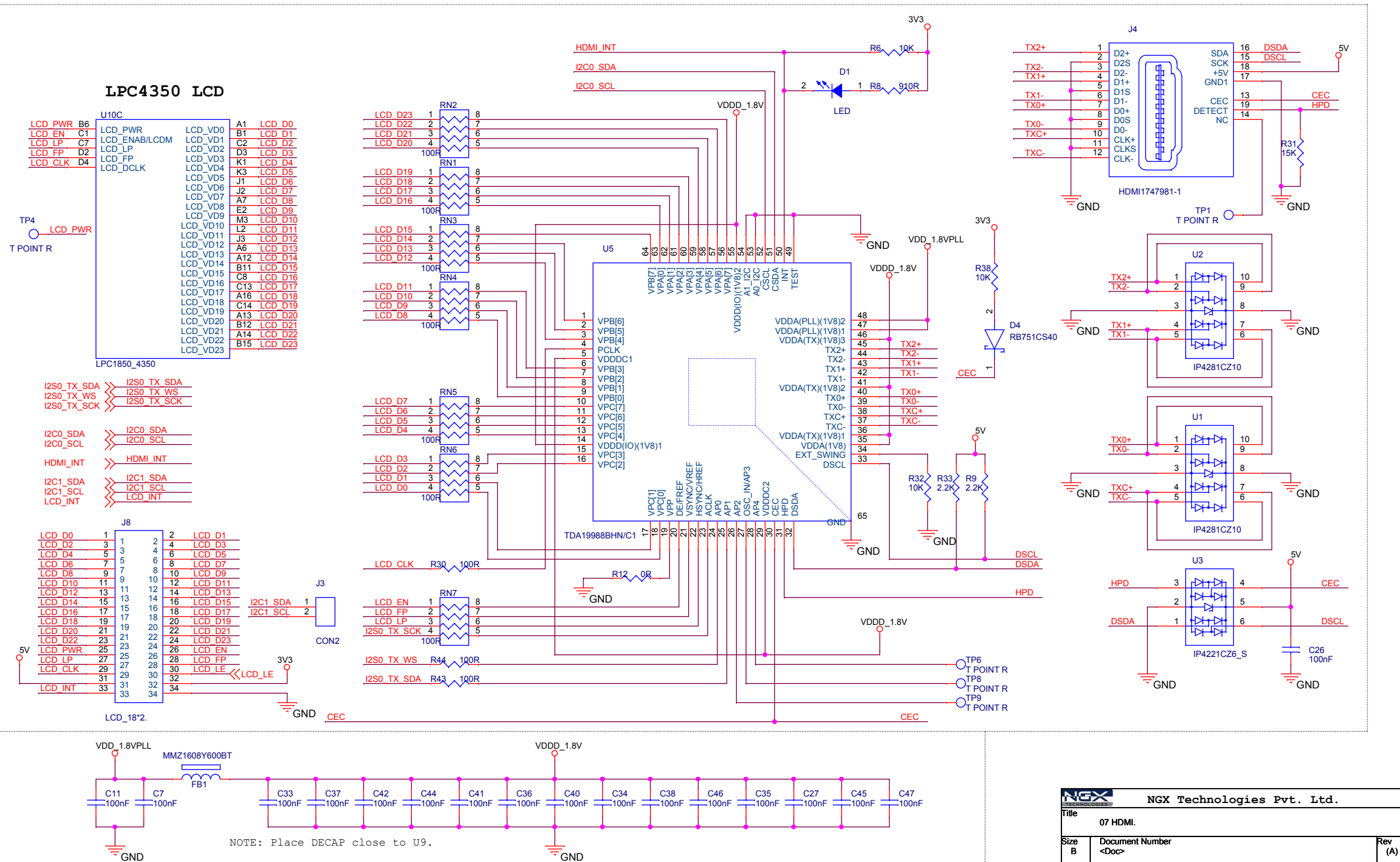


USB SECTION

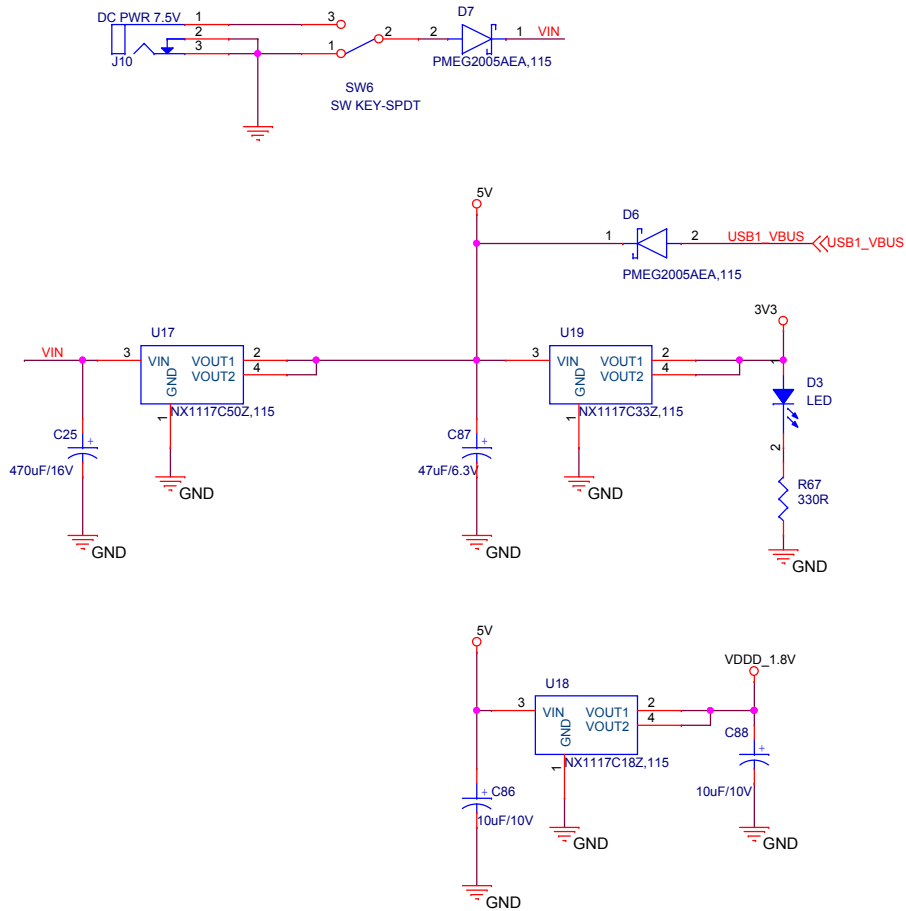
LPC4350 USB



HDMI



POWER



UNUSED PINS OF LPC4350

U10H		
L11	P9_5	M9
L12	P9_4	M6
J14	PA_0	N8
K15	PA_1	N6
H11	PA_2	P9_1
G13	PA_3	P9_0
E4	PA_4	P8_8
L5	PC_1	P8_2
N1	PC_11	P8_1
F6	PC_14	P8_0
F5	PC_2	P7_0
N4	PC_3	P6_7
K2	PC_8	P6_6
N2	PC_9	P6_3
P1	PD_0	P6_2
P11	PD_1	P6_1
N9	PD_10	P6_0
N11	PD_11	P4_0
T14	PD_12	P1_5
R13	PD_13	P1_4
T15	PD_14	P1_3
R14	PD_15	P1_20
E14	PD_16	P1_19
D16	PE_10	P1_18
D15	PE_11	P1_17
F14	PE_12	P1_16
E16	PE_8	P1_15
D12	PE_9	P0_1
E11	PF_0	P0_0
A3	PF_1	N.C.
A2	PF_10	CLK3
D11	PF_11	CLK1
E10	PF_2	ADC7
D10	PF_3	ADC6
E9	PF_4	ADC5
E7	PF_5	ADC4
B7	PF_6	ADC3
E6	PF_7	ADC2
D6	PF_8	ADC1
	PF_9	ADC0

LPC1850_4350



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08 Power 3.3V, 1V5, 5V.

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