Kevin Lee

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Full-stack engineer specializing in distributed systems, observability, and large-scale cloud platforms.

EXPERIENCE

JP Morgan & Chase Co.

Software Engineer III - Payments Observability | Austin, TX *Dec 2024 - Present*

- Building a greenfield observability platform (Datadog-like) using Java, React, and OpenTelemetry, providing end-to-end visibility across distributed systems.
- Prototyped telemetry pipelines and dashboards with OpenTelemetry, Jaeger, and Grafana to establish the platform's core capabilities.
- Defined scalable, modular architecture in collaboration with platform architects, improving system maintainability and velocity.
- Shaped product strategy by translating technical constraints into roadmap priorities and working closely with stakeholders.
- Unified 10+ internal/external integrations into a cohesive platform feature set, accelerating partner onboarding and data visibility.
- Engineered an atomic, multi-service deployment strategy using Spinnaker, enabling rollbacks and safer releases.
- Supported team growth by onboarding and mentoring junior engineers and participating in technical interviews.

Software Engineer I/II - Transact | Houston, TX *Dec 2021 - Dec 2024*

- Architected and developed scalable Kotlin + Spring Boot microservices, leveraging Reactor and coroutines.
- Integrated with third-party teams using IBM MQ and implemented gRPC for internal service communication, reducing latency by 40%.
- Reduced major incidents through a flexible Jenkins-based CI/CD framework and proactive deployment strategies.
- Delivered critical features for a major client migration securing 80%+ of platform revenue, ahead of schedule.
- Cut server costs by 20% by developing a custom Kubernetes scheduler to optimize compute resource utilization.
- Led a hackathon-winning project—a Kubernetes release verification tool built with Python and React—streamlining agile deployments.
- Improved test reliability via Cucumber-based BDD tests; resolved complex production issues through cross-team collaboration and real-time monitoring.

University of Texas at Austin

Software Developer Intern *Jun 2020 – Aug 2020*

- Developed Verilog for a PWM generator, emulating Texas Instruments' ePWM functionality for use in an FPGA-based control platform.
- Partnered with graduate researchers to validate designs using MATLAB HDL Coder, improving simulation fidelity and hardware verification.

SKILLS

Languages: Kotlin, Java, Python, Javascript, SQL, HTML, Terraform
Frameworks: Spring Boot, Spring WebFlux, React, gRPC
DevOps & Infra: Docker, Kubernetes, Spinnaker, AWS (ECS), Jenkins
Observability: OpenTelemetry, Grafana, Jaeger, Datadog
Messaging & Data: Kafka, IBM MQ, Oracle DB, Postgres

EDUCATION

University of Texas at Austin

BS, Electrical and Computer Engineering *Aug 2021*

Relevant coursework: Algorithms, Discrete Math, Statistics, Network Protocols, Software Analysis and Testing