

# KEVIN YING

9905 Edward Avenue  
Bethesda, MD 20814  
Cell: (240) 812-9538

110 Dryden Road Unit 1D  
Ithaca, NY 14850  
Email: kzy2@cornell.edu

## EDUCATION

**Cornell University**, College of Engineering, Ithaca, NY  
Bachelor of Science, Electrical and Computer Engineering  
Dyson Business Minor for Engineers  
GPA: 4.14/4.30

**Expected May 2020**

*Relevant Courses:* Digital Systems Design Using Microcontrollers, Computer Architecture, Embedded Systems, Introduction to Analysis of Algorithms, Object Oriented Programming and Data Structures

## ENGINEERING EXPERIENCE

**Software Engineering Co-op**, Alarm.com, *McLean, VA* **Summer 2018, Spring 2019**

- Implement a new feature for home light automation, construct rules using proprietary assembly
- Develop and maintain webpages (ASP.NET) and backend services (.NET, SQL Server)
- Participate in code reviews, write and refactor automated unit tests

**Electrical Member**, Autonomous Underwater Vehicle Project Team, *Ithaca, NY* **Oct. 2016-Present**

- Design, populate and program circuit boards as part of a 14-person team to meet decreased space constraints and minimize passive power draw
- Selecting components and presenting design reviews to reduce the failure rate of ICs
- Secured 1st place win out of 44 teams at 2017 AUVSI/ONR RoboSub competition

**Software Intern**, Johns Hopkins University Applied Physics Laboratory, *Laurel, MD* **Summer 2017**

- Investigated potential of convolutional neural networks to perform image resolution alongside deblurring
- Implemented digital image processing functions in both MATLAB and Python
- Collaborated with team to evaluate data, compose reports, and present to group (~60 people)

**Research Intern**, National Institute of Standards and Technology, *Gaithersburg, MD* **Summer 2015**

- Developed LabVIEW program to automate moving individual atoms with scanning tunneling microscope
- Designed new UI; sped up process eight-fold and allowed overnight manipulations with error checking
- Independently wrote and presented project poster for senior scientists

## ON-CAMPUS INVOLVEMENT

**Teaching Assistant**, Digital Logic and Computer Organization, *Ithaca, NY* **Jan. 2018–Present**

- Organize and hold lab sessions for hands on experience with logic gates, Verilog, and FPGA
- Apply knowledge of low-level computing hardware to build FPGA single cycle microprocessor
- Evaluated homework and test performance and difficulty to ensure student understanding

**Co-President**, Cornell Institute of Electrical and Electronics Engineers, *Ithaca, NY* **Jun. 2018-Present**

- Lead team of 25, hold weekly meetings, and facilitate communication between various subteams
- Organize and host events around campus for sponsors around the Cornell ECE community
- Promote science and engineering through technical talks and social activities

**Teaching Assistant**, Discrete Structures, *Ithaca, NY* **Aug. 2017–Dec. 2017**

- Apply knowledge of number theory, graph theory, and probability theory
- Assist students with proof writing and formalizing abstract ideas in small and large group settings
- Grade approximately 30 assignments weekly in addition to three sets of exams per semester

## SPECIALIZED SKILLS

**Programming Languages:** C#, Java, MATLAB, Python, LabVIEW, Verilog, some SQL Server/jQuery

**Electrical Skills:** KiCAD Eeschema/Pcbnew, soldering, microcontroller/SPI programming (C), oscilloscope, Git