VLSI REPORT : HUMAN DETECTION BY HISTOGRAM OF ORIENTED GRADIENTS

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1. Introduction

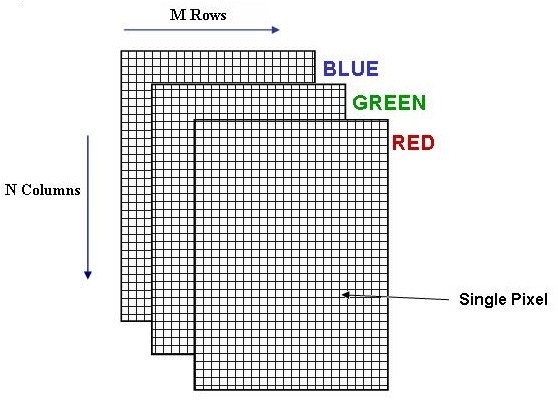
Include the human detection as one of image recognition technology. Human detection is a technique used in the digital camera, the automobile automatic brake system and monitoring system.  Therefore, this exercise is aimed at performing a hardware design of the operation unit as a theme of "human detection by Histogram of Oriented Gradients", conscious operations and cost calculation algorithm.

1. Simulation Result and Analysis
   1. Greyscaling

Human detection on colored images is difficult, so the input image is converted into a greyscale image. A color image has three matrices, consists of R, G, and B matrix, so we must combine each of those matrices into a single matrix by using the following equation.

Y = 0.298912R + 0.586611G + 0.114478B

With that equation, we get a greyscale matrix that will be used as an input to the human detection system.



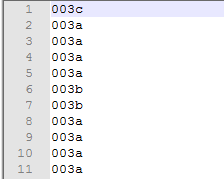
Representation of matrices from a color image

http://archive.cnx.org/resources/089a36ecd6aa8fe1f70b489a6cb1d44950cb5802/graphics1.jpg

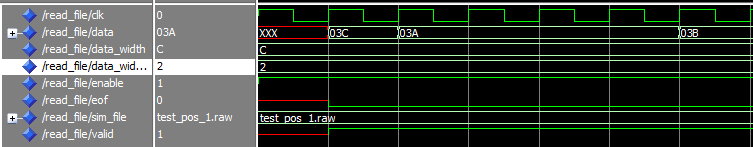
The implementation from this greyscaling process is to change all the three matrices into a 24-bit signal. The R matrix will be entered into the input signal from the first bit to the eighth bit. The G matrix will be entered into the ninth bit until to the 16th bit, and the rest were filled by the B matrix. We’re using VHDL library called ieee.std\_logic\_textio to translate a raw image that contain six hexadecimal numbers which represents those matrices. Each matrix were represented by two numbers, with the R matrix in the first two numbers, and then G and B respectively.



A sample of greyscaled image



Sample of raw image represented in hex numbers.



Signal created from reading raw file in modelsim.

* 1. Resize

Before we scan the image to find objects, the size of image must be recalculated and resized to smaller size so we can detect human in the image faster. First, what we are going to do is to define how many times the image needed to be scaled. We compare the image to a 134 x 70 pixels image and then we can determine the scale from it up to 1.25n.

After we get the scale number, we will iterate the process of human detection up to n times. With each of iteration, the scaled image will have it’s matrix rescaled too. The new matrix’s index number are multiplier of the scaler itself.

The implementation of resizing the input image is similar with MATLAB algorithm reference that given in this task, with some change such as in the Scaler module, instead using bilinear interpolation , we use iteration of scaling loop.

|  |
| --- |
| function pict = Scaler(img, scale)  [rows, cols ] = size(img);  row1 = floor(rows/scale);  col1 = floor(cols/scale);  pict= zeros(row1,col1,'uint8');    for i=1 : row1  for j=1 : col1  a = floor(scale\*i);  b = floor(scale\*j);  pict(i,j) = img(a,b);  end  end    end |

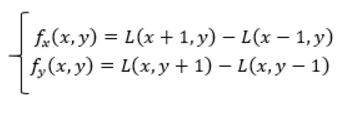
Scaler function that were made with MATLAB

* 1. Scanning & Get Histogram

In this section, the input image is scanned to get the gradien from every pixel of the image. The input image will be checked by using a 128 x 64 pixels of virtual rectangle so the scanning process can be emphasized into smaller area and we obtain each gradien faster.

The rectangle is dragged from top left of the image to the right, then followed by next rectangle below and then finally stop at bottom right of the image. We will calculate the gradien in every cell with a block size as an increment inside those rectangles. The conclusion is to calculate the gradien inside a rectangle with a block of 16 x 16 pixels that dragged with an increment of a cell or 8 pixels across the rectangle, the same way as the rectangle does to the image.

While doing scan, we will collect data of the gradien by substracting every matrix of cell with following expression:

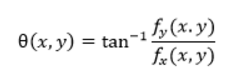


Equation used to calculate gradien

After we get *fx* and *fy*, we can calculate the magnitude an angle of gradien by using following expression:

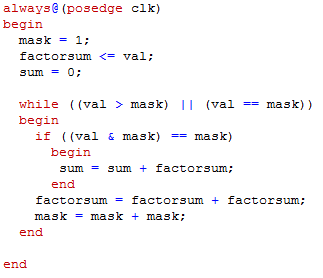


Equation used to calculate magnitude of gradien

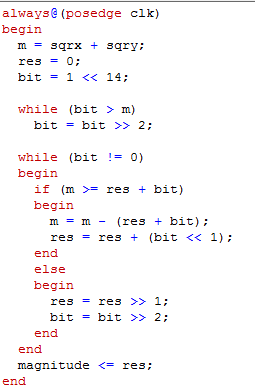


Equation used to calculate angle of gradien

But in RTL level, we cannot design a module using multiplier, root and divider easily. So, we will made an approximation to get these values of magnitudes and angles.

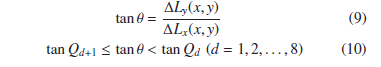


Verilog Algorithm to approximate a multiplier operation

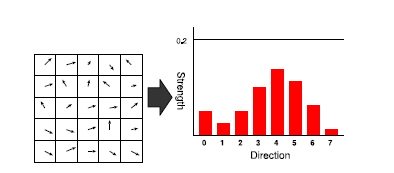


Verilog algorithm to approximate a root operation

On the other hand, the tangent values of the quantization threshold angles Qd(d = 1, 2, . . . , 8), which are described in the next section, can be precomputed since they are constant values. Therefore, we get a quantized gradient orientation by the conditional expression with tan Qd and tan Qd+1 as shown in the equations below.



Then the gradient strengths m are voted according to the corresponding quantized orientation label for every luminance value in a cell to make the histograms, as shown in the figure below.



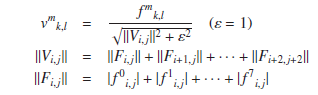
Since we quantize the gradient orientation into eight labels, 8-dimension feature vectors are eventually generated. For a (w × h) input image, a total of w/5 × h/5 histograms are obtained since each cell consists of 5×5 luminance values.

Since quantization has been made in the calculation step of a gradient orientation θd as mentioned above, any additional quantization process is not required when histograms are made. We have 320×240 values of luminance gradient, which gives us a cell array with the width of 320/5 = 64 cells and

the height of 240/5 = 48 cells. Since the maximum value of the gradient strength Fi, j is 361×25 = 9,025 for a histogram of a single cell, data size for one cell becomes 14 bits ×8. Therefore, total histograms of luminance gradient for the entire single image are expressed with (14×8)×(64×48) = 344,064 bits.

* 1. Normalization

The normalization process described in the equation below needs calculation of a square root and division, making compact FPGA implementation difficult.



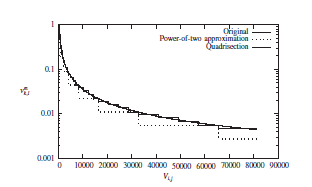
Therefore, we take an approximation approach which expands the method proposed in [4].If the denominator of the equation above (Sqr(||Vi, j||2 + ε2)) is approximated by 2α as 2α−1 <Sqr(||Vi, j||2 + ε2) ≤ 2α, the division for the normalization can be replaced by a shift operation. However, naive approximation to the nearest power-of-two value increases the normalization error. To mitigate the approximation error, we divided the interval between 2α−1 and 2α into four sub-intervals.

The range of 2α−1 to 2α can be divided into four intervals; (2α−1, 2α−1 + 2α−14 ], (2α−1 + (2^α−1)/4 , 2α−1 + 2 × 2^(α−1)/4 ], (2α−1 + 2 ×2^(α−1)/4 , 2α−1 + 3 × 2^(α−1)/4 ], and (2α−1 + 3 × 2^(α−1)/4 , 2α]. Here, ε = 1 and this is significantly smaller than ||Vi, j||2. Therefore, the conditional statements in the normalization equation can be derived when we think:

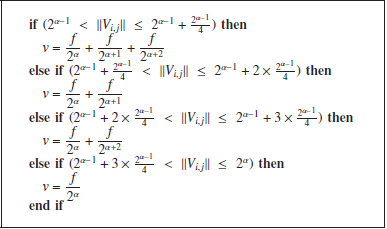
Sqr( ||Vi, j||^2 + ε^2) =||Vi, j||.

The figure below shows comparison results of approximation errors for our quadrisection approach and the naive power-of-two approach, in the case of f m k,l = 361. The results show that the normalization errors are effectively reduced with the relatively

simple calculation process.



Since the maximum value of ||Vi, j|| is 81,225, the maximum number of shift operations for the division is 19. Thus, as a fraction part, 19 bits of 0s are appended to the LSB side of f m k,l in advance of shifting, and obtained vm k,l is also expressed with a fixed point arithmetic number with 14 + 19 = 33 bits.



* 1. Support Vector MachiNe

We can use a support vector machine (SVM) when your data has exactly two classes. An SVM classifies data by finding the best hyperplane that separates all data points of one class from those of the other class. The *best* hyperplane for an SVM means the one with the largest *margin* between the two classes. Margin means the maximal width of the slab parallel to the hyperplane that has no interior data points.

The *support vectors* are the data points that are closest to the separating hyperplane; these points are on the boundary of the slab. The following figure illustrates these definitions, with + indicating data points of type 1, and – indicating data points of type –1.

The data for training is a set of points (vectors) *xj* along with their categories *yj*. For some dimension *d*, the *xj* ∊ *Rd*, and the *yj* = ±1. The equation of a hyperplane is

f(x)=x′*β*+b=0

where *β* ∊ *Rd* and *b* is a real number.

The following problem defines the *best* separating hyperplane (i.e., the decision boundary). Find *β* and *b* that minimize ||*β*|| such that for all data points (*xj*,*yj*),

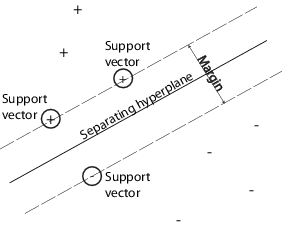
yjf(xj)≥1.

The support vectors are the *xj* on the boundary, those for which yjf(xj)=1.

For mathematical convenience, the problem is usually given as the equivalent problem of minimizing ←*β*←. This is a quadratic programming problem. The optimal solution (ˆ*β*,ˆb) enables classification of a vector *z* as follows:

class(z)=sign(z′ˆ*β*+ˆb)=sign(ˆf(z)).

ˆf(z) is the *classification score* and represents the distance *z* is from the decision boundary.



As the SVM from the LSI-Contest website still has unpredictable flaws, we decided to train our own SVM classifier using two different datasets, namely MIT pedestrian database and INRIA database. We used Matlab’s built-in SVM classifier and constructed a non-linear SVM classifier using a method known as kernel matching. The result of the training process is a new weighing kernel and a new bias kernel to be used as the primary means of human detection.

1. Conclusion

The conclusions in this report are:

* We use gradien to compute histogram so we can conclude if there is an object in an image by using SVM.
* We’re succeed to approximate multiplier, divider and root operation by using only shifter and iteration.

References

1. <http://www.lsi-contest.com/shiyou_1e.html>, accessed in 27 December 2016
2. <https://www.mathworks.com/help/fixedpoint/ug/convert-cartesian-to-polar-using-cordic-vectoring-kernel.html>, accessed in 27 December 2016Adiono, Trio. *Perancangan Sistem VLSI,* Pusat Mikroelektronika ITB, Bandung, 2012.
3. Ciletti, M.D. *Advanced Digital Design With the Verilog HDL,* Publishing House of Electonics Industry, Beijing, 2010.