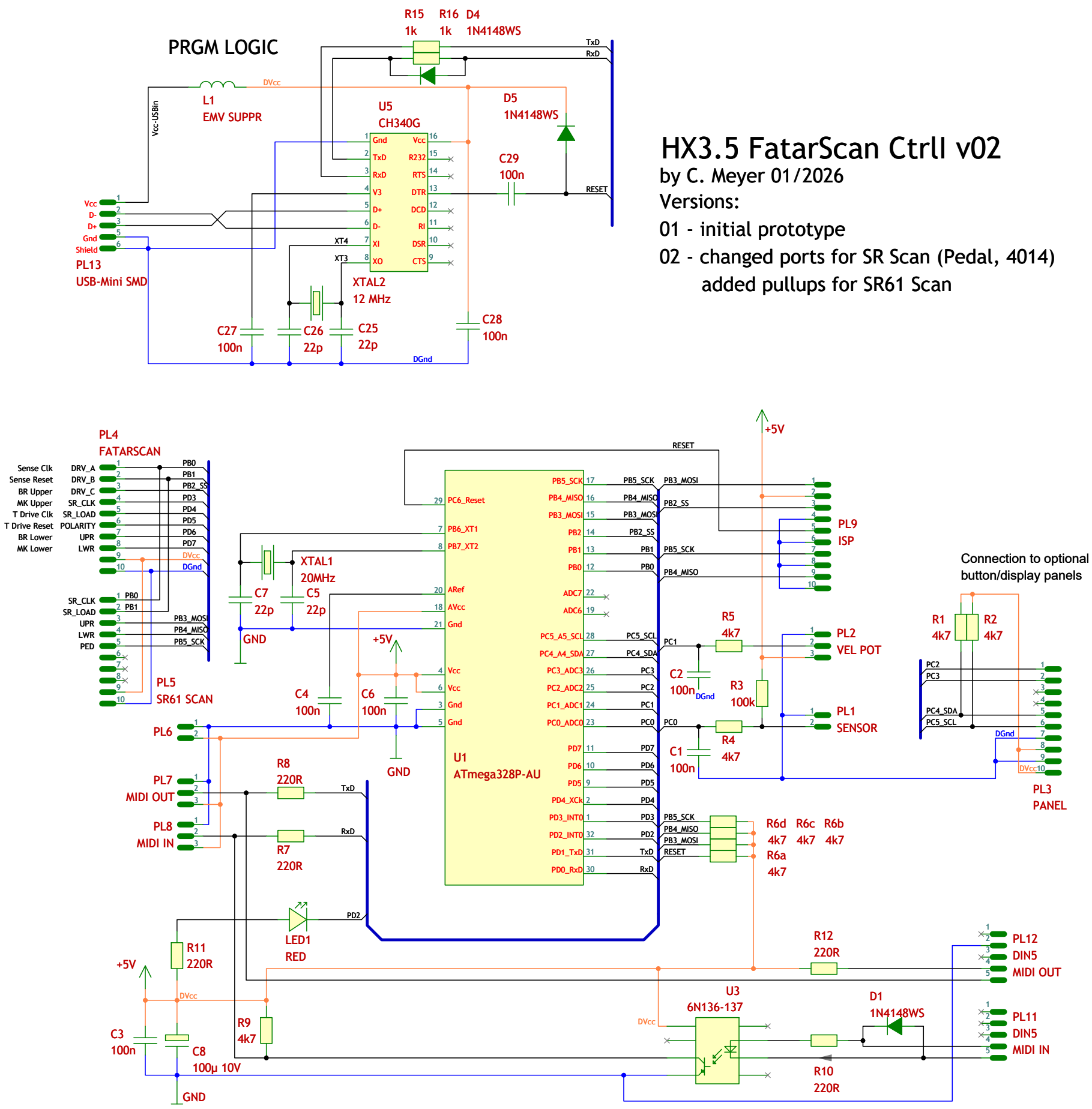
PRGM LOGIC
R15 1k
R16 1k
D4 1N4148WS
TxD
RxD
DVcc
L1
EMV SUPPR
Vcc-USBin
U5
CH340G
D5
1N4148WS
C29
100n
RESET
Vcc
D-
D+
Gnd
Shield
PL13
USB-Mini SMD
XT4
XT3
XTAL2
12 MHz
C27
100n
C26
22p
C25
22p
C28
100n
DGnd
HX3.5 FatarScan Ctrll v02
by C. Meyer 01/2026
Versions:
01 - initial prototype
02 - changed ports for SR Scan (Pedal, 4014)
added pullups for SR61 Scan
PL4
FATARSCAN
Sense Clk
Sense Reset
BR Upper
MK Upper
T Drive Clk
T Drive Reset
BR Lower
MK Lower
DRV_A
DRV_B
DRV_C
SR_CLK
SR_LOAD
POLARITY
UPR
LWR
PB0
PB1
PB2_SS
PD3
PD4
PD5
PD6
PD7
DVcc
DGnd
PB3_MOSI
PB4_MISO
PB5_SCK
PED
PL5
SR61 SCAN
PL6
PL7
MIDI OUT
PL8
MIDI IN
XTAL1
20MHz
C7
22p
C5
22p
GND
C4
100n
C6
100n
+5V
U1
ATmega328P-AU
RESET
PC6_Reset
PB6_XT1
PB7_XT2
AREF
AVcc
Gnd
Vcc
PC5_A5_SCL
PC4_A4_SDA
PC3_ADC3
PC2_ADC2
PC1_ADC1
PC0_ADC0
PD7
PD6
PD5
PD4_XCK
PD3_INT0
PD2_INT0
PD1_TxD
PD0_RxD
PC5_SCL
PC4_SDA
PC1
PC3
PC2
PC0
R5
4k7
C2
100n
R3
100k
R4
4k7
C1
100n
PL9
ISP
PL2
VEL POT
PL1
SENSOR
Connection to optional
button/display panels
R1
4k7
R2
4k7
PL3
PANEL
R8
220R
R7
220R
LED1
RED
R6d
4k7
R6c
4k7
R6b
4k7
R6a
4k7
R12
220R
PL12
DIN5
MIDI OUT
R11
220R
R9
4k7
C3
100n
C8
100µ 10V
U3
6N136-137
D1
1N4148WS
R10
220R
PL11
DIN5
MIDI IN