Facilitating Kernel UAF Vulnerability Exploitation: an idea

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Overview

UAF vulnerability pattern:

UAF chunk size

certain bytes readable

certain bytes writable

Overview

Given an UAF vul pattern, I want to:

- identify candidate objects that can be sprayed to the UAF region
- figure out syscalls with proper arguments to perform the spray
- find proper approach to exploit

UAF vulnerability pattern:

UAF size: one kmalloc-32 chunk

readable bytes: none

writable bytes: the first 8 bytes

exploitation: *ldt_struct*

```
struct ldt_struct {
         * Xen requires page-aligned LDTs with special permissions. This is
         * needed to prevent us from installing evil descriptors such as
        * call gates. On native, we could merge the ldt_struct and LDT
         * allocations, but it's not worth trying to optimize.
       struct desc_struct
                                *entries:
       unsigned int
                               nr_entries;
         * If PTI is in use, then the entries array is not mapped while we're
        * in user mode. The whole array will be aliased at the addressed
         * given by ldt_slot_va(slot). We use two slots so that we can allocate
         * and map, and enable a new LDT without invalidating the mapping
         * of an older. still-in-use LDT.
         * slot will be -1 if this LDT doesn't have an alias mapping.
         */
       int
                                slot:
};
```

```
SYSCALL_DEFINE3(modify_ldt, int , func , void __user * , ptr ,
                unsigned long , bytecount)
        int ret = -ENOSYS:
        switch (func) {
        case 0:
                ret = read_ldt(ptr, bytecount);
                break:
        case 1:
                ret = write_ldt(ptr, bytecount, 1);
                break:
        case 2:
                ret = read_default_ldt(ptr, bytecount);
                break:
        case 0x11:
                ret = write_ldt(ptr, bytecount, 0);
                break:
         * The SYSCALL_DEFINE() macros give us an 'unsigned long'
         * return type, but tht ABI for sys_modify_ldt() expects
         * 'int'. This cast gives us an int-sized value in %rax
         * for the return code. The 'unsigned' is necessary so
         * the compiler does not try to sign-extend the negative
         * return codes into the high half of the register when
         * taking the value from int->long.
         */
        return (unsigned int)ret;
```

```
static int read_ldt(void __user *ptr, unsigned long bytecount)
        struct mm_struct *mm = current->mm;
        unsigned long entries_size;
        int retval;
        if (copy_to_user(ptr, mm->context.ldt->entries, entries_size)) {
                retval = -EFAULT;
                goto out_unlock;
```

two solves during the contest, both unintended:

- @Balsn gave a 1-day exp, unrelated to the vulnerability at all
- *@Organizor* gave a kernel ROP solve, too delicate and complicated to reproduce

conclusion: Identifying proper structs is vital for UAF exploitation



Q1: where do these structs come from?

UAF vulnerability pattern:

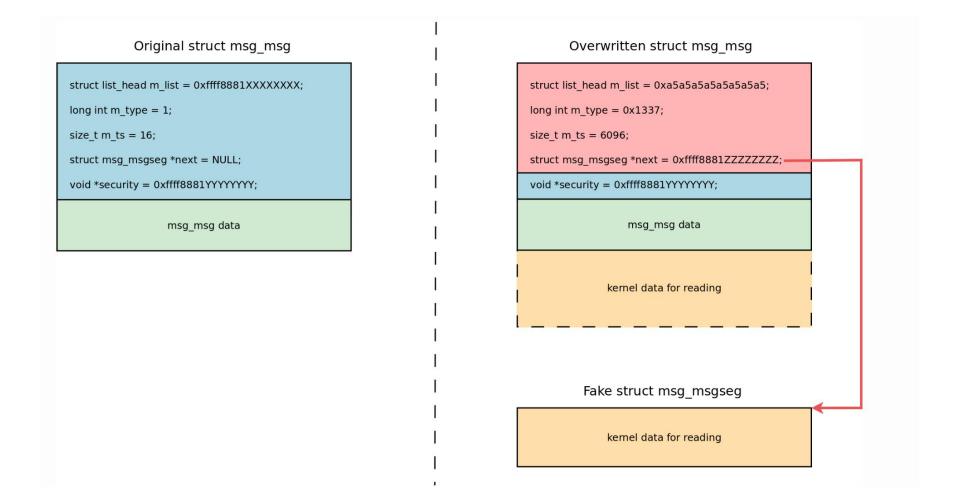
- UAF size: one kmalloc-64 chunk
- readable bytes: none
- writable bytes: 4 bytes at offset 40

the pointer *security* is at offset 40, and it will be passed to kfree() resulting to arbitrary free, and can be transformed into UAF

New UAF vulnerability pattern:

- UAF size: one kmalloc-64 chunk
- readable bytes: none
- writable bytes: the first 40 bytes

arbitrary read:



execution path around msg_msg struct can be extended, achieving both arbitrary read and write (corCTF 2021 Finals - Fire of Salvation)

Q2: finding such execution path needs extensive expertise in kernel and intensive manual efforts. Can it be automatized?

UAF vulnerability pattern:

- UAF size: one kmalloc-0x400 chunk
- readable bytes: the first 0x60 bytes
- writable bytes: the first 0x60 bytes

a fancy function, available in kernels who enable multi-cpu support:

```
static void work_for_cpu_fn(struct work_struct *work)
{
    struct work_for_cpu *wfc = container_of(work, struct work_for_cpu, work);

    wfc->ret = wfc->fn(wfc->arg);
}
```

after compilation:

```
static void work_for_cpu_fn(size_t * args)
{
    args[6] = ((size_t (*) (size_t)) (args[4](args[5]));
}
```

perfect for us to execute commit_creds(prepare_kernel_cred(0))

- hijack ioctl() of a tty struct to work_for_cpu_fn()
- set tty_struct[4]=prepare_kernel_cred, tty_struct[5]=0
- invoke ioctl() and the new generated cred struct will be placed at tty_struct[6], and we can read it
- do the trick again to invoke commit_creds()

conclusion: execution path of exploit is not limited to syscall sequences, sometimes can be delicate.

Q3: Is it possible to identify more such fancy functions?

Possible Solution

Q1: where do these structs come from?

- manually create an UAF chunk
- trace kmalloc()
- fuzz the kernel until the UAF chunk is taken

Possible Solution

Q2: finding such execution path needs extensive expertise in kernel and intensive manual efforts. Can it be automatized?

one possible solution...

if we can identify all control flow related to the struct, we can symbolicly execute them until we meet instructions like

- call/jmp SYMBOLIC_VALUE ----- control flow hijack
- mov [SYMBOLIC_VALUE], rsi ----- arbitrary read
- •

Possible Solution

Q3: Is it possible to identify more such fancy functions?

intuitive rules

Thanks!