**CS161L - Winter 2018**

**Design and Architecture of Computer Systems Lab**



**Textbook**: John L. Hennessy and David A. Patterson, "Computer

Organization and Design, The Hardware/Software Interface," 5th

Edition. Morgan Kaufmann Publishers, 2013.**" (Zybook for 161)**

* **Prerequisite:** EE**/**CS120A
* **Class website:** <http://ilearn.ucr.edu> check it often!!
* **Grading**: 85% lab assignments, 15% in class quizzes. Quizzes may or may not be announced ahead of time and will cover material discussed ***in class***.

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| **Instructor** | **TA** |
| Skyler Windh  Winston Chung Hall,  Room 464 windhs@cs.ucr.edu  Office Hours: by appointment. | Jose Rodriguez  Winston Chung Hall,  Room 464  [jrodr050@ucr.edu](mailto:jrodr050@ucr.edu)  Office Hour: TBA or by appointment |

* This Lab course complements CS 161 by working on hands-on implementations of some of the concepts covered in the lecture course.
* The CS 161L also cover material on computer arithmetic that is not covered in CS 161 (Chapter 3 in the textbook). Most of it will be a review from EE/CS 120A, but new arithmetic concepts are also introduced and analyzed such as binary coded decimal arithmetic, fixed point representations etc.
* Other topics covered in the lectures and lab of CS 161L include:
  + Content-Addressable Memories (CAM) and Ternary CAMs.

• FPGAs: their architectures and utilizations.