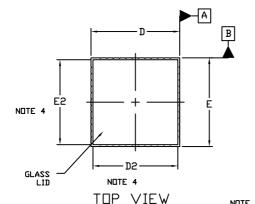
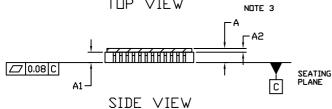
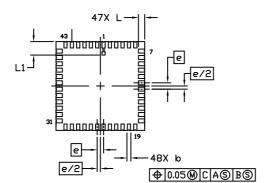
LCC48, 14.22x14.22

CASE 115AO ISSUE A

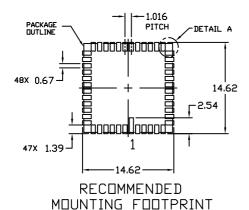
DATE 31 JAN 2012







BOTTOM VIEW



NOTES:

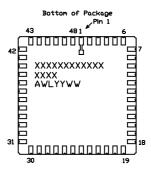
- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- 2. CONTROLLING DIMENSION: MILLIMETERS
- DIMENSION A INCLUDES THE PACKAGE BODY AND LID BUT DOES NOT INCLUDE HEATSINKS OR OTHER ATTACHED FEATURES.
- 4. THE LID DEFINED BY DIMENSIONS D2 AND E2 MUST BE LOCATED WITHIN DIMENSIONS D AND E.



DETAIL A

| | MILLIMETERS | | |
|-----|-------------|-------|--|
| DIM | MIN. | MAX. | |
| Α | 1.85 | 2.55 | |
| A1 | 1.65 REF | | |
| A2 | 0.55 REF | | |
| b | 0.43 | 0.59 | |
| D | 14.09 | 14.35 | |
| D2 | 13.60 REF | | |
| Ε | 14.09 | 14.35 | |
| E2 | 13.60 REF | | |
| е | 1.016 BSC | | |
| L | 0.84 | 1.20 | |
| L1 | 1.90 | 2.42 | |
| | | | |
| | | | |

GENERIC MARKING DIAGRAM



XXXXX = Specific Device Code A = Assembly Location

WL = Wafer Lot YY = Year WW = Work Week NNNN = Serial Number

| DOCUMENT NUMBER: | 98AON56514E |
|------------------|---------------------------|
| STATUS: | ON SEMICONDUCTOR STANDARD |
| REFERENCE: | |
| DESCRIPTION | I CC48 14 22 X 14 22 |

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PAGE 2 OF 2

| ISSUE | REVISION | DATE |
|-------|--|-------------|
| 0 | RELEASED FOR PRODUCTION FROM CYPRESS DRAWING #001-57635 TO ON SEMICONDUCTOR. REQ. BY K. BERKMAN. | 30 JUN 2011 |
| Α | CORRECTED MARKING DIAGRAM. REQ. BY S. WOUTERS. | 31 JAN 2012 |
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