

Khaled Zaki Ahmed

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ACADEMIC DEGREES

Master of Science, Electrical and Electronic Engineering, University of Bradford, United Kingdom, 1998, **specialized** in Real Time Electronics

Bachelor of Science in Physics, American University in Cairo, Egypt, 1996, **specialized** in Electronics

Highly Specialized Postgraduate Professional Degrees 2019-2021

(Master's degree level)

Digital Signal Processing Specialization (4 Courses) by École Polytechnique Fédérale de Lausanne

Digital systems from logic gates to processors Universitat Autònoma de Barcelona

FPGA design for embedded systems (3 courses) University of Colorado Boulder

Sensors and sensor Circuits University of Colorado Boulder

Linear circuits analysis AC and DC (2 Courses) Georgia Institute of Technology

Work History

Analog and mixed signal IC Designer, eFabless community-eFabless, 2017-2019

. eFabless offer chips that I design as an IP partner

Senior Software and Content Developer, Epson Omega, 2006-2013

. Develop mathematical tools for circuit analyses and design. Published in Nokia Ovi store.

Senior Lecturer in Programming, New El-Nozha Computer Center, Cairo, Egypt, 2002-2004 .

Duties included lecturing and preparing material for various programming subjects.

Lecturer, Computer science department, El-Mehana El-Shamla high Institute, Libya, 2001-2002 .

Taught computer architecture, algorithm design, and operating systems.

Achievements

Independent research in very high speed, low power analog circuits design:

The IP produced are Tested and datasheet certified and published by Efabless incorporation, San Jose, USA at <https://platform.efabless.com/profiles/2532>

Published many mathematical software tools, Nokia Ovi store, Iceland, 2007-20013

Patent No 545, Smart Village, Egypt 13/3/2008 Mathematical tool for circuit analysis and design

Patent No 546, Smart Village, Egypt 13/3/2008 Digital Signal processing Software tools

Expertise and Skills

Analog Design

Mastery level expertise in the design of all major analog building blocks (bandgaps, current sources, and amplifiers). Deep understanding of ADC and DAC architectures. Expertise was demonstrated in building various industry standard high performance operational amplifiers and comparators that are published at Efabless and are available At efabless IP marketplace.

Digital Design

Extensive hands-on experience gained in numerous post graduate professional degrees in digital circuit design from elementary gates to major building blocks in arithmetic circuits, control unit and processor design as well as full computer systems architecture implementation by building system on chip and firmware for it. Both VHDL and Verilog were extensively used as well as training in using professional state of the art EDA tools that covered the whole design flow.

Programming

Mastery level of C/C++, JAVA

Language Skills

Bilingual level of proficiency in both written and spoken Arabic and English