GTX 1050

PCIe 3rd Gen x8lane

NAND constants		Size of Matrix	Sparsity	Basic CUDA	Complete CUDA	StoRM	Speed-up factor
N	K		%	mS	mS	mS	$\frac{StoRM}{Complete}$
20	1	78332 x 78332	99.99802	647.00	438.00	2341.00	5.34
20	2	154942 x 154942	99.999001	1226.67	690.33	4283.67	6.21
20	3	231552 x 231552	99.99933	1738.67	1002.33	6605.33	6.59
20	4	308162 x 308162	99.9995	2360.00	1279.33	9031.33	7.06
40	1	1004862 x 1004862	99.99984	7389.67	3731.33	31504.67	8.44
40	2	2003082 x 2003082	99.99992	15472.33	7279.00	60795.67	8.35
40	3	3001302 x 3001302	99.99995	22687.67	11045.33	88435.00	8.01
40	4	3999522 x 3999522	99.99996	28681.67	14817.67	119887.33	8.09
60	1	4717592 x 4717592	99.99997	33870.00	17040.33	135073.33	7.93
60	2	9420422 x 9420422	99.999983	77001.33	34431.67	270160.00	7.85
60	3	14123252 x 14123252	99.999989	109827.00	50593.33	401094.33	7.93
60	4	18826082 x 18826082	99.999992	138954.67	67511.67	556970.67	8.25

Model Name	Matrix Size	Sparsity	Basic CUDA	Complete CUDA	StoRM CPU	Speed-up factor
		%	mS	mS	mS	$\frac{StoRM}{Complete}$
Herman3	8 x 8	56.25	249.7	213.3	12.0	0.06
Herman5	32 x 32	76.17	203.3	224.3	24.3	0.11
Herman7	128 x 128	86.64	207.7	237.7	82.0	0.35
Herman9	512 x 512	92.5	221.7	220.3	589.0	2.67
Herman11	2048 x 2048	95.8	504.0	413.7	5047.0	12.20
Herman13	8192 x 8192	97.62	3583.0	1116.7	17966.0	16.09
Herman15	32768 x 32768	98.66	31202.0	8197.0	152400.0	18.59