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# Kshitij Gokul Khandelwal

## Electrical and Electronics Engineer

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## EDUCATION

**AUGUST 2015 - PRESENT: Birla Institute of Technology and Science, Pilani (CGPA - 8.609/10)**

- Junior Year Undergraduate BE (Hons) student in Electrical and Electronics Engineering.

### PROJECTS:

1. Study project on **Optimization of Gain and Directivity of Microstrip Patch Antenna Arrays using Artificial Neural Networks and Bacterial Foraging Optimization** under Dr. Navneet Gupta, Assistant Professor and Head, Department of Electrical and Electronics Engineering, BITS Pilani
2. Summer research project on **designing an optimum L-Band micro-strip Low Pass Filter** under Mr. Pritam Kr. Sinha, Scientist 'C', Solid State Physics Laboratory (SSPL), Defence Research and Development Organization (DRDO), Ministry of Defence, Govt. of India.

### COURSES:

Engineering Mathematics I, II & III Probability and Statistics Optimization	Electronic Devices Microelectronic Circuits Analog and Digital VLSI Design
Electrical Sciences Electrical Machines Control Systems	Digital Design Microprocessors and Interfacing Computer Architecture
Electromagnetic Theory Signal and Systems Digital Signal Processing Communication Systems	Computer Programming Neural Networks and Fuzzy Logic
General Chemistry General Biology Thermodynamics Mech. Oscillations and Waves Engineering Graphics	Principles of Economics Symbolic Logic Public Administration Print & Audio Visual Advertising

**JULY 2013 - JUNE 2015: Shri Dawale College of Science, Akola (Class 12th - 93%)**

- Physics, Chemistry and Mathematics with Electronics as the additional subject.

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JUNE 2007 - JUNE 2013: **Mount Carmel Secondary School, Akola (Class 10th - 98%)**

## WORK EXPERIENCE

### **Solid State Physics Laboratory (SSPL) - DRDO**

- **Summer Research Intern.**

MAY 2017 - JULY 2017

- Performed a comprehensive study and simulation based experimentation in Filter Design and Transmission Line Theory for microwave applications.
- Designed an optimum L-band micro-strip low pass filter with a cutoff frequency of 2 GHz.

### **Hyperloop India**

- **Control Systems Engineer, Braking and Levitation Subsystem.**

APRIL 2017 - PRESENT

- Performed stability analysis and optimization on highly nonlinear Levitation and Braking models for the Orcapod, developed by the team.
- Hyperloop India is one of the only 24 teams in the world to test their scaled-down model of the Hyperloop Pod at the SpaceX Design Competition in Hawthorne, LA.

### **Association for Computing Machinery (ACM) Student Chapter, BITS Pilani**

- **Chair (Operations)**

AUGUST 2015 - PRESENT

- Head of all on campus and off campus membership operations of the ACM Student Chapter.
- Current winner of the ACM-India Best Student Chapter Award, for the fourth consecutive year.
- Published four editions of the BITS-ACM Newsletter, Digital Elixir.
- Designed the course for and instructed the SIGs on Linux Shell Scripting and Cryptography.

### **National Service Scheme (NSS), BITS Pilani**

- **Teaching Volunteer**

AUGUST 2015 - APRIL 2016

- Volunteered as a teacher to teach underprivileged students of ages 12 - 16.
- Volunteered for organizing a Junoon, a country-wide Sports Festival dedicated to the specially abled youth.

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## TECHNICAL SKILLS

- MATLAB
- LT Spice and Verilog HDL
- MASM and DebugX (Assembly Programming)
- Agilent ADS
- NI LabVIEW
- Python and C Programming
- Linux Shell Scripting
- AutoCAD

## AWARDS

### **National Talent Search Examination (NTSE) Scholar**

Both Class 8th (2011) and Class 10th (2013).

## NON ACADEMIC INTERESTS

Writing short stories; poetry; playing and following football; exploring and enjoying metal, rock and indie music.