Kshitij Gokul Khandelwal

Electrical and Electronics Engineer

201, Geetai Residency, Birla Colony
Akola, Maharashtra, India (444005)
(+91) 9421881230
EMail: f2015156@pilani.bits-pilani.ac.in | khandelwalkshitij@outlook.com
linkedin.com/in/khandelwalkshitij | khandelwalkshitij.github.io

EDUCATION

AUGUST 2015 - PRESENT: Birla Institute of Technology and Science, Pilani (CGPA - 8.609/10)

- Junior Year Undergraduate, BE (Hons) in Electrical and Electronics Engineering.

PROJECTS:

1. Study project on **Optimization of Gain and Directivity of Microstrip Patch Antenna Arrays using Artificial Neural Networks and Bacterial Foraging Optimization** under Dr. Navneet Gupta, Assistant Professor and Head, Department of Electrical and Electronics Engineering, BITS Pilani
2. Summer research project on **designing an optimum L-Band micro-strip Low Pass Filter** under Mr. Pritam Kr. Sinha, Scientist 'C', Solid State Physics Laboratory (SSPL), Defence Research and Development Organization (DRDO), Ministry of Defence, Govt. of India.

COURSES:

Engineering Mathematics I, II & III Probability and Statistics Optimization	Electronic Devices Microelectronic Circuits Analog and Digital VLSI Design
Electrical Sciences Electrical Machines Control Systems	Digital Design Microprocessors and Interfacing Computer Architecture
Electromagnetic Theory Signal and Systems Digital Signal Processing Communication Systems	Computer Programming Neural Networks and Fuzzy Logic
General Chemistry General Biology Thermodynamics Mech. Oscillations and Waves Engineering Graphics	Principles of Economics Symbolic Logic Public Administration Print & Audio Visual Advertising

JULY 2013 - JUNE 2015: Shri Dawale College of Science, Akola (Class 12th - 93.08%)

- Physics, Chemistry and Mathematics with Electronics as the additional subject.

JUNE 2007 - JUNE 2013: **Mount Carmel Secondary School, Akola (Class 10th - 97.82%)**

WORK EXPERIENCE

Solid State Physics Laboratory (SSPL) - DRDO: Summer Research Intern**.**

AUGUST 2015 - PRESENT

- Performed a comprehensive study and simulation based experimentation in Filter Design and Microstrip Line Theory for microwave applications.
- Designed an optimum L-band micro-strip low pass filter with a cutoff frequency of 2 GHz.

Hyperloop India: Control Systems Engineer, Braking Subsystem**.**

APRIL 2017 - PRESENT

- Performed stability analysis and designed the control algorithm for the braking modules of the OrcaPod
- Worked as a Business Development Personnel for the team.

Association for Computing Machinery (ACM) Student Chapter, BITS Pilani: Chair (Operations)

AUGUST 2015 - PRESENT

- Head of all on campus membership operations of the ACM Student Chapter.

National Service Scheme (NSS), BITS Pilani: Teaching Volunteer

AUGUST 2015 - APRIL 2016

TECHNICAL SKILLS

- MATLAB
- LT Spice and Verilog HDL
- MASM and DebugX (Assembly Programming)
- Agilent ADS
- NI LabVIEW
- Python and C Programming
- Linux Shell Scripting
- AutoCAD

AWARDS

2011, 2013: National Talent Search Examination (NTSE) Scholar