October 2015

Sunday	Monday	Tuesday	Wednesday	Thursday	Friday	Saturday
January	····o···aay	· uccuuj	11001100000	1	2	3
4	5	6	7	8	9	10
11	12	13	14	15	16	17
18	19	20	21	22	23	24
Create Proposal, 1 d Gather Requirement			Finalize Proposal, 1			Finalize Requirement
25	26	27	28	29	30	31
Finalize Require	ements, 3 days	Design Initial RF Sch	Design Initial MCU S			

Overflow Tasks

ID	Name	Start	Finish
27	Part Selection	Sun 10/18/15	Tue 10/20/15
47	Test Completed Boards	Sun 10/18/15	Tue 10/20/15
46	Create Project Presentation Board	Wed 10/21/15	Thu 10/22/15
16	Design Initial USB to UART Schematic	Tue 10/27/15	Tue 10/27/15
17	Design Initial Battery Charger Schematic	Tue 10/27/15	Tue 10/27/15

November 2015

Monday	Tuesday	Wednesday	Thursday	Friday	Saturday
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Trestient Cariouale, 1		•			Revise 1st Board Lay
Order Parts, 1 day					
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i i i	oc 13t Escharapysioc, 1012	auy 5			
22	2.4	0.5	0.5		
23				27	28
Review Test Plans 1	_	odia Assembly, 5 days		Program Tranceiver	Initialization 2 days
Neview Test Flatis, 1				Frogram tranceiver	mitialization, 2 days
30					
	9 Review Schedule, 1 Order Parts, 1 day 16 rd Layout, 2 days Review 23 Review Test Plans, 1	9 10 Review Schedule, 1 Create Final Schema Order Parts, 1 day 16 17 rd Layout, 2 days Order Boards, 1 day Revise 1st Exarella pybliot, ISD 23 24 Review Test Plans, 1	Develop Preliminary Schedule, Schematic Design R. Setup Tool Chain, 1 Proview Schedule, 1 (Create Final Schematic Revision, 2 days Create Final Schematic Revision, 2 days Order Parts, 1 day The series of th	Develop Preliminary Schedule, 2 days Schematic Design R Setup Tool Chain, 1 Program Hello Worl Setup Tool Chain, 1 Review Schedule, 1 ι Revise Schedule, 3 days Create Final Schematic Revision, 2 days Order Parts, 1 day 16 17 18 19 rd Layout, 2 days Order Boards, 1 day Develop Test Plans, 3 day Revise 1st Είλεντεἰφργδιοτ, ιδείλανs 23 24 25 26 Board Assembly, 3 days Review Test Plans, 1	Develop Preliminary Schedule, 2 days Revise Schematic, 3 day Schematic Design R. Setup Tool Chain, 1 Review Schedule, 1 Revise Schedule, 3 days Create Final Schematic Revision, 2 days Order Parts, 1 day To deep Boards, 1 day Develop Test Plans, 3 day Review Board Layou Revise 1st Expandlopy@dot,k50days Review Test Plans, 1 Review Test Plans, 1 Program Tranceiver

Overflow Tasks

ID	Name	Start	Finish
21	Develop Initial Board Layout	Thu 11/12/15	Fri 11/13/15
26	Create Solder Paste Stencil	Sun 11/22/15	Sun 11/22/15
41	Program Test of GPIO	Fri 11/27/15	Sat 11/28/15
33	Program Tansceiver Listening Interrupt	Sun 11/29/15	Mon 11/30/15
35	Program Tranceiver Broadcasting	Sun 11/29/15	Mon 11/30/15
38	Program Test of Timers	Sun 11/29/15	Mon 11/30/15

December 2015

Sunday	Monday	Tuesday	Wednesday	Thursday	Friday	Saturday
		1	2	3	4	
						Create Project Preser
6		8	9	10	11	12
Create Project Preser	ntation Video, 3 days					
13	14	15	16	17	18	19
13		13	10	.,	10	13
20	21	22	23	24	25	26
27	28	29	30	31		

Overflow Tasks

ID	Name	Start	Finish
39 40	Program Test of SPI Program Test of UART	Tue 12/1/15 Tue 12/1/15	Wed 12/2/15 Wed 12/2/15