ECGR 4101/5101 LECTURE 14-8, +x-8; ry-g -> Data [i]; ry- 9-7 Tail; VX-8 -> 5:20; t= 48; x48 Receive x 45 / Receive pre 1 Consume x 52 Receive ore Consulve x201 Receive x201 KZOV x20x20-420, 420 x20

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Receive x 41

Receive x 42

Consume Return 2

Consume Return 2

Consume Return 0

Consume Return 0

Receive x 43

rv-g-7 Data [i] 000000 rv-g-7 Head 0x2 rv-g-> Tail 0x23 rv-g-> Size 0x0x01

```
// Quiz 11 code
#define Q SIZE (8)
typedef struct {
  unsigned char Data[Q SIZE];
  unsigned int Head; // points to oldest data element
  unsigned int Tail; // points to next free space
                                                               0
  unsigned int Size; // quantity of elements in queue
} Q T;
                                                                1
Q T tx q, rx q;
                                                               2
                                                               3
void Q Init(Q T * q) {
  unsigned int i;
                                                               4
  for (i=0; i<Q SIZE; i++)
    q->Data[i] = 0; // to simplify our lives when debugging
  q->Head = 0;
  q->Tail = 0;
  q->Size = 0;
int Q Empty(Q T * q) {
  return q->Size == 0;
int Q Full(Q T * q) {
  return q->Size == Q SIZE;
// Q Enqueue - Called by a UART ISR - put a char on the queue
int Q Enqueue(Q_T * q, unsigned char d) {
  if (!Q_Full(q)) { // What if queue is full?
    q-Data[q-Tail++]=d;
    q->Tail %= Q_SIZE;
    q->Size++;
    return 1; // success
  } else
    return 0; // failure
// Q Dequeue-called by a consumer function-take a char from queue
unsigned char Q Dequeue (Q T * q) {
  unsigned char t=0;
                        // Must check to see if queue is empty 1st
  if (!Q Empty(q)) {
    t = q-Data[q-Head];
    q-Data[q-Head++]=0; // to simplify debugging, clear
    q->Head %= Q SIZE;
    q->Size--;
  return t;
}
```