LECTURE - 18 ECGR 4101

State machines

App low Drivers

OS

Mcontroller Board

HOWR Sensor

Actuator

App of the day Soutery Device

ECGR 4101/5101	-	LECTUR	2E 1	8
CDMt cell ph 1.024 seconds For a "page"	one a	ohone li	ous stas	
			1	Preset
Timer reads:		TITLETELS	LITE	V →
Clock Counter/"alarm	PARTY OF THE PARTY	Stort Stort		8
Event counter:				
	A			
T	1	- Jk		
		Not clack		1
		0		

PCGR 4101/5101 - LECTURE 18	
24 MHz clock PCLK	(3)
/8192	
New Frequency? 24000 000 Hz = 2929.	68 x3
.0003413 Seconds	
100 ms = how many "ticks"	
100 ms = 100 ms = 292, 96875 .0003413 seconds = 292, 96875	
293± 2929.6875 = .1000106	-
Running at PCLK/8192 ->	
and out 2017	
egral 0.100 second	
Register 293 Court Clown	
How Nilot D	
realister.	
FEFFE 293 Need a bits	bit

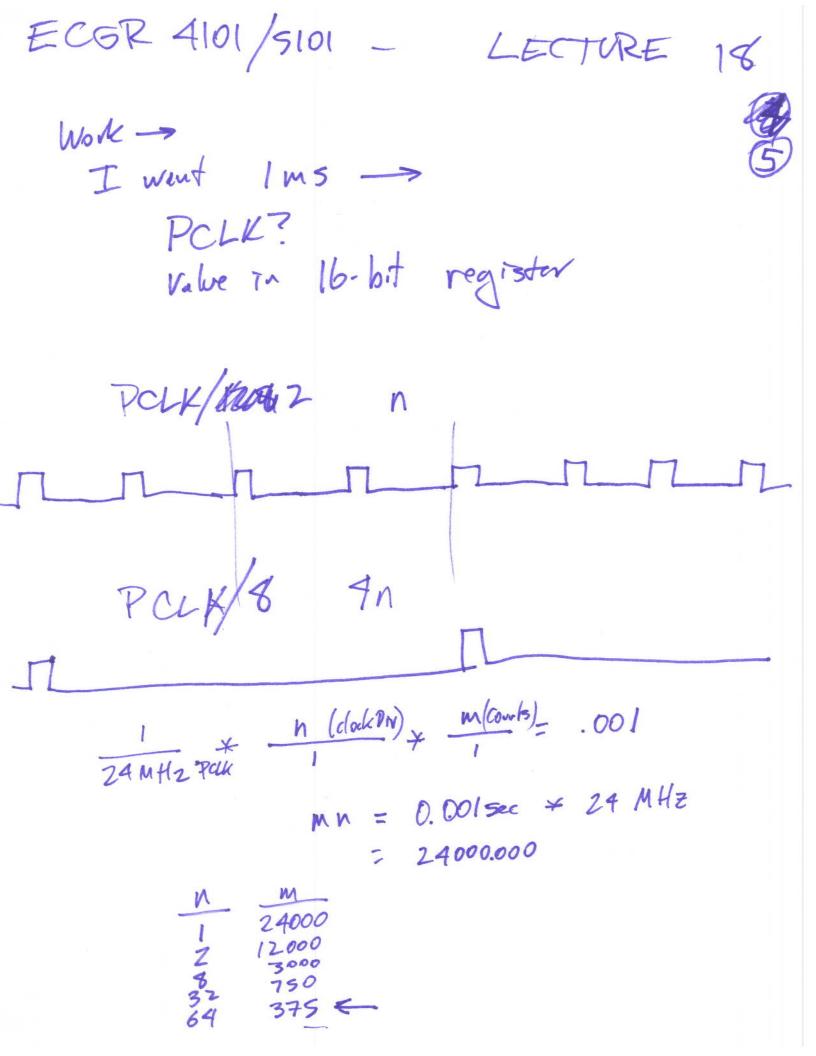
Timers

8 bits (4 available)

16 bits (2 available)

1 counter

255



ECGR 4101/5101

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