

MOHAMMED SUBHAN KHAN

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Chicago, Illinois

EXPERIENCE

Project Engineer, Wipro Ltd (Hyderabad, India)

Aug 2017 - Mar 2019

VLSI engineer in FPGA verification.

- Testbench development in SystemVerilog with UVM methodology.
- Development of UVM testbench environment, verification components, sequences, and tests used for HDMI 2.1 at chip level.
- Developed and integrated basic tests related to DDR4 controller in testbench.
- Contributed to development of test plans and hardware design documents.

Research Intern, DLRL (Hyderabad, India)

May 2016 - Jun 2016

- Implemented the FFT algorithm in C and MATLAB for various radix sizes. Found a correlation between amplitude of the FFT of different radix sizes and the accuracy with which properties of receiver signal can be obtained.

EDUCATION

Osmania University, India

2013-2017

- B.E. in Electronics and Communication Engineering. GPA: 8.72/10
- **Relevant Coursework** Computer Architecture, Computer Networks, Microprocessors, C, C++.

PROJECTS

All project code can be found on GitHub at, <https://github.com/khansubhan95>

- **Blog System** Blog application that provides CRUD features using Spring MVC, Hibernate and MySQL. Includes a user authentication system made with Spring Security.
- **Tasks API** RESTful API made using Spring that allows user to create/edit tasks. Implements a JWT token based auth system and uses MongoDB for storage.
- **Book Trading** Node.js application that allows users to lend and borrow books. Also consists of a REST API. Uses MongoDB for storage and Passport for authentication.
- **Markdown Editor** React app that renders markdown text into html in real time.
- **Minesweeper** Python clone of the Minesweeper game made using PyGame.

TECHNICAL SKILLS

- **Languages** Java, JavaScript, Python.
- **Frontend** React, Bootstrap, jQuery, Sass.
- **Backend** Spring, Spring MVC, Node.js/Express, Hibernate.
- **Databases** MySQL, MongoDB.
- **Tools/Others** Git.