

Harrison Carpenter, WORK LOG

Milestone 1 Work

Wednesday, January 3rd, 2024

Met with team for 30 minutes and divided up work

Tuesday, January 9th, 2024

Met with team and completed milestone 1

M2

Completed single-cycle RTLs for add, sub, addi, lw, sw, beq, bgt, and JAL.

M3

Completed multi-cycle RTL and created and tested MUX component in verilog.

M4

Implemented and tested control unit in verilog. Also implemented immediate generator and tested.

M5

Assisted with control unit implementation.