



FTBU: From booleans to memory

From the Bottom Up Series

Lecture Outline

01

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Registers

- Store bits until instruction to load or store other bits
- Combination of muxes and flip flops

02

...

Multi Bit Registers

- A w -bit register is an array of 1 bit registers

03

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RAM

- Sequence of n addressable registers with addresses 0 to $n-1$
- Registers hold bits which can be interpreted as instructions (load, store, add, subtract, etc)

