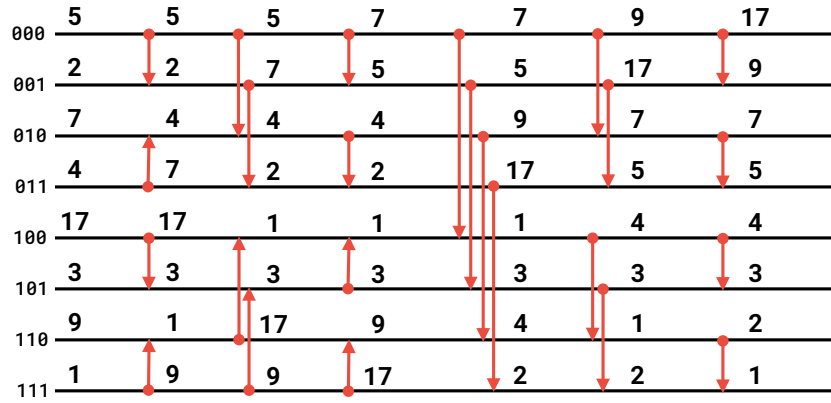


CSE 6220 Homework 3

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1

Draw a 8-element bitonic sorting circuit to sort elements in non-increasing order. Use horizontal lines for the numbers and vertical lines to denote comparison operations. Label the comparison operations as \uparrow or \downarrow where the direction of the arrowhead indicates the destination of the smaller element. Illustrate how the input 15, 2, 7, 4, 17, 3, 9, 1 is sorted using the diagram.



The only modification needed to the bitonic sort algorithm to sort elements in *non-increasing* order, instead of *non-decreasing* order, is to flip the `Compare_Exchange` directions.

2

The *Bitonic Split* operation defined in class assumes that the bitonic sequence has an even length. Extend this operation to bitonic sequences of odd length. Now, show how the bitonic sequence 5, 3, 4, 7, 10, 14, 17, 13, 8 can be converted into a sorted sequence using repeated bitonic split operations.

Ordinary Bitonic Split works by decomposing a bitonic sequence $l = x_0, x_1, \dots, x_{n-1}$ into

$$l_i^- \equiv \min(x_i, x_{i+\frac{n}{2}}), \quad l_i^+ \equiv \max(x_i, x_{i+\frac{n}{2}}), \quad 0 \leq i \leq \frac{n}{2} - 1.$$

One way to extend this operation to bitonic sequences of odd length is to use a similar strategy to the one we used to extend parallel prefix to non-power-of-two numbers of processors. In that case, we kept the exact same algorithm, "pretending" that there were $p = 2^m$ processors, and ignored them during communication. Similarly, we can "pretend" the given sequence l has

length $2m$, by appending a "dummy" element if l has odd length. Then, after completion of the split, we can remove the dummy element.

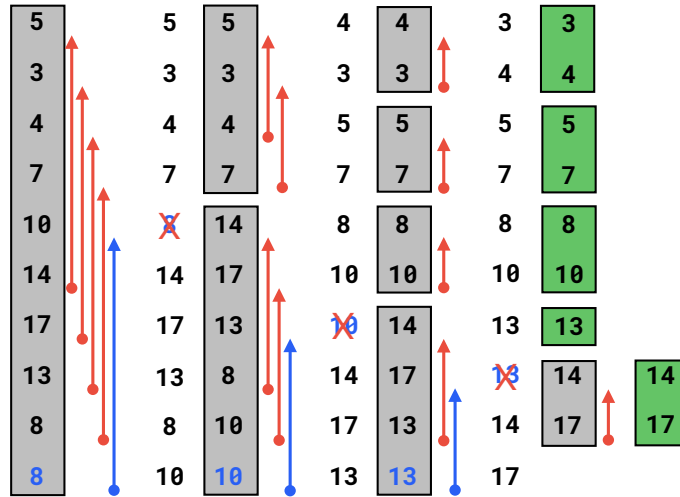
Let $\tilde{l} \equiv l + \{D\}$ refer to the sequence obtained by appending the element D to the sequence l . Then as long as we choose D such that \tilde{l} is also bitonic, after we apply the usual split operation we will have bitonic split sequences $\tilde{l}^- \equiv \tilde{l}_{min}$ and $\tilde{l}^+ \equiv \tilde{l}_{max}$, and we will also have $\max(\tilde{l}^-) \leq \min(\tilde{l}^+)$.

When we later *remove* the appended element D , we must remove it from either \tilde{l}^- or \tilde{l}^+ . If D was in \tilde{l}^- , removing it will clearly maintain $\max(\tilde{l}^- - \{D\}) \leq \max(\tilde{l}^-)$. Likewise, removing D from \tilde{l}^+ will maintain $\min(\tilde{l}^+ - \{D\}) \geq \min(\tilde{l}^+)$. So we have $\max(\tilde{l}^- - \{D\}) \leq \min(\tilde{l}^+ - \{D\})$. Finally, note that by the definition of bitonic sequences, removing *any* single element from a bitonic sequence results in another bitonic sequence.

Thus, if we append an element that produces another bitonic sequence, then perform the usual split operation on the resulting sequence, and finally remove the appended element, we maintain all properties required for the Bitonic Split operation. A simple choice for D which guarantees \tilde{l} is bitonic is to *duplicate the last element*. (We could also *prepend* the first element.)

Here is our algorithm:

- If l has length 1 or 2, there is nothing to do.
- Otherwise, if l has even length, perform ordinary bitonic split.
- Otherwise, if $l = x_0, x_1, \dots, x_{n-1}$, has odd length,
 - Append a new element $D = l_{n-1}$ to create $\tilde{l} = x_0, x_1, \dots, x_{n-1}, x_{n-1}$.
 - Perform ordinary bitonic split on \tilde{l} , marking the new position of the element D during its compare-exchange.
 - Create split sequences $l^- \equiv \tilde{l}^- - \{D\}$, $l^+ \equiv \tilde{l}^+ - \{D\}$. (To be clear, only one of \tilde{l}^- or \tilde{l}^+ will contain D .)



3

Give an algorithm to merge two sorted sequences of lengths m and n , respectively. You may assume that the input is an array of length $m + n$ with one sequence followed by the other, distributed across processors such that each processor has a subarray of size $\frac{m+n}{p}$. What are the computation and communication times for your algorithm? (You may assume the use of any permutation-style communication, not necessarily hypercubic.)

Observe that for any two consecutive sorted sequences, we can reverse the second sequence to obtain a single bitonic sequence.

First, let's restrict ourselves to the case where $m + n = p$, so that each processor gets one element. Then, we can reverse the last n elements by exchanging every pair

$$(m, m + n - 1), (m + 1, m + n - 2), \dots, \left(m + \frac{n}{2} - 1, m + \frac{n}{2}\right).$$

This takes $O((\tau + \mu)m)$ communication time. However, each processor does not know what the value of m is. We can determine m by finding the first processor whose element is *less than* the previous processor's element. This can be accomplished with a right-shift communication and a comparison on each processor ($O(\tau + \mu)$ communication time, $O(1)$ computation time).

After finding m and reversing the last n elements, we can perform a single Bitonic Merge, with computation time $O(\log p)$ and communication time $O((\tau + \mu) \log p)$.

Thus, when $m + n = p$, we can merge the two sorted sequences with:

Computation time: $O(1 + \log p)$

Communication time: $O((\tau + \mu)m + \tau + \mu + (\tau + \mu) \log p)$ TODO clarify comm time for bitonic merge w/ $n = p$. And also make sure thinking about shift operator time correctly. Then generalize to each processor holding $\frac{m+n}{p}$ elements.

4

Two algorithms are designed for the All-to-all communication primitive, and have the following runtimes:

Algorithm 1 : $\Theta(\tau \log p + \mu mp \log p)$

Algorithm 2 : $\Theta(\tau p + \mu mp)$

Determine which algorithm runs faster asymptotically as a function of the message size.

As mentioned in class, we can simplify this problem by comparing the dominating terms. Since τp dominates $\tau \log p$, and $\mu mp \log p$ dominates μmp , we can simply compare these two terms.

Asymptotically with respect to m , Algorithm 1 will run faster roughly when

$$\Theta(\mu mp \log p) < \Theta(\tau p) \implies m < \frac{\tau}{\mu \log p}.$$

For messages sizes beyond this threshold, Algorithm 2 will run faster.

5

Consider a tree of n nodes and having a bounded degree (i.e., the number of children per node is bounded by a constant). The tree is stored in an array A of size n . Each node also contains the

indices at which its parent and children are stored in the array. The array is distributed among processors using block decomposition, i.e., P_i contains $A[i\frac{n}{p}], A[i\frac{n}{p} + 1], \dots, A[(i+1)\frac{n}{p} - 1]$. For each of the following operations, which communication primitive will you use and what is the runtime?

- (a) Each node in the tree has $O(1)$ sized data that should be sent to its parent.
- (b) Each node in the tree has $O(1)$ sized data that should be sent to all its children.

Since each node's parent and/or children may each be located in a different processor, we must either use the All-to-All or Many-to-Many communication primitive for both operations.

For (a), the size of each sent message will be constant, since each node apart from the root has exactly one parent. This would indicate using All-to-All. However, we can improve the runtime using Many-to-Many by observing that each *received* message size can be variable (since each parent may have a different number of children) and bounded (by the tree degree).

The runtime for Many-to-Many can be derived by replacing the message size m in the All-to-All runtime $\Theta(\tau p + \mu m p)$ with the sum of the maximum sent and received message sizes.

Let D denote the tree degree, and R and S denote the maximum received and sent message sizes for a single processor. Then we have

$$m \leq (R + S) = \left(D\frac{n}{p} + \frac{n}{p} \right) = \frac{n}{p}(D + 1),$$

giving a runtime for (a) of

$$\Theta(\tau p + \mu m p) = \Theta(\tau p + \mu \frac{n}{p}(D + 1)p) = \Theta(\tau p + \mu n(D + 1)) = \Theta(\tau p + \mu n).$$

For (b), the upper bound for the received and sent message sizes are flipped, but this will not change the value of $R + S$. So the runtime for both (assuming we have a network allowing arbitrary permutations) will be $\Theta(\tau p + \mu n)$.