

CERTIFICATE OF BEST PAPER

THIS IS HEREBY PRESENTED TO

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PATIL, *5 HUSSAIN AL-ASAAD**
(***1,2,3,4,5(UNIVERSITY OF CALIFORNIA, DAVIS, USA)**)

FOR THE PAPER TITLED

**EFFICIENT SIMULATION-BASED VERIFICATION USING HIGH-
LEVEL DESIGN ERROR MODELS**

IN THE CATEGORY OF

VLSI, SEMICONDUCTOR INNOVATION, AND NANO ELECTRONICS

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ELECTRONICS AND MOBILE COMMUNICATION CONFERENCE

