CONFIDENTIAL - Do Not Release to Students

SINGAPORE POLYTECHNIC

2019 / 20 Semester _1_ Mid Semester Test

Module Name: MAPP / EDBPII

Module Code: <u>ET1010 / ET1216</u>

Course: DASE/DESM/DCPE/DEEE/DMRO/DEB

Year: 2FT

Set by: M.Fikret Ercan

Page 1 of 6

No.	SOLUTION	MARKS	TOTAL MARKS
	Section A		
A1	d	3	
A2	а	3	
А3	b	3	
A4	b	3	
A 5	С	3	
A 6	а	3	
A7	d	3	
A8	b	3	
A 9	С	3	
A10	b	3	
			30

^{1.} All diagrams should be drawn clearly in black. Fine lined images may not be captured by the photocopier.

^{2.} Lines should be drawn as thick as possible and in bold.

^{3.} The use of shading and colouring is discouraged. If shading is necessary, use 5% grey.

CONFIDENTIAL - Do Not Release to Students

SINGAPORE POLYTECHNIC

2019 / 20 Semester _1_ Mid Semester Test

Module Name: MAPP / EDBPII

Module Code: ET1010 / ET1216

Course: DASE/DESM/DCPE/DEEE/DMRO/DEB

Year: 2FT

Set by: M.Fikret Ercan

Page 2 of 6

No. SOLUTION	MARKS	TOTAL MARKS
Section B		
a) TRISD = 0b 0000 0000; // last bit must be 0	4	
b) PORTDbits.RD0 = 1; // Set pulse HIGH	1	
delay_ms(2); // delay for 2 ms	2	
PORTDbits.RD0 = 0; // Set LOW	1	
delay_ms(18); // delay for 20 ms	2	
с)		
int i; // configure the pins TRISDbits.TRISD0 = 0; while (1) { for (i=0;i<100;i++) { PORTDbits.RD0 = 1;	2	
<u>PORTDbits.RD0 = 0;</u> // set LOW <u>delay ms(18);</u> // delay 18 ms	2	
} // for for (<u>i=0;i<150;i++</u>) {	2	
PORTDbits.RD0 = 1; // set HIGH delay ms(1); // delay for 1ms PORTDbits.RD0 = 0; // set LOW	2	
delay ms(19); // delay 19 ms		
} // for } // while		
		18

^{1.} All diagrams should be drawn clearly in black. Fine lined images may not be captured by the photocopier.

^{2.} Lines should be drawn as thick as possible and in bold.

^{3.} The use of shading and colouring is discouraged. If shading is necessary, use 5% grey.

CONFIDENTIAL - Do Not Release to Students

SINGAPORE POLYTECHNIC

2019 / 20 Semester _1_ Mid Semester Test

Module Name: _MAPP_/EDBPII

Module Code: <u>ET1010 / ET1216</u>

Course: _DASE/DESM/DCPE/DEEE/DMRO/DEB

Year: 2FT_

Set by: _M. Fikret Ercan

Page 3 of 6

No.	SOLUTION	MARKS	TOTAL MARKS
	Section B		
B2	(a)		
	Buzzer 470 RD7 Q 5V 6 RD7 Q 6 RD		
	Correct seven segment display circuit 2 marks, Correct button circuit 2 marks ,Correct buzzer circuit 2 marks	6	
	(b) TRISD = 0b 0000 0000; TRISB = 0b 1111 1111;	2	

^{1.} All diagrams should be drawn clearly in black. Fine lined images may not be captured by the photocopier.

^{2.} Lines should be drawn as thick as possible and in bold.

^{3.} The use of shading and colouring is discouraged. If shading is necessary, use 5% grey.

CONFIDENTIAL - Do Not Release to Students

SINGAPORE POLYTECHNIC

2019 / 20 Semester _1_ Mid Semester Test

Module Name: _MAPP / EDBPII

Module Code: <u>ET1010 / ET1216</u>

Course: DASE/DESM/DCPE/DEEE/DMRO/DEB

Year: 2FT

Set by: <u>M. Fikret Ercan</u>

Page 4 of 6

No.	SOLUTION	MARKS	TOTAL MARKS
	Section B		
B2	Set i/o pins Counter=0 Delay 1 sec Increment count YES Stop buzzer Stop buzzer K Start A Set i/o pins B Increment counter=0 Is counter=0 C Is button pressed? Display count F Start buzzer Stop buzzer K Start A Set i/o pins B Increment counter H counter=0 C Is button pressed? Is count = 8? J Delay 1 sec Order for "Set i/o pins", "counter=0" can be reversed award full mark as long as flow chart works)	10	
			18

^{1.} All diagrams should be drawn clearly in black. Fine lined images may not be captured by the photocopier.

^{2.} Lines should be drawn as thick as possible and in bold.

^{3.} The use of shading and colouring is discouraged. If shading is necessary, use 5% grey.

CONFIDENTIAL - Do Not Release to Students

SINGAPORE POLYTECHNIC

2019 / 20 Semester _1_ Mid Semester Test

Module Name: _MAPP/EDBPII

Module Code: <u>ET1010 / ET1216</u>

Course: _DASE/DESM/DCPE/DEEE/DMRO/DEB_

Year: _2FT_

Set by: _M. Fikret Ercan

Page 5 of 6

No.	SOLUTION	MARKS	TOTAL MARKS
	Section B		
В3	a) $\frac{5v-0}{10-5} = \frac{2.78-0}{?-5} \text{ Ph=7.78}$	4	
	b) 0 and 1023 (or, 000000000 and 111111111)	2	
	c) Ph =9 ->818 -> 1100110010	6	
	Ph=8 -> 614 -> 1001100110		
	(3 marks each)		
	d) Waiting for AD conversion to complete and data captured	2	
	e) left justified	2	
			16

^{1.} All diagrams should be drawn clearly in black. Fine lined images may not be captured by the photocopier.

^{2.} Lines should be drawn as thick as possible and in bold.

^{3.} The use of shading and colouring is discouraged. If shading is necessary, use 5% grey.

CONFIDENTIAL - Do Not Release to Students

SINGAPORE POLYTECHNIC

2019 / 20 Semester _1_ Mid Semester Test

Module Name: <u>MAPP/EDBPII</u>

Module Code: <u>ET1010 / ET1216</u>

Course: _DASE/DESM/DCPE/DEEE/DMRO/DEB_

Year: _2FT_

Set by: _M,Fikret Ercan

Page 6 of 6

No.	SOLUTION	MARKS	TOTAL MARKS
	Section B		
B4	(a) correct use of shunt diode (2 marks), correct connection of motor (2 marks), correct transistor connection (4 marks) PIC18F4550 Digital output pin bc current	8	
	(b) Example solution (Student may provide different solution. Award grade based on your professional judgement). Port configuration 2 marks, correct use of while loop (2 marks), correct if-else constructs (6 marks) // configure the pins int i=0; TRISAbits.TRISA0 = 1; TRISDbits.TRISD1 = 0; while (1) { if (PORTAbits.RA0==1 && i==0) { delay_ms (2000); if (PORTAbits.RA0==1) i=1; else i=0; // check if SW1 still pressed } if ((PORTAbits.RA0==1) && (i==1)) { // i=1 indicates 2 second delay PORTDbits.RD1 = 1; // turn on the meter delay_ms(20); // give a short delay } else { PORTDbits.RD1 = 0; // turn off the motor i=0; } } // while	10	
	<i>In</i> write		

^{1.} All diagrams should be drawn clearly in black. Fine lined images may not be captured by the photocopier.

^{2.} Lines should be drawn as thick as possible and in bold.

^{3.} The use of shading and colouring is discouraged. If shading is necessary, use 5% grey.