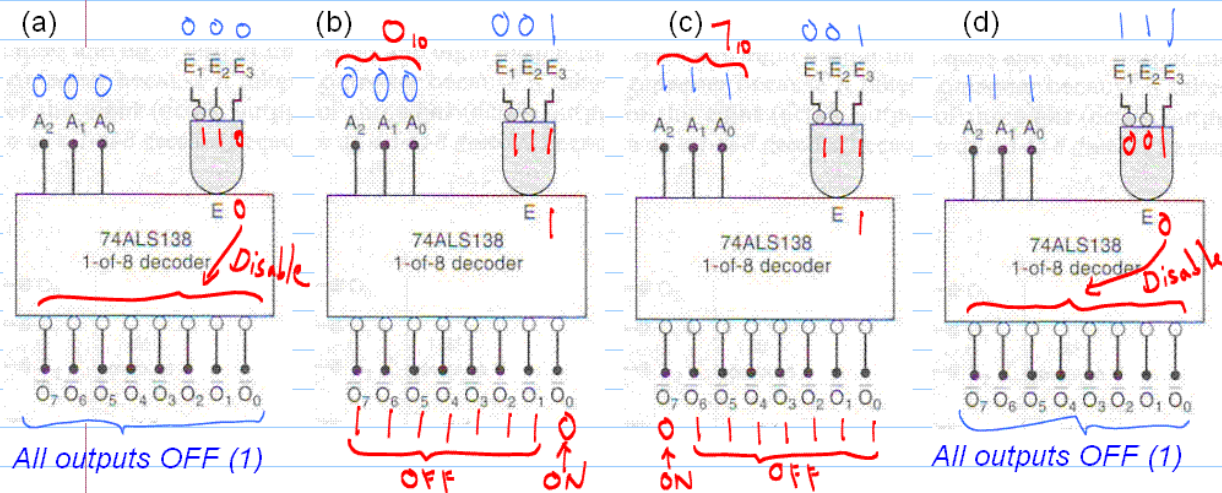


→ 9-1. Refer to Figure 9-3. Determine the levels at each decoder output for the following sets of input conditions.

05/01/2010

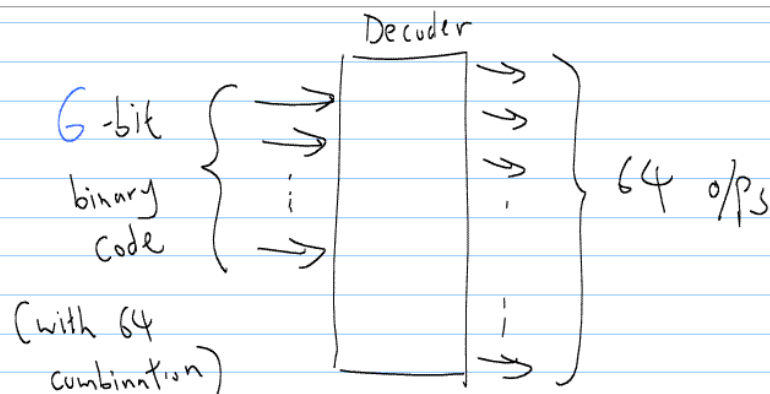
- (a) All inputs LOW
- (b) All inputs LOW except  $E_3 = \text{HIGH}$
- (c) All inputs HIGH except  $\bar{E}_1 = \bar{E}_2 = \text{LOW}$
- (d) All inputs HIGH



→ 9-2. What is the number of inputs and outputs of a decoder that accepts 64 different input combinations?

Note Title

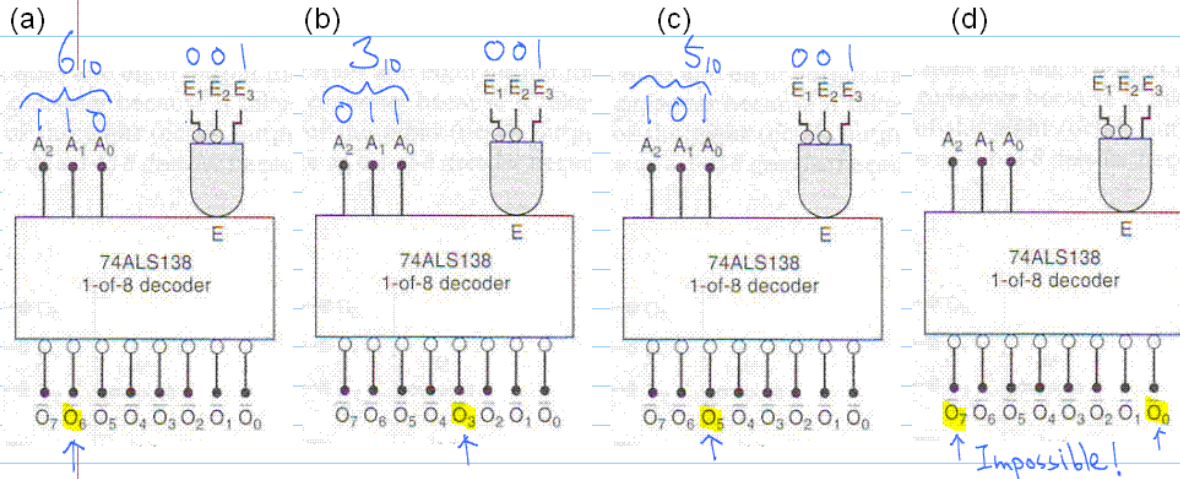
09/01/2009



Each combination will make 1 of the o/p's ON (while the other o/p's are OFF).

9-3. For a 74ALS138, what input conditions will produce the following outputs:

- (a) LOW at  $\bar{O}_6$
- (b) LOW at  $\bar{O}_3$
- (c) LOW at  $\bar{O}_5$
- (d) LOW at  $\bar{O}_0$  and  $\bar{O}_7$ , simultaneously

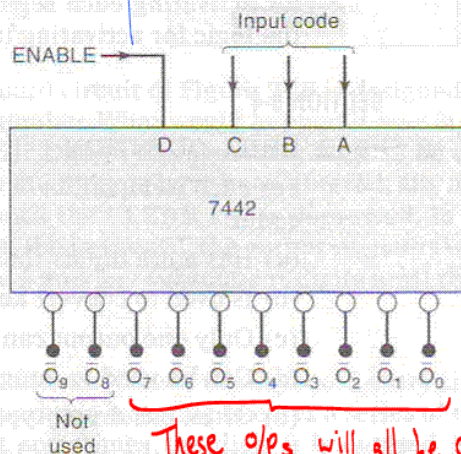


9-7  
Connect 7442 as  
a 1-of-8 decoder.

When  $D=0$ , one of  $Q_0$  to  $Q_7$  will be ON

$D=1$ , none  $Q_0$  to  $Q_7$  will be ON

(Only  $Q_8$  or  $Q_9$  will be ON)



Inputs				Active Output
D	C	B	A	
L	L	L	L	$\bar{O}_0$
L	L	L	H	$\bar{O}_1$
L	L	H	L	$\bar{O}_2$
L	L	H	H	$\bar{O}_3$
L	H	L	L	$\bar{O}_4$
L	H	L	H	$\bar{O}_5$
L	H	H	L	$\bar{O}_6$
L	H	H	H	$\bar{O}_7$
H	L	L	L	$\bar{O}_8$
H	L	L	H	$\bar{O}_9$
H	L	H	L	None
H	L	H	H	None
H	H	L	L	None
H	H	L	H	None
H	H	H	L	None
H	H	H	H	None

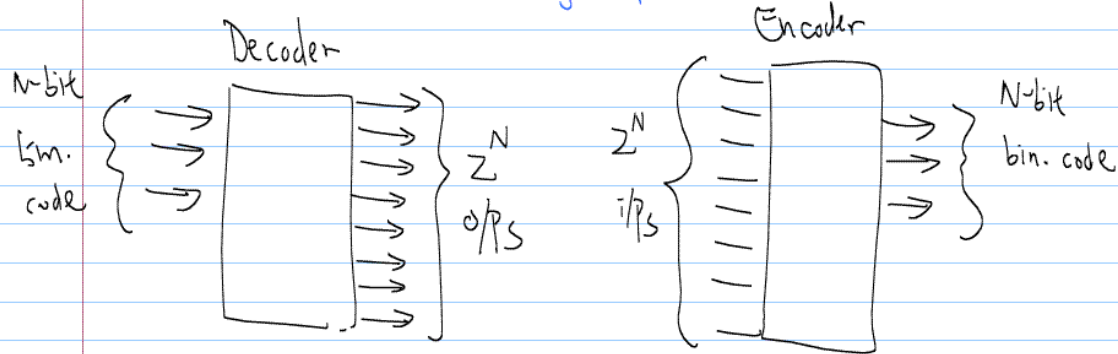
H = HIGH Voltage Level  
L = LOW Voltage Level

### → 9.13. DRILL QUESTION

For each item, indicate whether it is referring to a decoder or an encoder.

- (a) Has more inputs than outputs. *Encoder*
- (b) Is used to **convert** key actuations **to a binary code**. *Encoder*
- (c) Only one output can be activated at one time. *Decoder*
- (d) Can be used to interface a BCD input to an LED display. *Encoder* (Sect. 9-2 P470 - not covered)
- (e) Often has driver-type outputs to handle large  $I$  and  $V$ . *Decoder*

*ie current or voltage amplifier*



### → 9.14. Determine the output levels for the 74147 encoder when $\bar{A}_8 = \bar{A}_4 = 0$ and all other inputs are HIGH.

$A_1$	$A_2$	$A_3$	$A_4$	$A_5$	$A_6$	$A_7$	$A_8$	$A_9$	$O_3$	$O_2$	$O_1$	$O_0$
1	1	1	1	1	1	1	1	1	1	1	1	1
X	X	X	X	X	X	X	X	0	0	1	1	0
X	X	X	X	X	X	X	0	1	1	0	0	0
X	X	X	X	X	0	1	1	1	1	0	0	1
X	X	X	X	0	1	1	1	1	1	0	1	0
X	X	X	0	1	1	1	1	1	1	1	0	1
X	X	0	1	1	1	1	1	1	1	1	0	0
X	0	1	1	1	1	1	1	1	1	1	0	1
0	1	1	1	1	1	1	1	1	1	1	1	0

X = either 0 or 1

Since  $\bar{A}_8$  has higher priority than  $\bar{A}_4$ , the output will give the inverted version of 8 ( $1000_2 \rightarrow 0111_2$ )

$\underbrace{1000_2}_{8_{10}} \quad \underbrace{0111_2}_{\text{Inverted}}$