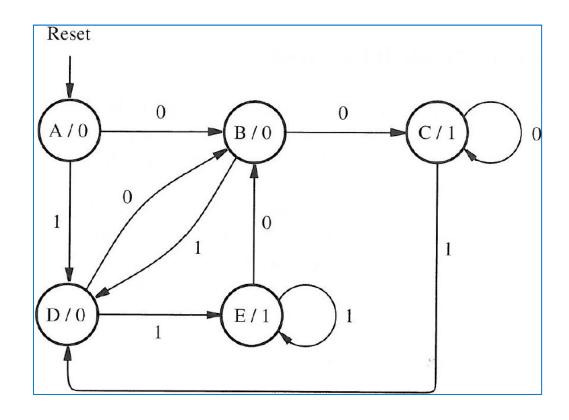
A finite state machine has an input w and an output z. The machine is a sequence detector that produces z = 1 when the previous two values of w were 00 or 11; otherwise z = 0.

- (a) Draw the Moore's state diagram.
- (b) Derive the state table from part (a).



Present state	Next	Output	
	w = 0	w = 1	Z
A	В	D	0
В	C	D	0
C	C	D	1
D	В	E	0
E	В	E	1

(Not required in question B1.)

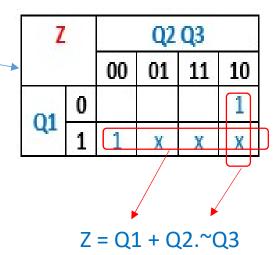
Using binary state assignment (3-bit: Q1, Q2, Q3).

Present	Next s	Output		
state	w = 0	w = 1	Z	
A ₀₀₀	B001	D011	0	
B001	C010	D011	0	
C010	C010	D 011	. 1	
D011	B001	E 10¢	0	
E100	B001	E 10¢	1	

1	Next State				Output	•
Primary	Decoder		Current		Decoder	Prima
Input W	Combinational	D	State	200	Combinational	Outpu
· VV	circuit	_	Flip-flops	Q	circuit	Z
Г	-	_				
				J 🥕		
					Note: A, Q	
Clock —		_			can be mult	i-bit.

Deriving for the Output Circuit:

State	Q1	Q2	Q3	Output Z
Α	0	0	0	0
В	0	0	1	0
С	0	1	0	1
D	0	1	1	0
Е	1	0	0	1
	1	0	1	X
	1	1	0	Х
	1	1	1	Х



Deriving for the Next State Circuit:

State	Q1	Q2	Q3	Input w	Next State	D1	D2	D3
^	0	0	0	0	В	0	0	1
Α	0	0	0	1	D	0	1	1
В	0	0	1	0	С	0	1	0
Ь	0	0	1	1	D	0	1	1
С	0	1	0	0	С	0	1	0
	0	1	0	1	D	0	1	1
D	0	1	1	0	В	0	0	1
D	0	1	1	1	E	1	0	0
Е	1	0	0	0	В	0	0	1
L	1	0	0	1	Е	1	0	0
	1	0	1	0		X	X	X
	1	0	1	1		X	X	X
Not	1	1	0	0		X	X	X
used	1	1	0	1		X	X	X
	1	1	1	0		X	X	X
	1	1	1	1		X	X	X

D1		Q3 w				
		00	01	11	10	
100	00					
04.03	01			1		
Q1 Q2	11	Х	X	X	Х	
	10		1	X	Х	

D1 = Q1.w + Q2.Q3.w

D2		Q3 w				
14.0		00	01	11	10	
	00	j	1	1	1	
04.03	01	1	1			
Q1 Q2	11	Х	Х	Х	Х	
	10			Х	Х	

 $D2 = Q2.^{Q}3 + ^{Q}2.Q3 + ^{Q}1.^{Q}3.w$

D3		Q3 w				
		00	01	11	10	
Q1 Q2	00	1		П	X X	
	01		1		1	
	11	Х	Х	Х	Χ	
	10	1	8 33	Х	X	

D3 =

~Q2.~Q3.~w +

~Q1.~Q3.w +

~Q1.~Q2.w +

Q2.Q3.~w