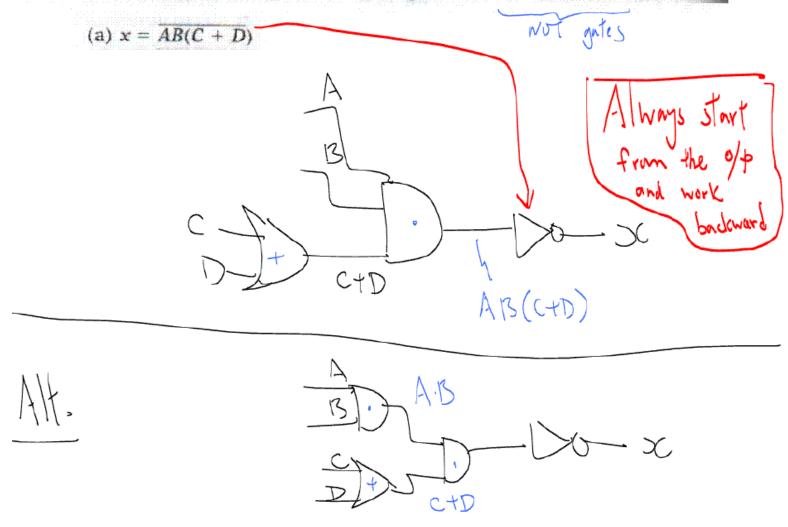
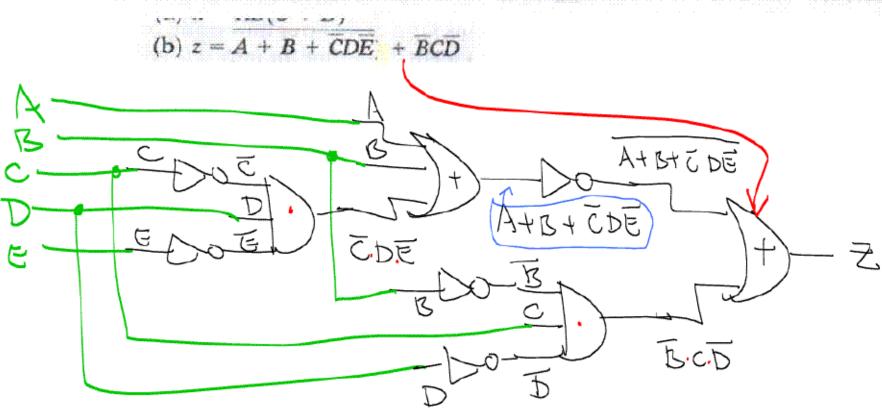
(Without simplification)

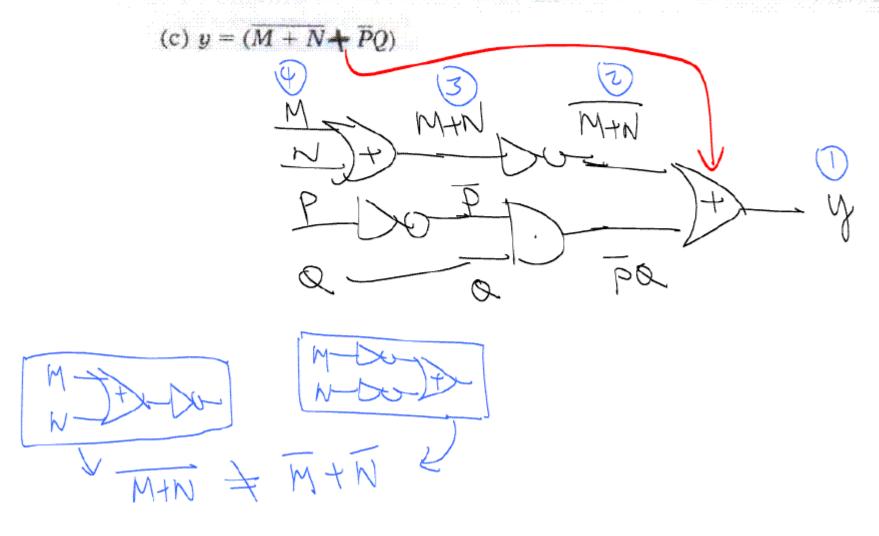
B→3-16. For each of the following expressions, construct the corresponding logic circuit, using AND and OR gates and INVERTERs.



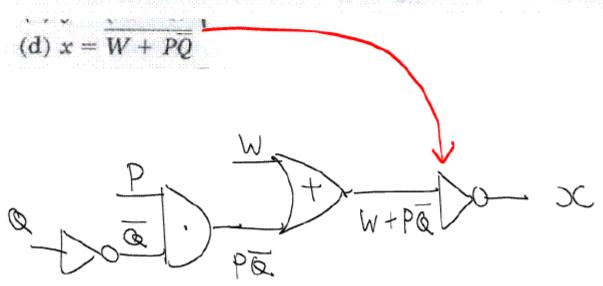
B→3-16. For each of the following expressions, construct the corresponding logic circuit, using AND and OR gates and INVERTERs.

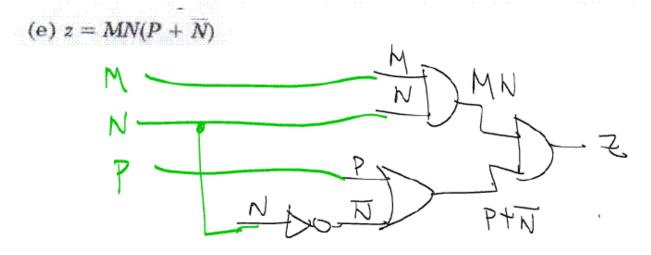


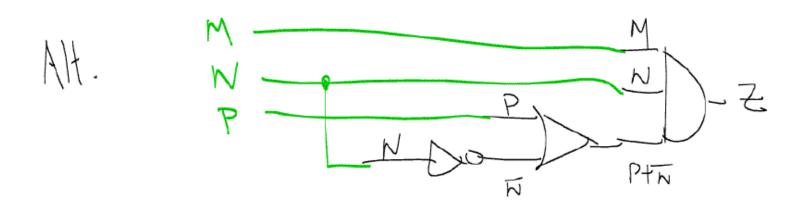
B→3-16. For each of the following expressions, construct the corresponding logic circuit, using AND and OR gates and INVERTERs.



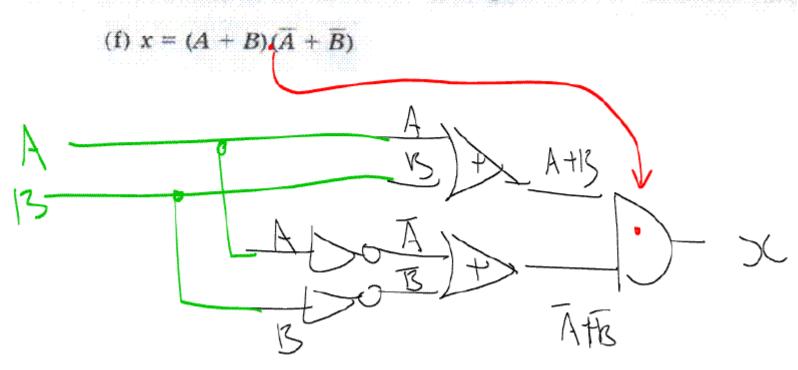
B → 3-16. For each of the following expressions, construct the corresponditional logic circuit, using AND and OR gates and INVERTERs.







B > 3-16. For each of the following expressions, construct the corresponditional logic circuit, using AND and OR gates and INVERTERs.



3-22. DRILL QUESTION

Complete each expression.

(a)
$$A + 1 =$$

(b)
$$A \cdot A = \underline{\triangle}$$

(c)
$$B \cdot \overline{B} = \bigcirc$$

(d)
$$C + C = C$$

(e)
$$x \cdot 0 = \bigcirc$$

(f)
$$D \cdot 1 =$$

(g)
$$D + 0 =$$

(h)
$$C + \overline{C} = \underline{\hspace{1cm}}$$

(i)
$$G + GF = G(1+F) = G$$

(j)
$$y + \overline{w}y = \underline{y(1+\overline{w})} = \underline{y}$$

Further exercise:

(ABC)+ (ABO) =1

3-24. (a) Simplify the following expression using theorems (13b), (3), and (4):

$$= (M + N)(M + P)(N + P)$$

$$= (MM + MP + NM + NP) (N + P)$$

$$= MPN + MPP + NMN + NMP + NPN + NPP$$

$$= MPN + NMP$$

$$= MPN + NMP$$

$$= MNP + MNP$$

3-24

(b) Simplify the following expression using theorems (13a), (8), and (6):

$$z = \overline{ABC} + AB\overline{C} + B\overline{C}D$$

$$= (\overline{A} + A) B \overline{C} + B \overline{C}D$$

$$= B \overline{C} (1 + D)$$

$$= B \overline{C}$$

-> 3-26. Simplify each of the following expressions using DeMorgan's the

(a)
$$\overline{ABC}$$
 (d) $\overline{A+B}$ (g) $\overline{A(B+C)D}$

$$= \overline{A} + \overline{B} + \overline{C}$$

$$= \overline{A} + \overline{B} + \overline{C}$$

$$= \overline{A+B+C}$$

$$= \overline{A+B+C} + \overline{D}$$

$$= \overline{A} + \overline{B} + \overline{C} + \overline{D}$$

-> 3-26. Simplify each of the following expressions using DeMorgan's theory

(b)
$$\overline{A} + \overline{BC}$$
 (e) \overline{AB}

$$= \overline{A} \cdot \overline{BC}$$

$$= \overline{A} \cdot (\overline{B} + C) \quad \text{The ans.}$$

$$= \overline{A} \cdot (\overline{B} + C) \quad \text{on } P.557$$

$$= \overline{AB} + \overline{AC} \quad \text{wrong:}$$

$$= \overline{AB} + \overline{AC} \quad ATB$$

$$(M) \overline{(M+M)(M+M)}$$

$$= \overline{(M+N)(M+M)}$$

$$= \overline{(M+N)} + \overline{(M+N)}$$

$$= \overline{(M,N)} + \overline{(M,N)}$$

-> 3-26. Simplify each of the following expressions using DeMorgan's the

(c)
$$\overline{ABCD}$$
 (f) $\overline{A} + \overline{c} + \overline{D}$ (i) \overline{ABCD}

$$= \overline{A} + \overline{B} + \overline{CD} = \overline{A} \cdot \overline{C} \cdot \overline{D} = \overline{ABC} + \overline{D}$$

$$= \overline{A} + \overline{B} + \overline{CD} = \overline{A \cdot C \cdot D} = \overline{ABC} + \overline{D}$$

$$= \overline{A} + \overline{B} + \overline{CD} = \overline{ABC} + \overline{D}$$

$$= \overline{ABCD} = \overline{$$

(i)
$$\overrightarrow{ABCD}$$

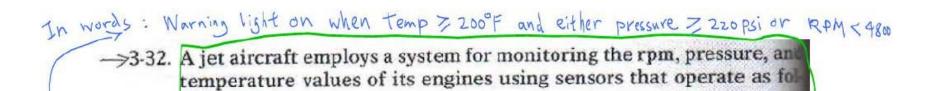
$$= \overrightarrow{ABC} + \overrightarrow{D}$$

$$= \overrightarrow{ABC} + \overrightarrow{D}$$

$$= (\overrightarrow{A+B})C + \overrightarrow{D}$$

$$= \overrightarrow{AC+BC+D}$$

$$= \overrightarrow{AC+BC+D}$$



RPM sensor output = 0 only when speed < 4800 rpm
P sensor output = 0 only when pressure < 220 psi

T sensor output = 0 only when temperature < 200° F

Figure 3-56 shows the logic circuit that controls a cockpit warning light for certain combinations of engine conditions. Assume that a HIGH at output W activates the warning light.

- (a) Determine what engine conditions will give a warning to the pilot
- (b) Change this circuit to one using all NAND gates.

ows:

