

CSC488S Code Generation Templates

The purpose of requiring code templates is to help ensure that you have a complete and correct mapping from the project language to the machine and to help you plan for the code generator implementation in Assignment 5.

The code generation templates should describe in great detail how you plan to map the project language onto the CSC488S pseudo machine. You should describe your overall design strategy and give specific details on each of the categories listed below. Organize your report by the categories listed below, and not by the code generation action numbers.

For each of the items listed below - describe your strategy for implementing the item - give one or more examples of the machine code you expect to generate.

1. Storage

Describe your strategy for allocating storage for and addressing all kinds of variables and constants

- variables in the main program
- variables in procedures and functions
- variables in minor scopes (e.g. '{' declaration statement '}')
- integer and boolean constants (e.g. 'true' and 'false')
- text constants (e.g. "Like This")

2. Expressions

Describe how all forms of expressions will be implemented.

- describe how the values of constants (including text constants) will be accessed.
- describe how the values of scalar variables will be accessed.
- Describe how array elements will be accessed.
Show details of array subscripting in the general case.
- describe how you will implement each of the arithmetic operators +, -, *, and /
- describe how you will implement each of the comparison operators <, <=, =, 'not' =, >=, >
- describe how you will implement each of the boolean operators 'and', 'or', 'not'
- describe how you will implement conditional expressions

3. functions and procedures

Describe how you will implement

- the activation record for functions and procedures.
- procedure and function entrance code (activation record setup, allocation of local variables, etc)
- procedure and function exit code (cleanup, return values, etc)
- describe how you will implement parameter passing.
- describe how you will implement function call and function value return.
- describe how you will implement procedure call
- Describe your display management strategy

4. Statements

Describe the general case of code you will generate for each of the statements in the language.

- a. assignment statement
- b. if statements
- c. the while and repeat statements
- d. all forms of exit statements
- e. return statements
- f. **'read'** and **'write'** statements
- g. handling of minor scopes e.g. '{' declaration statement '}'

5. Everything Else

- a. main program initialization and termination
- b. any handling of scopes not described above
- c. any other information that you think is relevant.

Notes

The design of a complete code generation scheme for mapping a language to a machine has a huge number of moving parts that all have to work well together to produce a correct result. **Consistency** and **Completeness** are hard to achieve in a design this size. There are a lot of degrees of freedom in designing this mapping, choices you make in one area will inevitably affect other areas. You need to clearly and completely describe how the individual parts of your design fit together to make a working whole.

A Suggested Strategy¹

1. Each group member should independently review the entire document, ideally once or twice with a break in between reviews. This will help detect blatant contradictions / inconsistencies across the sections of the document. It's much better to discover these problems before submitting the document for marking.
2. A good Rule of Thumb would be *If everyone understands and agrees on all parts of the document then it is ready to submit*. If anyone doesn't understand any part of the document then either there is a communication problem, a mistake, or the person doesn't understand the machine model.
3. Be explicit about everything! Examples: order of operations/argument evaluation, What happens during function call (e.g. is it the caller or the callee of a function that is responsible for argument evaluation, activation record allocation/deallocation, etc.)

¹Suggested by Peter Goodman, CSC488S TA, Winter 2013/2014