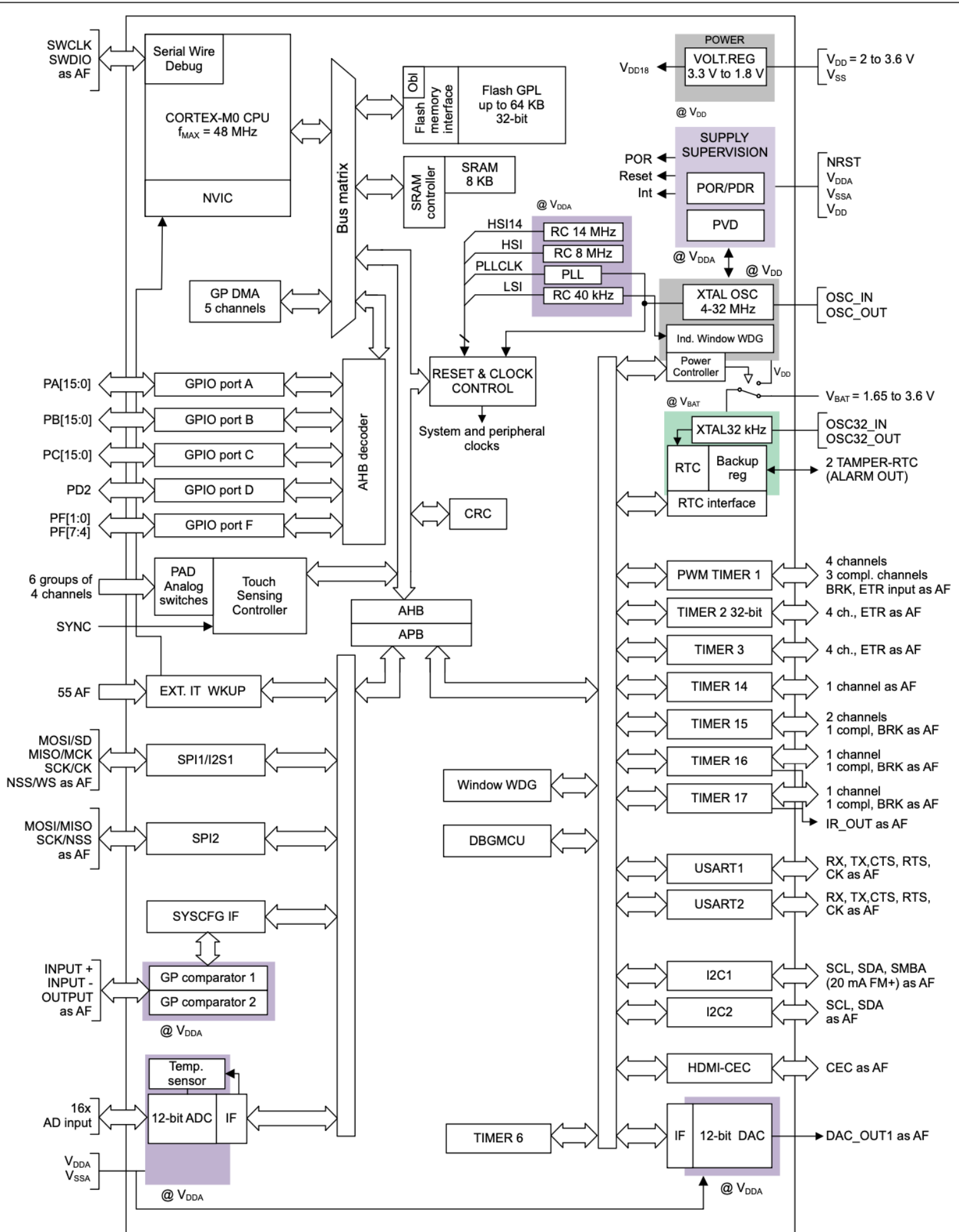


Figure 1. Block diagram



Power domain of analog blocks :

■ V_{BAT} ■ V_{DD} ■ V_{DDA}

Figure 2. Clock tree

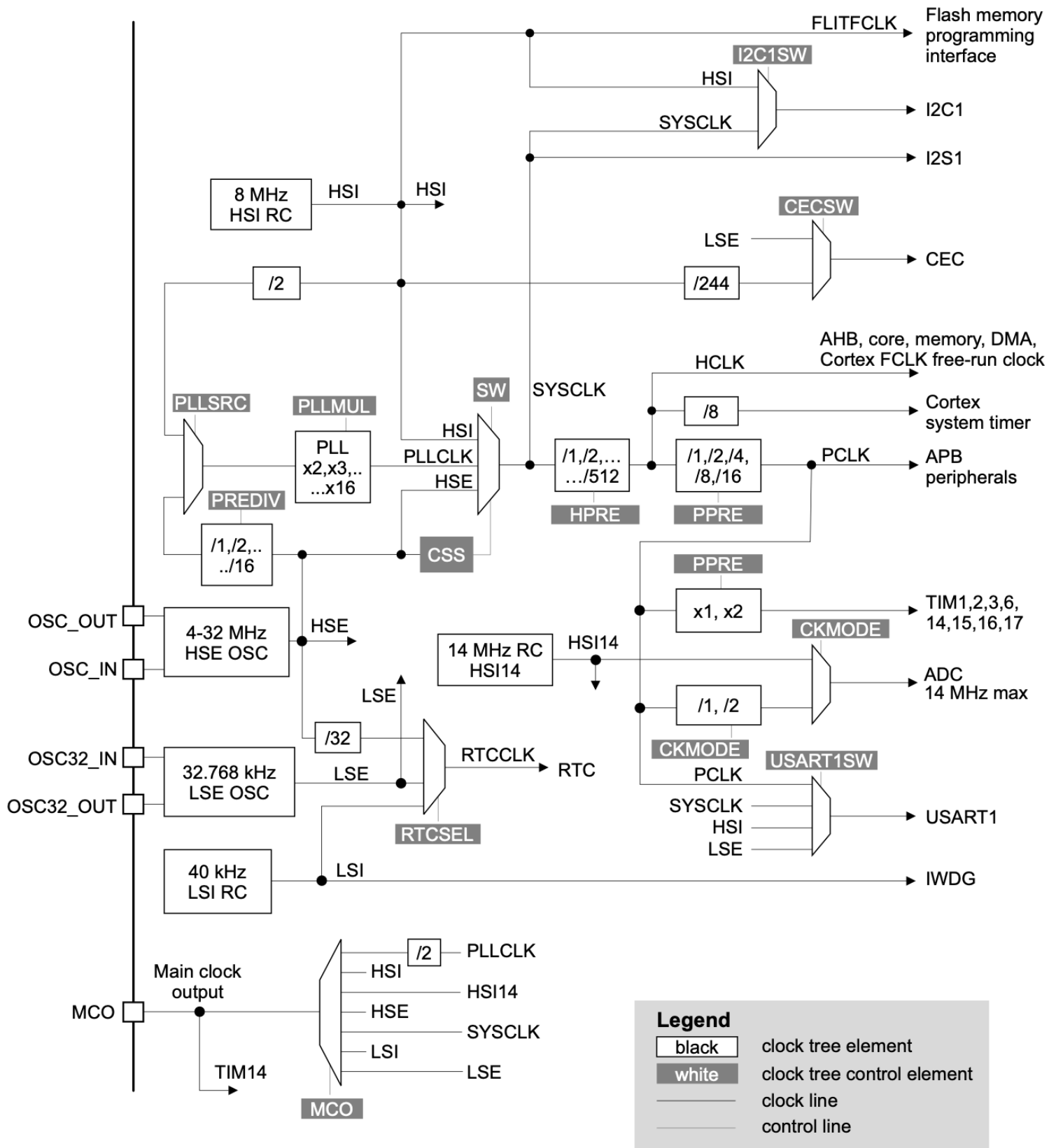


Figure 5. STM32F40xxx block diagram

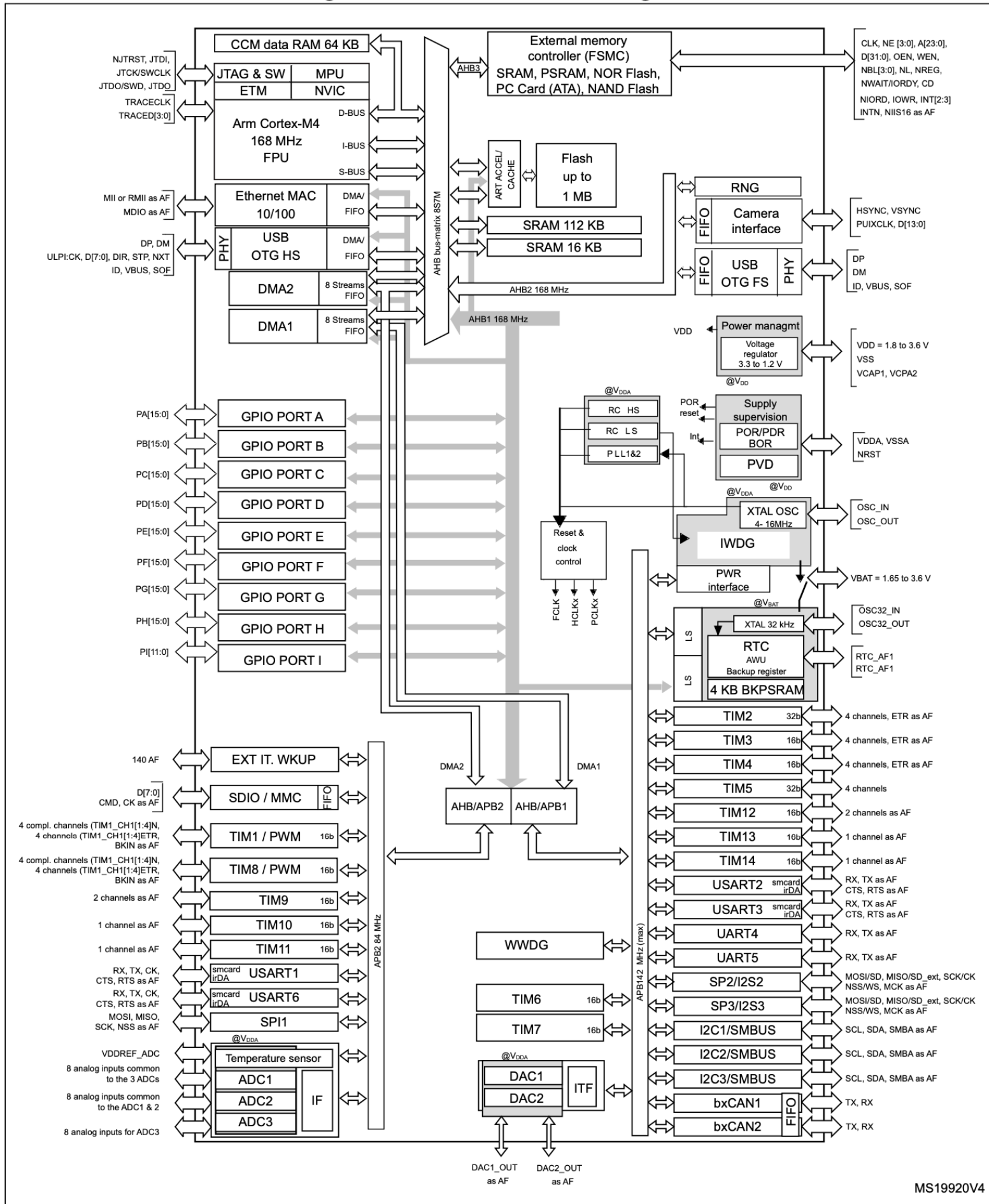


Figure 21. Clock tree

