

Instruction	Type	EX						MEM		WB		
		alumus1_sel	alumus2_sel	alu_op	cmpmux_sel	cmp_op	mem_write	mem_read	pcmux_sel	regfile_sel	load_regfile	mdr_sel
LUI	U-Type	2	1	ADD			0	0	0	2	1	
AUIPC	U-Type	1	1	ADD			0	0	0	1	1	
JAL	J-Type	1	4	ADD			0	0			0	
JALR	J-Type	1	4	ADD			0	0			0	
BEQ	B-Type	1	2	ADD	0	EQ	0	0	1		0	
BNE	B-Type	1	2	ADD	0	NE	0	0	1		0	
BLT	B-Type	1	2	ADD	0	LT	0	0	1		0	
BGE	B-Type	1	2	ADD	0	GE	0	0	1		0	
BLTU	B-Type	1	2	ADD	0	LTU	0	0	1		0	
BGEU	B-Type	1	2	ADD	0	GEU	0	0	1		0	
LB	I-Type	0	0	ADD			0	1		0	1	3
LH	I-Type	0	0	ADD			0	1		0	1	1
LW	I-Type	0	0	ADD			0	1	0	0	1	0
LBU	I-Type	0	0	ADD			0	1		0	1	4
LHU	I-Type	0	0	ADD			0	1		0	1	2
SB	S-Type	0	3	ADD			1	0			0	
SH	S-Type	0	3	ADD			1	0			0	
SW	S-Type	0	3	ADD			1	0	0		0	
ADDI	I-Type	0	0	ADD			0	0	0	2	1	
SLTI	R-Type	0	5	ADD			0	0		2	1	
SLTIU	R-Type	0	5	ADD			0	0		2	1	
XORI	I-Type	0	0	XOR			0	0	0	2	1	
ORI	I-Type	0	0	OR			0	0	0	2	1	
ANDI	I-Type	0	0	AND			0	0	0	2	1	
SLLI	I-Type	0	0	SLL			0	0	0	2	1	
SRLI	I-Type	0	0	SRL			0	0	0	2	1	
SRAI	I-Type	0	0	SRA			0	0		2	1	
ADD	R-Type	0	5	ADD			0	0	0	2	1	
SUB	R-Type	0	5	ADD			0	0		2	1	
SLL	R-Type	0	5	SLL			0	0	0	2	1	
SLT	R-Type	0	5	ADD			0	0		2	1	
SLTU	R-Type	0	5	ADD			0	0		2	1	
XOR	R-Type	0	5	XOR			0	0	0	2	1	
SRL	R-Type	0	5	SRL			0	0	0	2	1	
SRA	R-Type	0	5	SRA			0	0		2	1	
OR	R-Type	0	5	OR			0	0	0	2	1	
AND	R-Type	0	5	AND			0	0	0	2	1	