## POKHARA UNIVERSITY

	Pr	Vel: Bachelor Internal Exam Ogramme: BE Computer (III/I) Ourse: Embedded Systems  Year : 2025 Full Marks: 100 Pass Marks: 45	
	C	Pass Marks: 45 Time : 3hrs.	
et of de		andidates are required to give their answers in their own words as far practicable.	
d	T	ne figures in the margin indicate full marks.	
	A	ttempt all the questions.	
L.	a	Define an embedded system. How does it differ from a general	7
	þy	purpose computing system? Explain in brief.  Explain the architecture of AVR microcontrollers. Highlight its key features.	8
2.	a)	How are I/O ports configured in AVR microcontrollers: Displaced to AVR microcontrollers: Displaced in AVR microcontroller	8
	b)	Write a program to store and retrieve a value from the EEPROM memory of an AVR microcontroller.	7
3.	a)	What is an RTOS, and how does it benefit embedded system applications?	8
	- Jey	Discuss semaphore-based and mutex-based resource sharing techniques in RTOS.	7
1.	a)	What are the primary differences between structural and behavioural modelling styles in VHDL? Explain briefly with some simple	8
	W	examples. Write a VHDL code to implement a full-adder.	7
5.	a)	Implement a Moore state machine for an elevator control system with	7
	b)	states for up, down, and idle.  A smart farming solution includes soil moisture sensors, weather stations, and irrigation controllers connected via an embedded system. Data should be transmitted to a central monitoring unit and accessible via mobile apps.	8
		i. Compare LoRa and Zigbee for long-range, low-power communication in an agricultural setting.	r,
		ii. What are the trade-offs between using wired vs wireless communication in this scenario?	
6.	a)	Write an AVR C program to interface a 16x2 LCD (in 4-bit mode) with an AVR microcontroller (e.g., ATmega16/ATmega32). The program should:	7
		i. Initialize the LCD in 4-bit mode.	

