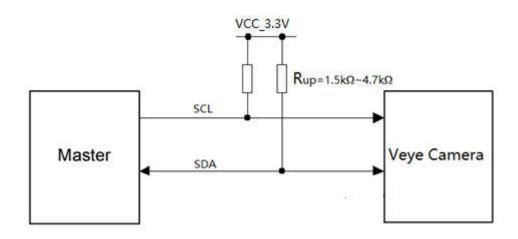


VEYE 系列 200 万像素星光模组控制接口使用指南

概要

VEYE 系列模组 控制接口采用 I2C 串行通信接口,遵循标准 I2C 接口协议(100KHz)。通过该接口,可以对 VEYE 系列模组进行帧率调整、宽动态控制、强制黑白、日夜滤波片切换等相关控制。

设备连接



设备地址

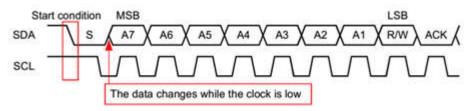
VEYE 系列模组设备地址 SlaveAddress[7:1]=0x7B,即

I	MSB							LSB
ĺ	1	1	1	1	0	1	1	0/1(W/R)

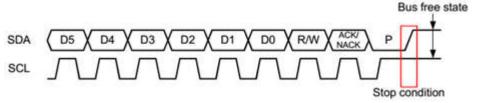
访问协议

控制接口采用 MSB 模式, 高位优先传输。

开始 (Start Condition)

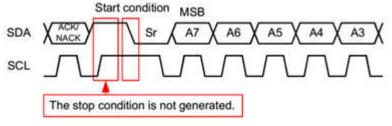


结束 (Stop Condition)

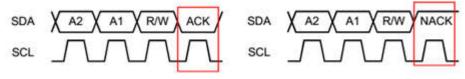


重复开始 (Repeated Start Condition)

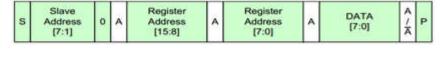




响应与不响应(Acknowledge and Negative Acknowledge)



写随机寄存器 (Single Write to Random Location)



From Master to Slave S : Start Condition A : Acknowledge

From Slave to Master P : Stop Condition A : Negative Acknowledge

读随机寄存器(Single Read to Random Location)

s	Slave Address [7:1]	0 A	Register Address [15:8]	A	Register Address [7:0]	A	Sr	Slave Address [7:1]	1	A	DATA [7:0]	Ā	P	
---	---------------------------	-----	-------------------------------	---	------------------------------	---	----	---------------------------	---	---	---------------	---	---	--

From Master to Slave S : Start Condition A : Acknowledge

Sr : Repeated Start Condition

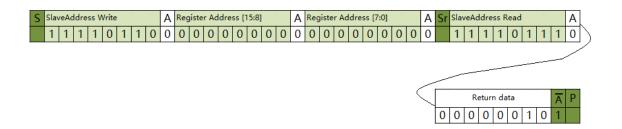
From Slave to Master P : Stop Condition A : Negative Acknowledge

访问范例

1、复位视频流 (Video Stream Reset) 0x0008=0xFE ---> 0x0008=0xFF

S	SlaveAddress Write					A Register Address [15:8]					Α	Register Address [7:0]				Α			٧	Vrite	dat	a		Α	P													
	1	1		1	1	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	1	1	1	1	1	1	1	0	0	
S	S SlaveAddress Write A Register Address [15:8]							Α	Reg	jiste	r Ad	ldre	ss [7	7:0]			Α			٧	Vrite	dat	a			Α	P											
	1	1	-	1	1	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	1	1	1	1	1	1	1	1	0	

2、 获取设备类型, 0x0000 = 0x02(MIPI_2Lane)





设备寄存器表

地址	位	读写特性	寄存器名	位描述	默认
	[7.4]	RO	Davisativas	0x0:VEYE-MIPI-290	
	[7:4]	RO	- DeviceType	0x1:VEYE-LVDS-290	
00000		RO		0x1:1 Lane	0::00
0x0000	[0.0]	RO	D \ A /: -	0x2:2 Lane	0x02
	[3:0]	RO	- DataWidth	0x3:3 Lane	
		RO		0x4:4 Lane	
0x0001	[7:0]	RO	Version	Hardware Version	0x01
	[7:4]	RO	Reserve	Reserve	
	_	DVA	Infrared lamp	1:High Level means lamp On	
	3	RW	Active level	0:Low level means lamp On	
	2	RW	IR-Cut Direction	IR filter Direction	
0x0002	1	D) 4 /	5	1:Force Mode Enable	0x08
	1	RW	ForceMode	0:Force Mode disable	
				When ForceMode=1	
	0	RW	ForceColor	1:Color Mode	
				0:Black-and-White Mode	
	7	RW	txfifo_rstn	VideoTxCmdFIFO reset, low active	
	6	RO	Reserve	Reserve	
0x0003	5	RO	txfifo_full	VideoTxCmdFIFO full	0x9a
	4	RO	txfifo_empty	VideoTxCmdFIFO empty	
	[3:0]	RO	Reserve	Reserve	
0.0007	[7:1]	RO	Reserve	Reserve	0.55
0x0007	0	RW	VideoCtrlDisable	VideoCtrlDisable,high active	OxFE
0.0000	[7:1]	RO	Reserve	Reserve	0.55
8000x0	0	RW	Video Stream Reset	Video Stream Reset, Low Active	OxFF
0.0000	[7:1]	RO	Reserve	Reserve	0.55
0x000B	0	RW	ContinuousClockMode	MIPI Continuous Clock Mode Enable	OxFE
0x0010	[7:0]	RW	VideoRegAddrMSB	Video Control Register Address MSB	0x00
0x0011	[7:0]	RW	VideoRegAddrLSB	Video Control Register Address LSB	0x00
0x0012	[7:0]	RW	VideoRegCfgValue	Write Video Control Register Value	0x00
00010	[7.0]	D\\\'	\/:- D	0x00:Write Command	0.00
0x0013	[7:0]	RW	VideoRegCmd	0x01:Read Command	0x00
0x0014	[7:0]	RW	VideoRegAckValue	Current Video Control Register Value	0x00
Others	[7:0]	RW	Reserve	Reserve	0xFF



视频控制寄存器 (Video Control Register) 表

地址	位	读写 特性	寄存器名	位描述	默认						
	[7:2]	Res erve	Reserve	Reserve							
				Back Light Mode Select							
0xDB32				0x0: OFF	0x00						
	[1:0]	RW	Back Light Mode	0x1: BLC							
				0x2: HBLC							
				0x3: WDR							
	[7:1]	Res erve	Reserve	Reserve							
0xDEC2				FRAME RATE Select	0x00						
	0	RW	FRAME RATE	0x0: 25fps							
				0x1: 30fps							
	[7:2]	Res erve	Reserve	Reserve							
				Mirror Mode							
0xDE57				0x0: Normal	0x00						
	[1:0]	RW	MIRROR_MODE	0x1: Mirror							
				0x2: V-Flip							
				0x3: Mirror And V-Flip(180 Degree Rotate)							
	[7:4]	Res erve	Reserve	Reserve							
				NR 2D Mode							
				0x0: OFF							
	[3:2]	RW	DNR2_MODE	0x1: LOW							
0xD89B				0x2: MIDDLE	0,,00						
UXD89B				0x3: HIGH	0x02						
				NR 3D Mode							
				0x0: OFF							
	[1:0]	RW	DNR3_MODE	0x1: LOW							
				0x2: MIDDLE							
				0x3: HIGH							
	[7:5]	Res erve	Reserve	Reserve							
		1		0x0:1/2							
				0x0:1/2							
0xDA64			LOW LIGHT	0x2:1/6	0x00						
	[4:1]	RW	FRAME RATE Control	0x3:1/8							
				0x4:1/10							
	1			0x5:1/15							



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				0x6:1/20					
				0x7:1/25					
				0x8:1/30					
			LOW LIGHT	0x0: FRAME RATE constant					
	0	RW	FRAME RATE Control	0x1:Low Light FRAME RATE Control Enable					
			FRAME RATE COMITO						
	[7:5]	Res	Reserve	Reserve					
	[7.5]	erve	Ke3elve	1000.10					
0x40EA	4	RW	TestEnable	Output Test Pattern Enable	0x00				
	[3:0]	Res	Reserve	Reserve					
	[0.0]	erve	11030110	Reserve					
	[7:5]	Res	Reserve	Reserve					
	[, .0]	erve	NOSOI VO	Reserve					
				0x0: Horizontal Color Bar					
				0x1: Vertical Color Bar,					
				0x2 : Horizontal Color Plane,					
				0x3 : Vertical Color					
				0x4 : Horizontal B/W Line Bar,					
				0x5 : Vertical B/W Line Bar,					
				0x6 : Outline,					
				0x7 : White Plane,					
				0x8 : Yellow Plane,					
0x40EB				0x9 : Cyan Plane,	0x14				
	[4:0]	RW	Test Pattern	0xA : Green Plane,					
				0xB : Magenta Plane,					
				0xC : Red Plane,					
				0xD : Blue Plane,					
				0xE : Black Plane,					
				0xF: User Color,					
				0x10 : Hatch,					
				0x11 : Horizontal Y Ramp1,					
				0x12: Vertical Y Ramp1,					
				0x13: Horizontal C RAmp1,					
				0x14: Vertical C RAmp1,					
	1	<u> </u>							