TEST CASE

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| **MACHINE CODE** | **MIPS** | **NOTE** |
| 2009002b  ac090000  8c080000  21080002  08000000 | .data  input: .word 2147483647  input2: .word 0  .text  main:  addi $9, $0, 43  sw $9 input  lw $8 input  addi $8 $8 2  j main | test sw |
| 2009002b  ac090000  8c080000  00084400  00084402  2108ffd3  00084043  08000000 | .data  input: .word 2147483647  .text  main:  addi $9, $0, 43  sw $9 input  lw $8 input  sll $8,$8,16  srl $8,$8,16  addi $8,$8,-45  sra $8,$8,1  j main | test shift |
| 20080002  20100001  200a0001  8c090000  11090003  72088002  010a4020  08000004  72098002  02008022  08000000 | .data  input: .word 5  .text  main:  addi $8, $0, 2  addi $16, $0, 1  addi $10, $0, 1  lw $9, input  for:  beq $8, $9, exit  mul $16, $16, $8  add $8, $8, $10  j for  exit:  mul $16, $16, $9  sub $16, $16, $0  j main | Tính giai thừa input |
| 2009002b  2008fffd  0128481a  0128481a  0128481a  08000000 | main:  addi $9, $0, 43  addi $8, $0, -3  div $9, $9, $8  div $9, $9, $8  div $9, $9, $8  j main | test div |
| 2009002b  2008fffd  0120481a  20090002  20080006 | main:  addi $9, $0, 43  addi $8, $0, -3  div $9, $9, $0  addi $9, $0, 2  addi $8, $0, 6 | div/0 |
| 2009002b  8c080002  08000c00 | .data  input1: .half 5  input: .half 5  .text  main:  addi $9, $0, 43  lw $8 input  j main | no aligned |
| 2009002b  12345678 | addi $9, $0, 43  0x12345678 | invalid ins |
| 2009002b  21200001  08000000 | main:  addi $9, $0, 43  addi $0, $9, 1  j main | insert into $0 |