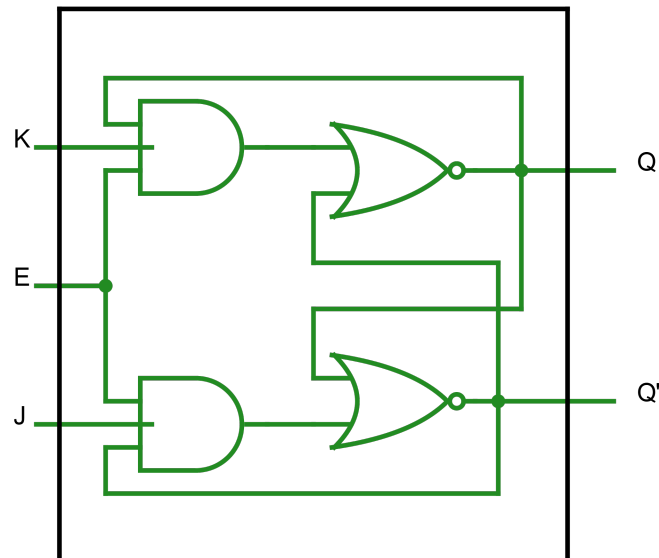
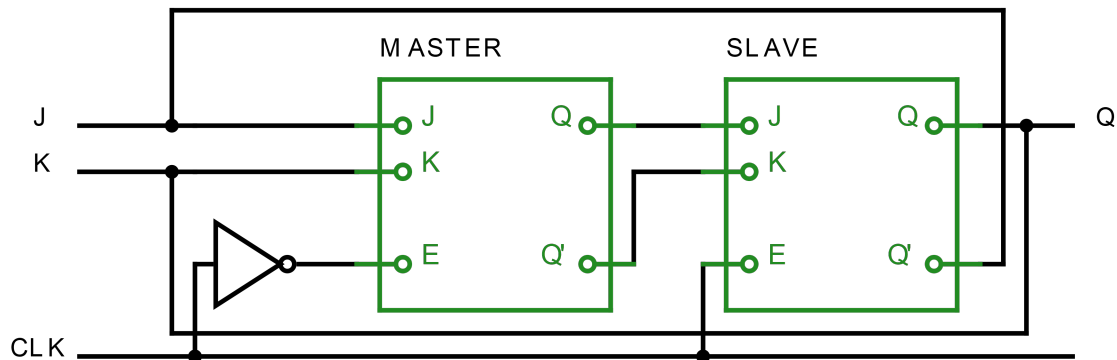
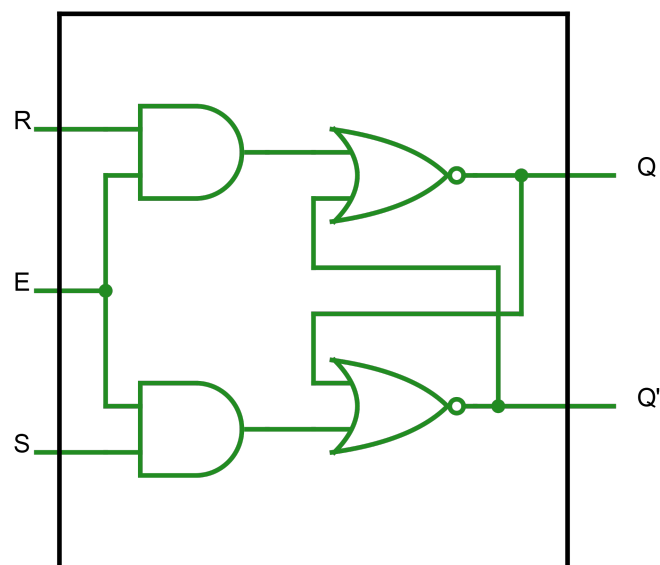
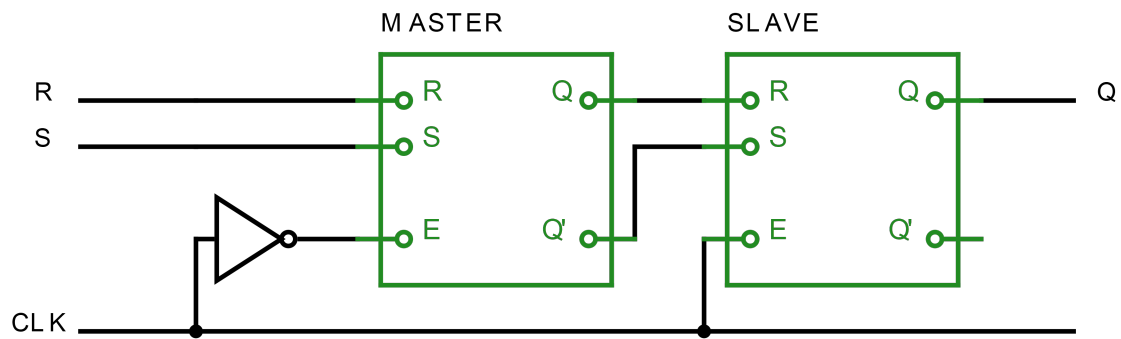


6.Design JK flip-flop using gated JKs (latches).



⁰I wasn't sure if a block diagram was acceptable or not, so the bottom circuit shows the inside of each of the JK latches. Additionally, I was unsure if the flip-flop should be rising, falling, or dual edge, so I designed it to be rising edge.

7.Design SR flip-flop using gated SRs (latches).



⁰Again, unsure about block diagrams so I made a compromise. This is also a rising edge circuit since the instructions were vague.