

# ERA-Tutorium 9

Thomas Kilian

# Mikroinstruktion

- wir haben im letzten Tutorium hauptsächlich die Steuerung der ALU betrachtet
- Weitere Felder:
  - ➔ BZ/IR-Befehle, MWE
  - ➔ CEMUE\*/CEM\*
  - ➔ Statusregistertest
  - ➔ CCEN, AM2910-Befehle
  - ➔ Direktdaten

# Mikroinstruktion

- BZ\_LD: lädt von Datenbus in BZ
- BZ\_ED: lädt BZ nach Datenbus
- BZ\_INC: inkrementiert BZ
- BZ\_EA: lädt BZ nach Adressbus
- IR\_LD: lädt Datenbus in IR
- MWE\*: steuert Zugriff auf den Speicher (lesend/schreibend)

# Wichtig für MI:

ÜBEN ÜBEN ÜBEN!

Am besten jetzt schon, nicht erst kurz vor der Klausur

# Aufgabe 1.1

Welche der oben angegebenen Befehle belegen 16 Bit, welche 32 Bit im Hauptspeicher?  
Bitte die jeweiligen Opcodes angeben!

# Aufgabe 1.2

Assemblieren des angegebenen  
Maschinenprogramms

Programm liegt ab 0x0100 im Speicher

# Aufgabe 1.3

- Implementieren Sie folgende Maschinenbefehle
  - ➔ JMP addr
  - ➔ INC RB
  - ➔ CMP [RA], RB
  - ➔ JNE addr

# JMP addr

79	78	77	76	75	74	73	72	71	70	69	68	67	66	65	64	63	62	61	60	59	58	57	56	55	54	53	52	51	50	49	48	47	46	45	44	43	42	41	40	39	38	37	36	35	34	33	32	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
IE	I3	I2	I1	I0	KMUX	K15	K14	K13	K12	K11	K10	K9	K8	K7	K6	K5	K4	K3	K2	K1	K0	I2	I1	I0	I5	I4	I3	I2	I1	I0	A3	A2	A1	A0	ASEL	B3	B2	B1	B0	BSEL	ADUS*	DBUS*	I12	I11	I9	I8	I7	I6	CENUE*	CEN*	I5	I4	I3	I2	I1	I0	CCEN*	I3	I2	I1	I0	D11	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0	BZ_LD*	BZ_ED*	BZ_INC*	BZ_EA*	IR_LD*	MWB*
Interrupt					Konstante																			Src		Func		Dest		RA Addr		RB Addr				Y- MUX		CIN MUX		Schiebe- steuerung		Statusregister Test				AM2910 Befehle				Direktdaten																													

JMP adr		
040	D15	
041	D15	



# JMP addr

IE	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Interrupt	Konstante	Src	Func	Dest	RA Addr	RB Addr	Y-	CIN	Schiebe- steuerung	Statusregister Test	AM2910 Befehle	Direktadressen		
							MUX	MUX						

JMP adr	
040	DIS: X X X X X NOP X X X H H X X H H X X CONT X H H H E H R
041	DIS: [REDACTED]

# JMP addr

IE	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Interrupt	Konstante	Src	Func	Dest	RA Addr	RB Addr	Y-	CIN	Schiebe- steuerung	Statusregister Test	AM2910 Befehle	Direktadressen		
							MUX	MUX						

JMP adr																																	
040	DIS	X	X		X		X	X	NOP	X	X	X	X	H	H	X	X	H	H	X		X	CONT		X		H	H	H	E	H	R	
041	DIS	X	X		X		X	X	NOP	X	X	X	X	H	H	X	X	H	H	X		X	P S	CJP		IFETCH		L	H	H	H	H	R

# INC RB

[illegible]

## INC RB

050

# INC RB

76	75	74	73	72	71	70	69	68	67	66	65	64	63	62	61	60	59	58	57	56	55	54	53	52	51	50	49	48	47	46	45	44	43	42	41	40	39	38	37	36	35	34	33	32	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0			
IE	I3	I2	I1	I0	KMUX	K15	K14	K13	K12	K11	K10	K9	K8	K7	K6	K5	K4	K3	K2	K1	K0	I2	I1	I0	I5	I4	I3	I2	I1	I0	A3	A2	A1	A0	ASEL	B3	B2	B1	B0	BSEL	AHUS*	EHUS*	I12	I11	I9	I8	I7	I6	CENQUE*	CENM*	I3	I4	I3	I2	I1	I0	CCEN*	I3	I2	I1	I0	D11	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0	HZ_LD*	HZ_FD*	HZ_INC*	HZ_EA*	IR_11*	MVE*
Interrupt					Konstante																	Src		Func		Dest		RA Addr				RB Addr				Y-MUX		CIN-MUX		Schiebesteuerng		Statusregister Test				AM2910 Befehle				Direktdaten																													

## INC RB

050	DS	X	X	X	ZB	ADD	RAMF	X	X	X	I	R	H	H	C11	X	H	H	X	P	S	CJP	IFETCH	H	H	H	H	H	R
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# CMP [RA], RB

[illegible]

**CMP [RA],RB**

060	DIS	
061	DIS	
062	DIS	

# CMP [RA], RB

[illegible]

**CMP [RA],RB**

060	DIS	X	X		X		ZA	ADD	NOP	X	I	X	X	A	H	ClO	X	H	H	X	X	CONT		X		H	H	H	H	H	R
061	DIS																														
062	DIS																														

# CMP [RA], RB

[illegible]

**CMP [RA],RB**

060	DIS	X	X		X		ZA	ADD	NOP	X	I R	X	X	A B	H	ClO	X	H	H	X	X	CONT		X		H	H	H	H	H	R
061	DIS	X	D		X		DZ	ADD	RAMF	X	X	15	M R	H	H	ClO	X	H	H	X	X	CONT		X		H	H	H	H	H	R
062	DIS																														

# CMP [RA], RB

[illegible]

**CMP [RA],RB**

060	DIS	X	X		X	ZA	ADD	NOP	X	I R	X	X	A B	H	CI0	X	H	H	X	X	CONT		X		H	H	H	H	H	R
061	DIS	X	D		X	DZ	ADD	RAMF	X	X	15	M R	H	H	CI0	X	H	H	X	X	CONT		X		H	H	H	H	H	R
062	DIS	X	X		X	AB	SUBS	NOP	15	M R	X	I R	H	H	CI1	X	H	L	LOAD	P S	CJP		IFETCH		H	H	H	H	H	R



# JNE addr

70	78	77	76	75	74	73	72	71	70	69	68	67	66	65	64	63	62	61	60	59	58	57	56	55	54	53	52	51	50	49	48	47	46	45	44	43	42	41	40	39	38	37	36	35	34	33	32	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																														
IE	I1	I2	I3	I4	I5	I6	I7	I8	I9	I10	I11	I12	I13	I14	I15	I16	I17	I18	I19	I20	I21	I22	I23	I24	I25	I26	I27	I28	I29	I30	I31	I32	I33	I34	I35	I36	I37	I38	I39	I40	I41	I42	I43	I44	I45	I46	I47	I48	I49	I50	I51	I52	I53	I54	I55	I56	I57	I58	I59	I60	I61	I62	I63	I64	I65	I66	I67	I68	I69	I70	I71	I72	I73	I74	I75	I76	I77	I78	I79	I80	I81	I82	I83	I84	I85	I86	I87	I88	I89	I90	I91	I92	I93	I94	I95	I96	I97	I98	I99	I100	I101	I102	I103	I104	I105	I106	I107	I108	I109	I110	I111	I112	I113	I114	I115	I116	I117	I118	I119	I120	I121	I122	I123	I124	I125	I126	I127	I128	I129	I130	I131	I132	I133	I134	I135	I136	I137	I138	I139	I140	I141	I142	I143	I144	I145	I146	I147	I148	I149	I150	I151	I152	I153	I154	I155	I156	I157	I158	I159	I160	I161	I162	I163	I164	I165	I166	I167	I168	I169	I170	I171	I172	I173	I174	I175	I176	I177	I178	I179	I180	I181	I182	I183	I184	I185	I186	I187	I188	I189	I190	I191	I192	I193	I194	I195	I196	I197	I198	I199	I200	I201	I202	I203	I204	I205	I206	I207	I208	I209	I210	I211	I212	I213	I214	I215	I216	I217	I218	I219	I220	I221	I222	I223	I224	I225	I226	I227	I228	I229	I230	I231	I232	I233	I234	I235	I236	I237	I238	I239	I240	I241	I242	I243	I244	I245	I246	I247	I248	I249	I250	I251	I252	I253	I254	I255	I256	I257	I258	I259	I260	I261	I262	I263	I264	I265	I266	I267	I268	I269	I270	I271	I272	I273	I274	I275	I276	I277	I278	I279	I280	I281	I282	I283	I284	I285	I286	I287	I288	I289	I290	I291	I292	I293	I294	I295	I296	I297	I298	I299	I300	I301	I302	I303	I304	I305	I306	I307	I308	I309	I310	I311	I312	I313	I314	I315	I316	I317	I318	I319	I320	I321	I322	I323	I324	I325	I326	I327	I328	I329	I330	I331	I332	I333	I334	I335	I336	I337	I338	I339	I340	I341	I342	I343	I344	I345	I346	I347	I348	I349	I350	I351	I352	I353	I354	I355	I356	I357	I358	I359	I360	I361	I362	I363	I364	I365	I366	I367	I368	I369	I370	I371	I372	I373	I374	I375	I376	I377	I378	I379	I380	I381	I382	I383	I384	I385	I386	I387	I388	I389	I390	I391	I392	I393	I394	I395	I396	I397	I398	I399	I400	I401	I402	I403	I404	I405	I406	I407	I408	I409	I410	I411	I412	I413	I414	I415	I416	I417	I418	I419	I420	I421	I422	I423	I424	I425	I426	I427	I428	I429	I430	I431	I432	I433	I434	I435	I436	I437	I438	I439	I440	I441	I442	I443	I444	I445	I446	I447	I448	I449	I450	I451	I452	I453	I454	I455	I456	I457	I458	I459	I460	I461	I462	I463	I464	I465	I466	I467	I468	I469	I470	I471	I472	I473	I474	I475	I476	I477	I478	I479	I480	I481	I482	I483	I484	I485	I486	I487	I488	I489	I490	I491	I492	I493	I494	I495	I496	I497	I498	I499	I500	I501	I502	I503	I504	I505	I506	I507	I508	I509	I510	I511	I512	I513	I514	I515	I516	I517	I518	I519	I520	I521	I522	I523	I524	I525	I526	I527	I528	I529	I530	I531	I532	I533	I534	I535	I536	I537	I538	I539	I540	I541	I542	I543	I544	I545	I546	I547	I548	I549	I550	I551	I552	I553	I554	I555	I556	I557	I558	I559	I560	I561	I562	I563	I564	I565	I566	I567	I568	I569	I570	I571	I572	I573	I574	I575	I576	I577	I578	I579	I580	I581	I582	I583	I584	I585	I586	I587	I588	I589	I590	I591	I592	I593	I594	I595	I596	I597	I598	I599	I600	I601	I602	I603	I604	I605	I606	I607	I608	I609	I610	I611	I612	I613	I614	I615	I616	I617	I618	I619	I620	I621	I622	I623	I624	I625	I626	I627	I628	I629	I630	I631	I632	I633	I634	I635	I636	I637	I638	I639	I640	I641	I642	I643	I644	I645	I646	I647	I648	I649	I650	I651	I652	I653	I654	I655	I656	I657	I658	I659	I660	I661	I662	I663	I664	I665	I666	I667	I668	I669	I670	I671	I672	I673	I674	I675	I676	I677	I678	I679	I680	I681	I682	I683	I684	I685	I686	I687	I688	I689	I690	I691	I692	I693	I694	I695	I696	I697	I698	I699	I700	I701	I702	I703	I704	I705	I706	I707	I708	I709	I710	I711	I712	I713	I714	I715	I716	I717	I718	I719	I720	I721	I722	I723	I724	I725	I726	I727	I728	I729	I730	I731	I732	I733	I734	I735	I736	I737	I738	I739	I740	I741	I742	I743	I744	I745	I746	I747	I748	I749	I750	I751	I752	I753	I754	I755	I756	I757	I758	I759	I760	I761	I762	I763	I764	I765	I766	I767	I768	I769	I770	I771	I772	I773	I774	I775	I776	I777	I778	I779	I780	I781	I782	I783	I784	I785	I786	I787	I788	I789	I790	I791	I792	I793	I794	I795	I796	I797	I798	I799	I800	I801	I802	I803	I804	I805	I806	I807	I808	I809	I810	I811	I812	I813	I814	I815	I816	I817	I818	I819	I820	I821	I822	I823	I824	I825	I826	I827	I828	I829	I830	I831	I832	I833	I834	I835	I836	I837	I838	I839	I840	I841	I842	I843	I844	I845	I846	I847	I848	I849	I850	I851	I852	I853	I854	I855	I856	I857	I858	I859	I860	I861	I862	I863	I864	I865	I866	I867	I868	I869	I870	I871	I872	I873	I874	I875	I876	I877	I878	I879	I880	I881	I882	I883	I884	I885	I886	I887	I888	I889	I890	I891	I892	I893	I894	I895	I896	I897	I898	I899	I900	I901	I902	I903	I904	I905	I906	I907	I908	I909	I910	I911	I912	I913	I914	I915	I916	I917	I918	I919	I920	I921	I922	I923	I924	I925	I926	I927	I928	I929	I930	I931	I932	I933	I934	I935	I936	I937	I938	I939	I940	I941	I942	I943	I944	I945	I946	I947	I948	I949	I950	I951	I952	I953	I954	I955	I956	I957	I958	I959	I960	I961	I962	I963	I964	I965	I966	I967	I968	I969	I970	I971	I972	I973	I974	I975	I976	I977	I978	I979	I980	I981	I982	I983	I984	I985	I986	I987	I988	I989	I990	I991	I992	I993	I994	I995	I996	I997	I998	I999	I1000	I1001	I1002	I1003	I1004	I1005	I1006	I1007	I1008	I1009	I1010	I1011	I1012	I1013	I1014	I1015	I1016	I1017	I1018	I1019	I1020	I1021	I1022	I1023	I1024	I1025	I1026	I1027	I1028	I1029	I1030	I1031	I1032	I1033	I1034	I1035	I1036	I1037	I1038	I1039	I1040	I1041	I1042	I1043	I1044	I1045	I1046	I1047	I1048	I1049	I1050	I1051	I1052	I1053	I1054	I1055	I1056	I1057	I1058	I1059	I1060	I1061	I1062	I1063	I1064	I1065	I1066	I1067	I1068	I1069	I1070	I1071	I1072	I1073	I1074	I1075	I1076	I1077	I1078	I1079	I1080	I1081	I1082	I1083	I1084	I1085	I1086	I1087	I1088	I1089	I1090	I1091	I1092	I1093	I1094	I1095	I1096	I1097	I1098	I1099	I1100	I1101	I1102	I1103	I1104	I1105	I1106	I1107	I1108	I1109	I1110	I1111	I1112	I1113	I1114	I1115	I1116	I1117	I1118	I1119	I1120	I1121	I1122	I1123	I1124	I1125	I1126	I1127	I1128	I1129	I1130	I1131	I1132	I1133	I1134	I1135	I1136	I1137	I1138	I1139	I1140	I1141	I1142	I1143	I1144	I1145	I1146	I1147	I1148	I1149	I1150	I1151	I1152	I1153	I1154	I1155	I1156	I1157	I1158	I1159	I1160	I1161	I1162	I1163	I1164	I1165	I1166	I1167	I1168	I1169	I1170	I1171	I1172	I1173	I1174	I1175	I1176	I1177	I1178	I1179	I1180	I1181	I1182	I1183	I1184	I1185	I1186	I1187	I1188	I1189	I1190	I1191	I1192	I1193	I1194	I1195	I1196	I1197	I1198	I1199	I1200	I1201	I1202	I1203	I1204	I1205	I1206	I1207	I1208	I1209	I1210	I1211	I1212	I1213	I1214	I1215	I1216	I1217	I1218	I1219	I1220	I1221	I1222	I1223	I1224	I1225	I1226	I1227	I1228	I1229	I1230	I1231	I1232	I1233	I1234	I1235	I1236	I1237	I1238	I1239	I1240	I1241	I1242	I1243	I1244	I1245	I1246	I1247	I1248	I1249	I1250	I1251	I1252	I1253	I1254	I1255	I1256	I1257	I1258	I1259	I1260	I1261	I1262	I1263	I1264	I1265	I1266	I1267	I1268	I1269	I1270	I1271	I1272	I1273	I1274	I1275	I1276	I1277	I1278	I1279	I1280	I1281	I1282	I1283	I1284	I1285	I1286	I1287	I1288	I1289	I1290	I1291	I1292	I1293	I1294	I1295	I1296	I1297	I1298	I1299	I1300	I1301	I1302	I1303	I1304	I1305	I1306	I1307	I1308	I1309	I1310	I1311	I1312	I1313	I1314	I1315	I1316	I1317	I1318	I1319	I1320	I1321	I1322	I1323	I1324	I1325	I1326	I1327	I1328	I1329	I1330	I1331	I1332	I1333	I1334	I1335	I1336	I1337	I1338	I1339	I1340	I1341	I1342	I1343	I1344	I1345	I1346	I1347	I1348	I1349	I1350	I1351	I1352	I1353	I1354	I1355	I1356	I1357	I1358	I1359	I1360	I1361	I1362	I1363	I1364	I1365	I1366	I1367	I1368	I1369	I1370	I1371	I1372	I1373	I1374	I1375	I1376	I1377	I1378	I1379	I1380	I1381	I1382	I1383	I1384	I1385	I1386	I1387	I1388	I1389	I1390	I1391	I1392	I1393	I1394	I1395	I1396	I1397	I1398	I1399	I1400	I1401	I1402	I1403	I1404	I1405	I1406	I1407	I1408	I1409	I1410	I1411	I1412	I1413	I1414	I1415	I1416	I1417	I1418	I1419	I1420	I1421	I1422	I1423	I1424	I1425	I1426	I1427	I1428	I1429	I1430	I1431	I1432	I1433	I1434	I1435	I1436	I1

JNE adr (In Test statt „NEQ“ auch „NotZero“ möglich)

030	DIS	
031	DIS	
032	DIS	
033	DIS	

# JNE addr

70	78	77	76	75	74	73	72	71	70	69	68	67	66	65	64	63	62	61	60	59	58	57	56	55	54	53	52	51	50	49	48	47	46	45	44	43	42	41	40	39	38	37	36	35	34	33	32	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
IE	I3	I2	I1	I0	KMUX	K15	K14	K13	K12	K11	K10	K9	K8	K7	K6	K5	K4	K3	K2	K1	K0	L2	L1	L0	L5	L4	L3	L2	L1	L0	A3	A2	A1	A0	ASEL	B3	B2	B1	B0	BSEL	ABUS*	DBUS*	I12	I11	I9	I8	I7	I6	CEMUE*	CEM*	L3	L4	L3	L2	L1	L0	CCEN*	D3	D2	D1	D0	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0	HZ_LD*	HZ_ED*	HZ_INC*	HZ_EA*	IR_I17*	MWB*	
Interrupt					Konstante																		Src	Func	Dest	RA Addr	RB Addr					Y-MUX	CIN MUX	Schiebesteu- erung	Statusregister Test					AM2910 Befehle					Direktdaten																																		

JNE adr (In Test statt „NEQ“ auch „NotZero“ möglich)

030	DIS	X	X	X	X	X	NOP	X	X	X	X	H	H	CI0	X	H	H	MA-NEQ	C	CJP	032	H	H	H	H	R
031	DIS																									
032	DIS																									
033	DIS																									

# JNE addr

70	78	77	76	75	74	73	72	71	70	69	68	67	66	65	64	63	62	61	60	59	58	57	56	55	54	53	52	51	50	49	48	47	46	45	44	43	42	41	40	39	38	37	36	35	34	33	32	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
IE	I3	I2	I1	I0	KMUX	K15	K14	K13	K12	K11	K10	K9	K8	K7	K6	K5	K4	K3	K2	K1	K0	L2	L1	L0	L5	L4	L3	L2	L1	L0	A3	A2	A1	A0	ASEL	B3	B2	B1	B0	BSEL	ABUS*	DBUS*	I12	I11	I9	I8	I7	I6	CEMUE*	CEM*	L3	L4	L3	L2	L1	L0	CCEN*	D3	D2	D1	D0	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0	HZ_LD*	HZ_ED*	HZ_INC*	HZ_EA*	IR_I17*	MWB*	
Interrupt					Konstante																		Src	Func	Dest	RA Addr	RB Addr					Y-MUX	CIN MUX	Schiebesteu- erung	Statusregister Test					AM2910 Befehle					Direktdaten																																		

JNE adr (In Test statt „NEQ“ auch „NotZero“ möglich)

030	DIS	X	X	X	X	X	NOP	X	X	X	X	H	H	CI0	X	H	H	MA-NEQ	C	CJP	032	H	H	H	H	R
031	DIS	X	X	X	X	X	NOP	X	X	X	X	H	H	CI0	X	H	H	X	P S	CJP	IFETCH	H	H	I	H	R
032	DIS																									
033	DIS																									

# JNE addr

70	78	77	76	75	74	73	72	71	70	69	68	67	66	65	64	63	62	61	60	59	58	57	56	55	54	53	52	51	50	49	48	47	46	45	44	43	42	41	40	39	38	37	36	35	34	33	32	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
IE	I3	I2	I1	I0	KMUX	K15	K14	K13	K12	K11	K10	K9	K8	K7	K6	K5	K4	K3	K2	K1	K0	L2	L1	L0	L5	L4	L3	L2	L1	L0	A3	A2	A1	A0	ASEL	B3	B2	B1	B0	BSEL	ABUS*	DBUS*	I12	I11	I9	I8	I7	I6	CEMUE*	CEM*	L3	L4	L3	L2	L1	L0	CCEN*	D3	D2	D1	D0	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0	HZ_LD*	HZ_ED*	HZ_INC*	HZ_EA*	IR_I17*	MWB*	
Interrupt					Konstante																		Src	Func	Dest	RA Addr	RB Addr				Y-MUX	CIN MUX	Schiebesteu- erung	Statusregister Test				AM2910 Befehle				Direktdaten																																					

JNE adr (In Test statt „NEQ“ auch „NotZero“ möglich)

030	D15	X	X	X	X	NOP	X	X	X	X	H	H	CI0	X	H	H	MA-NEQ	C	CJP	032	H	H	H	H	R
031	D15	X	X	X	X	NOP	X	X	X	X	H	H	CI0	X	H	H	X	P S	CJP	IFETCH	H	H	I	H	R
032	D15	X	X	X	X	NOP	X	X	X	X	H	H	CI0	X	H	H	X	X	CONT	X	H	H	H	E	R
033	D15																								

# JNE addr

70	78	77	76	75	74	73	72	71	70	69	68	67	66	65	64	63	62	61	60	59	58	57	56	55	54	53	52	51	50	49	48	47	46	45	44	43	42	41	40	39	38	37	36	35	34	33	32	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
IE	I3	I2	I1	I0	KMUX	K15	K14	K13	K12	K11	K10	K9	K8	K7	K6	K5	K4	K3	K2	K1	K0	L2	L1	L0	L5	L4	L3	L2	L1	L0	A3	A2	A1	A0	ASEL	B3	B2	B1	B0	BSEL	ABUS*	DBUS*	I12	I11	I9	I8	I7	I6	CEMUE*	CEM*	L3	L4	L3	L2	L1	L0	CCEN*	D3	D2	D1	D0	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0	HZ_LD*	HZ_ED*	HZ_INC*	HZ_EA*	IR_I17*	MWB*	
Interrupt					Konstante																	Src	Func	Dest	RA Addr	RB Addr				Y-MUX	CIN MUX	Schiebesteu- erung	Statusregister Test				AM2910 Befehle				Direktdaten																																						

JNE adr (In Test statt „NEQ“ auch „NotZero“ möglich)

030	D15	X	X	X	X	NOP	X	X	X	X	H	H	CI0	X	H	H	MA-NEQ	C	CJP	032	H	H	H	H	H	R
031	D15	X	X	X	X	NOP	X	X	X	X	H	H	CI0	X	H	H	X	P	CJP	IFETCH	H	H	I	H	H	R
032	D15	X	X	X	X	NOP	X	X	X	X	H	H	CI0	X	H	H	X	X	CONT	X	H	H	H	E	H	R
033	D15	X	X	X	X	NOP	X	X	X	X	H	H	CI0	X	H	H	X	P	CJP	IFETCH	L	H	H	H	H	R

# Aufgabe 2

- Implementieren Sie *strcmp* *RA*, *RB*
  - ➔ Opcode: 42
  - ➔ vergleicht zwei Strings ab Adressen *RA* und *RB*
  - ➔ Zeichenketten terminieren mit Null-Byte (`\0`)
  - ➔ setzt Z-Flag, wenn Strings gleich

# STRCMP RA, RB

IE	I3	I2	I1	I0	KMUX	K15	K14	K13	K12	K11	K10	K9	K8	K7	K6	K5	K4	K3	K2	K1	K0	G7	G6	G5	G4	G3	G2	G1	G0	S9	S8	S7	S6	S5	S4	S3	S2	S1	S0	A9	A8	A7	A6	A5	A4	A3	A2	A1	A0	ASEL	B3	B2	B1	B0	RSEL	ABUS*	EBUS*	I12	I11	I9	I8	I7	I6	CEMUP*	CEMP*	I5	I4	I3	I2	I1	I0	CCEN*	D3	D2	D1	D0	D5	D4	D3	D2	D1	D0	RZ_LD*	RZ_ED*	RZ_INC*	RZ_EA*	IR_LD*	MWE*
Interrupt					Konstante																Src	Func	Dest	RA Addr				RB Addr				Y-MUX	CIN-MUX	Schiebesteu- erung	Statusregister Test				AM2910 Befehle				Direktdaten																																													

Adr. **STRCMP RA,RB**

Speichere RA nach r14 (weil RA nicht inkrementiert werden kann)

	X	X	X		X	H	H	X	X	CONT	X	H	H	H	H	H	R
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**Speichere [r14] nach r15. Vergleiche [RB] mit r15.**

	DIS	X	X	X		X	H	H	X	X	CONT	X	
	DIS	X	D	X		X	H	H	X	X	CONT	X	
	DIS	X	X	X		X	H	H	X	X	CONT	X	
	DIS	X	D	X		X				X	CONT	X	

Springe zu IFETCH, wenn [r14] ungleich [RB]

DIS	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X		IFETCH	H	H	H	H	H	R
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Springe zu IFETCH, wenn r15 gleich 0

	DIS	X	X	X											X		X	CONT	X	H	H	H	H	H	R
	DIS	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X		IFETCH	H	H	H	H	H	R		

Inkrementiere r14 und RB, weiter

	DIS	X	X	X		X	H	H	X	X	CONT	X	H	H	H	H	H	R
	DIS	X	X	X		X	H	H	X				H	H	H	H	H	R

# STRCMP RA, RB

[illegible]

Adr. **STRCMP RA,RB**

Speichere RA nach r14 (weil RA nicht inkrementiert werden kann)

420	X	X	X		X	H	H	X	X	CONT	X	H	H	H	H	H	R
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**Speichere [r14] nach r15. Vergleiche [RB] mit r15.**

	DIS	X	X	X		X	H	H	X	X	CONT	X	
	DIS	X	D	X		X	H	H	X	X	CONT	X	
	DIS	X	X	X		X	H	H	X	X	CONT	X	
	DIS	X	D	X		X				X	CONT	X	

Springe zu IFETCH, wenn [r14] ungleich [RB]

	DES	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X		IFETCH	H	H	H	H	H	R
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Springe zu IFETCH, wenn r15 gleich 0

	DIS	X	X	X											X		X	CONT	X	H	H	H	H	H	R
	DIS	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X		IFETCH	H	H	H	H	H	R		

Inkrementiere r14 und RB, weiter

	DIS	X	X	X		X	H	H	X	X	CONT	X	H	H	H	H	H	R
	DIS	X	X	X		X	H	H	X				H	H	H	H	H	R



# STRCMP RA, RB

76	75	74	73	72	71	70	69	68	67	66	65	64	63	62	61	60	59	58	57	56	55	54	53	52	51	50	49	48	47	46	45	44	43	42	41	40	39	38	37	36	35	34	33	32	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0			
IE	I3	I2	I1	I0	KMUX	K15	K14	K13	K12	K11	K10	K9	K8	K7	K6	K5	K4	K3	K2	K1	K0	G7	G6	G5	G4	G3	G2	G1	G0	A3	A2	A1	A0	ASEL	E3	E2	E1	E0	RSEL	ABUS*	EBUS*	I12	I11	I10	I9	I8	I7	I6	CEMUJ*	CEM*	I5	I4	I3	I2	I1	I0	CCEN*	D3	D2	D1	D0	D11	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0	BZ_LD*	BZ_ED*	BZ_INC*	BZ_EA*	TR_LD*	MWE*
Interrupt					Konstante																Src		Func		Dest		RA Addr		RB Addr		Y-MUX		CIN-MUX		Schiebesteu- erung				Statusregister Test				AM2910 Befehle		Direktdaten																																		

Adr. **STRCMP RA,RB**

Speichere RA nach r14 (weil RA nicht inkrementiert werden kann)

420	SIX	X	X	X	ZA	ADD	RAMA	X	I R	14	M R	H	H	CIO	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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**Speichere [r14] nach r15. Vergleiche [RB] mit r15.**

	DIS	X	X	X		X	H	H	X	X	CONT	X	
	DIS	X	D	X		X	H	H	X	X	CONT	X	
	DIS	X	X	X		X	H	H	X	X	CONT	X	
	DIS	X	D	X		X				X	CONT	X	

Springe zu IFETCH, wenn [r14] ungleich [RB]

DIS	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X		IFETCH	H	H	H	H	H	R
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Springe zu IFETCH, wenn r15 gleich 0

	DIS	X	X	X											X		X	CONT	X	H	H	H	H	H	R
	DIS	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X		IFETCH	H	H	H	H	H	R		

Inkrementiere r14 und RB, weiter

	DIS	X	X	X		X	H	H	X	X	CONT	X	H	H	H	H	H	R
	DIS	X	X	X		X	H	H	X				H	H	H	H	H	R

# STRCMP RA, RB

76	75	74	73	72	71	70	69	68	67	66	65	64	63	62	61	60	59	58	57	56	55	54	53	52	51	50	49	48	47	46	45	44	43	42	41	40	39	38	37	36	35	34	33	32	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0			
IE	I3	I2	I1	I0	KMUX	K15	K14	K13	K12	K11	K10	K9	K8	K7	K6	K5	K4	K3	K2	K1	K0	G7	G6	G5	G4	G3	G2	G1	G0	A3	A2	A1	A0	ASEL	E3	E2	E1	E0	RSEL	ABUS*	EBUS*	I12	I11	I10	I9	I8	I7	I6	CEMUJ*	CEM*	I5	I4	I3	I2	I1	I0	CCEN*	D3	D2	D1	D0	D11	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0	BZ_LD*	BZ_ED*	BZ_INC*	BZ_EA*	TR_LD*	MWE*
Interrupt					Konstante																Src		Func		Dest		RA Addr		RB Addr		Y-MUX		CIN-MUX		Schiebesteu- erung				Statusregister Test				AM2910 Befehle		Direktdaten																																		

Adr. **STRCMP RA,RB**

Speichere RA nach r14 (weil RA nicht inkrementiert werden kann)

420	516	X	X	X	ZA	ADD	RAMA	X	I R	14	M R	H	H	C10	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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Speichere [r14] nach r15. Vergleiche [RB] mit r15.

421	E1S	X	X	X		X	H	H	X	X	CONT	X	
	E1S	X	D	X		X	H	H	X	X	CONT	X	
	E1S	X	X	X		X	H	H	X	X	CONT	X	
	E1S	X	D	X		X				X	CONT	X	

Springe zu IFETCH, wenn [r14] ungleich [RB]

	DIS	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X		IFETCH	H	H	H	H	H	R
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Springe zu IFETCH, wenn r15 gleich 0

	DIS	X	X	X											X		X	CONT	X	H	H	H	H	H	R
	DIS	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X		IFETCH	H	H	H	H	H	R		

Inkrementiere r14 und RB, weiter

	DIS	X	X	X		X	H	H	X	X	CONT	X	H	H	H	H	H	R
	DIS	X	X	X		X	H	H	X				H	H	H	H	H	R

# STRCMP RA, RB

76	77	75	74	73	72	71	70	69	68	67	66	65	64	63	62	61	60	59	58	57	56	55	54	53	52	51	50	49	48	47	46	45	44	43	42	41	40	39	38	37	36	35	34	33	32	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0							
IE	I3	I2	I1	I0	KMUX	K15	K14	K13	K12	K11	K10	K9	K8	K7	K6	K5	K4	K3	K2	K1	K0	G7	G6	G5	G4	G3	G2	G1	G0	A9	A8	A7	A6	A5	A4	A3	A2	A1	A0	ASEL	B3	B2	B1	B0	RSEL	ABUS*	EBUS*	I12	I11	I9	I8	I7	I6	CEMUP*	CMP*	C5	C4	C3	C2	C1	C0	CCEN*	D3	D2	D1	D0	E11	E10	E9	E8	E7	E6	E5	E4	E3	E2	E1	E0	FZ_LD*	FZ_ED*	FZ_INC*	FZ_EA*	IR_LD*	MWE*
Interrupt					Konstante																Src		Func		Dest		RA Addr		RB Addr		Y-MUX		CIN MUX		Schiebesteu- erung		Statusregister Test		AM2910 Befehle		Direktdaten																																											

Adr. **STRCMP RA,RB**

Speichere RA nach r14 (weil RA nicht inkrementiert werden kann)

420	SI	X	X	X	ZA	ADD	RAMA	X	I	14	M	H	H	C/D	X	H	H	X	X	CONT	X	H	H	H	H	R
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**Speichere [r14] nach r15. Vergleiche [RB] mit r15.**

421	DIS	X	X	X	ZB	ADD	NOP	X	X	14	M R	A B	H	C/O	X	H	H	X	X	CONT	X	
	DIS	X	D	X											X	H	H	X	X	CONT	X	
	DIS	X	X	X											X	H	H	X	X	CONT	X	
	DIS	X	D	X											X				X	CONT	X	

Springe zu IFETCH, wenn [r14] ungleich [RB]

DIS	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X		IFETCH	H	H	H	H	H	R
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Springe zu IFETCH, wenn r15 gleich 0

	DIS	X	X	X											X		X	CONT	X	H	H	H	H	H	R
	DIS	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X		IFETCH	H	H	H	H	H	R		

Inkrementiere r14 und RB, weiter

	DIS	X	X	X		X	H	H	X	X	CONT	X	H	H	H	H	H	R
	DIS	X	X	X		X	H	H	X				H	H	H	H	H	R

# STRCMP RA, RB

[illegible]

Adr. **STRCMP RA,RB**

Speichere RA nach r14 (weil RA nicht inkrementiert werden kann)

420	SI	X	X	X	ZA	ADD	RAMA	X	I	14	M	H	H	C/D	X	H	H	X	X	CONT	X	H	H	H	H	R
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**Speichere [r14] nach r15. Vergleiche [RB] mit r15.**

421	DIS	X	X	X	ZB	ADD	NOP	X	X	14	M R	A B	H	C/O	X	H	H	X	X	CONT	X	H	H	H	H	H	R
	DIS	X	D	X											X	H	H	X	X	CONT	X						
	DIS	X	X	X											X	H	H	X	X	CONT	X						
	DIS	X	D	X											X				X	CONT	X						

Springe zu IFETCH, wenn [r14] ungleich [RB]

DIS	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X		IFETCH	H	H	H	H	H	R
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Springe zu IFETCH, wenn r15 gleich 0

	DIS	X	X	X											X		X	CONT	X	H	H	H	H	H	R
	DIS	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X		IFETCH	H	H	H	H	H	R		

Inkrementiere r14 und RB, weiter

	DIS	X	X	X		X	H	H	X	X	CONT	X	H	H	H	H	H	R
	DIS	X	X	X		X	H	H	X				H	H	H	H	H	R



# STRCMP RA, RB

76	75	74	73	72	71	70	69	68	67	66	65	64	63	62	61	60	59	58	57	56	55	54	53	52	51	50	49	48	47	46	45	44	43	42	41	40	39	38	37	36	35	34	33	32	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0								
IE	I1	I2	I1	I0	KMUX	K15	K14	K13	K12	K11	K10	K9	K8	K7	K6	K5	K4	K3	K2	K1	K0	I2	I1	I0	I5	I4	I3	I8	I7	I6	A3	A2	A1	A0	ASEL	B3	B2	B1	B0	RSFL	ABUS*	EBUS*	I12	I11	I9	I8	I7	I6	CEMUF*	CEMP*	I5	I4	I3	I2	I1	I0	CCEN*	I3	I2	I1	I0	D11	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	DC	BZ_LD*	BZ_ED*	BZ_INC*	BZ_EA*	IR_LD*	MWE*					
Interrupt					Konstante										Src					Func					Dest					RA Addr					RB Addr					Y-MUX					CIN-MUX					Schiebesteu- erung					Statusregister Test					AM2910 Befehle					Direktdaten																			

Adr. **STRCMP RA,RB**

Speichere RA nach r14 (weil RA nicht inkrementiert werden kann)

420	SK	X	X	X	ZA	ADD	RAMA	X	I R	14	M R	H	H	C/O	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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Speichere [r14] nach r15. Vergleiche [RB] mit r15.

421		X	X	X	ZB	ADD	NOP	X	X	14	M R	A B	H	C/O	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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422	X	D	X		X	H	H	X	X	CONT	X	
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	X	X	X		X	H	H	X	X	CONT	X	
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	DIS	X	D	X		X		X	CONT	X	
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Springe zu IFETCH, wenn [r14] ungleich [RB]

DIS	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X		IFETCH	H	H	H	H	H	R
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Springe zu IFETCH, wenn r15 gleich 0

	DIS	X	X	X		X		X	CONT	X	H	H	H	H	H	R
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	DIS	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X		IFETCH	H	H	H	H	H	R
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Inkrementiere r14 und RB, weiter

	510	X	X	X		X	H	H	X	X	CONT	X	H	H	H	H	H	R
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	DIS	X	X	X		X	H	H	X		H	H	H	H	H	R
--	-----	---	---	---	--	---	---	---	---	--	---	---	---	---	---	---

# STRCMP RA, RB

76	75	74	73	72	71	70	69	68	67	66	65	64	63	62	61	60	59	58	57	56	55	54	53	52	51	50	49	48	47	46	45	44	43	42	41	40	39	38	37	36	35	34	33	32	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0																											
IE	I3	I2	I1	I0	K[MUX]																K15	K14	K13	K12	K11	K10	K9	K8	K7	K6	K5	K4	K3	K2	K1	K0	G7																G6	G5	G4	G3	G2	G1	G0	A3	A2	A1	A0	ASEL	E3	E2	E1	E0	RSEL	Y-MUX	CIN-MUX	Schiebesteu- erung	CEMUJ*	CEM*	Y	E4	E3	E2	E1	E0	CCEN*	D3	D2	D1	D0	D11	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0	EZ_LD*	EZ_ED*	EZ_INC*	EZ_EA*	TR_LD*	MWE*
Interrupt					Konstante																Src		Func		Dest		RA Addr		RB Addr		Y-MUX		CIN-MUX		Schiebesteu- erung				Statusregister Test				AM2910 Befehle		Direktdaten																																																										

Adr. **STRCMP RA,RB**

Speichere RA nach r14 (weil RA nicht inkrementiert werden kann)

420	516	X	X	X	ZA	ADD	RAMA	X	I R	14	M R	H	H	CIO	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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**Speichere [r14] nach r15. Vergleiche [RB] mit r15.**

421	STC	X	X	X	ZB	ADD	NOP	X	X	14	M R	A B	H	C/O	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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422	X	D	X	DZ	ADD	RAMA	X	X	15	M R	H	H	C/O	X	H	H	X	X	CONT	X	
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	DES	X	X	X		X	H	H	X	X	CONT	X	
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	DIS	X	D	X		X		X	CONT	X	
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Springe zu IFETCH, wenn [r14] ungleich [RB]

DIS	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X		IFETCH	H	H	H	H	H	R
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Springe zu IFETCH, wenn r15 gleich 0

	DIS	X	X	X		X		X	CONT	X	H	H	H	H	H	R
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	MS	X	X	X	X	X	NOP	X	X	X	X	H	H	H	X	X		IFETCH	H	H	H	H	H	R
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Inkrementiere r14 und RB, weiter

	X	X	X		X	H	H	X	X	CONT	X	H	H	H	H	H	R
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	SIC	X	X	X		X	H	H	X		H	H	H	H	H	R
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# STRCMP RA, RB

76	77	75	74	73	72	71	70	69	68	67	66	65	64	63	62	61	60	59	58	57	56	55	54	53	52	51	50	49	48	47	46	45	44	43	42	41	40	39	38	37	36	35	34	33	32	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0											
IE	I3	I2	I1	I0	KM_MUX	K15	K14	K13	K12	K11	K10	K9	K8	K7	K6	K5	K4	K3	K2	K1	K0	G7	G6	G5	G4	G3	G2	G1	G0	S9	S8	S7	S6	S5	S4	S3	S2	S1	S0	A9	A8	A7	A6	A5	A4	A3	A2	A1	A0	ASEL	B3	B2	B1	B0	RSEL	ABUS*	EBUS*	I12	I11	I9	I8	I7	I6	CEMUP*	CEMP*	I5	I4	I3	I2	I1	I0	CCEN*	D3	D2	D1	D0	D5	D4	D3	D2	D1	D0	RZ_LD*	RZ_ED*	RZ_INC*	RZ_EA*	IR_LD*	MWE*
Interrupt					Konstante																Src	Func	Dest	RA Addr				RB Addr				Y-MUX	CIN-MUX	Schiebe- steuerung		Statusregister Test				AM2910 Befehle				Direktdaten																																												

Adr. **STRCMP RA,RB**

Speichere RA nach r14 (weil RA nicht inkrementiert werden kann)

420	X	X	X	ZA	ADD	RAMA	X	I R	14	M R	H	H	C	0	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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Speichere [r14] nach r15. Vergleiche [RB] mit r15.

421	218	X	X	X	ZB	ADD	NOP	X	X	14	M R	A B	H	CIO	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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422		X	D	X	DZ	ADD	RAMA	X	X	15	M R	H	H	C	0	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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	DES	X	X	X		X	H	H	X	X	CONT	X	
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	DIS	X	D	X		X		X	CONT	X	
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Springe zu IFETCH, wenn [r14] ungleich [RB]

00000000	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X		IFETCH	H	H	H	H	H	R
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Springe zu IFETCH, wenn r15 gleich 0

	DIS	X	X	X		X		X	CONT	X	H	H	H	H	H	R
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DIS	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X		IFETCH	H	H	H	H	R
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Inkrementiere  $r_{14}$  und  $RB_1$  weiter

	DIS	X	X	X		X	H	H	X	X	CONT	X	H	H	H	H	H	R
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	DIS	X	X	X		X	H	H	X		H	H	H	H	H	R
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# STRCMP RA, RB

I0	I1	I2	I3	I4	I5	I6	I7	I8	I9	I10	I11	I12	I13	I14	I15	I16	I17	I18	I19	I20	I21	I22	I23	I24	I25	I26	I27	I28	I29	I30	I31	I32	I33	I34	I35	I36	I37	I38	I39	I40	I41	I42	I43	I44	I45	I46	I47	I48	I49	I50	I51	I52	I53	I54	I55	I56	I57	I58	I59	I60	I61	I62	I63	I64	I65	I66	I67	I68	I69	I70	I71	I72	I73	I74	I75	I76	I77	I78	I79	I80	I81	I82	I83	I84	I85	I86	I87	I88	I89	I90	I91	I92	I93	I94	I95	I96	I97	I98	I99	I100	I101	I102	I103	I104	I105	I106	I107	I108	I109	I110	I111	I112	I113	I114	I115	I116	I117	I118	I119	I120	I121	I122	I123	I124	I125	I126	I127	I128	I129	I130	I131	I132	I133	I134	I135	I136	I137	I138	I139	I140	I141	I142	I143	I144	I145	I146	I147	I148	I149	I150	I151	I152	I153	I154	I155	I156	I157	I158	I159	I160	I161	I162	I163	I164	I165	I166	I167	I168	I169	I170	I171	I172	I173	I174	I175	I176	I177	I178	I179	I180	I181	I182	I183	I184	I185	I186	I187	I188	I189	I190	I191	I192	I193	I194	I195	I196	I197	I198	I199	I200	I201	I202	I203	I204	I205	I206	I207	I208	I209	I210	I211	I212	I213	I214	I215	I216	I217	I218	I219	I220	I221	I222	I223	I224	I225	I226	I227	I228	I229	I230	I231	I232	I233	I234	I235	I236	I237	I238	I239	I240	I241	I242	I243	I244	I245	I246	I247	I248	I249	I250	I251	I252	I253	I254	I255	I256	I257	I258	I259	I260	I261	I262	I263	I264	I265	I266	I267	I268	I269	I270	I271	I272	I273	I274	I275	I276	I277	I278	I279	I280	I281	I282	I283	I284	I285	I286	I287	I288	I289	I290	I291	I292	I293	I294	I295	I296	I297	I298	I299	I300	I301	I302	I303	I304	I305	I306	I307	I308	I309	I310	I311	I312	I313	I314	I315	I316	I317	I318	I319	I320	I321	I322	I323	I324	I325	I326	I327	I328	I329	I330	I331	I332	I333	I334	I335	I336	I337	I338	I339	I340	I341	I342	I343	I344	I345	I346	I347	I348	I349	I350	I351	I352	I353	I354	I355	I356	I357	I358	I359	I360	I361	I362	I363	I364	I365	I366	I367	I368	I369	I370	I371	I372	I373	I374	I375	I376	I377	I378	I379	I380	I381	I382	I383	I384	I385	I386	I387	I388	I389	I390	I391	I392	I393	I394	I395	I396	I397	I398	I399	I400	I401	I402	I403	I404	I405	I406	I407	I408	I409	I410	I411	I412	I413	I414	I415	I416	I417	I418	I419	I420	I421	I422	I423	I424	I425	I426	I427	I428	I429	I430	I431	I432	I433	I434	I435	I436	I437	I438	I439	I440	I441	I442	I443	I444	I445	I446	I447	I448	I449	I450	I451	I452	I453	I454	I455	I456	I457	I458	I459	I460	I461	I462	I463	I464	I465	I466	I467	I468	I469	I470	I471	I472	I473	I474	I475	I476	I477	I478	I479	I480	I481	I482	I483	I484	I485	I486	I487	I488	I489	I490	I491	I492	I493	I494	I495	I496	I497	I498	I499	I500	I501	I502	I503	I504	I505	I506	I507	I508	I509	I510	I511	I512	I513	I514	I515	I516	I517	I518	I519	I520	I521	I522	I523	I524	I525	I526	I527	I528	I529	I530	I531	I532	I533	I534	I535	I536	I537	I538	I539	I540	I541	I542	I543	I544	I545	I546	I547	I548	I549	I550	I551	I552	I553	I554	I555	I556	I557	I558	I559	I560	I561	I562	I563	I564	I565	I566	I567	I568	I569	I570	I571	I572	I573	I574	I575	I576	I577	I578	I579	I580	I581	I582	I583	I584	I585	I586	I587	I588	I589	I590	I591	I592	I593	I594	I595	I596	I597	I598	I599	I600	I601	I602	I603	I604	I605	I606	I607	I608	I609	I610	I611	I612	I613	I614	I615	I616	I617	I618	I619	I620	I621	I622	I623	I624	I625	I626	I627	I628	I629	I630	I631	I632	I633	I634	I635	I636	I637	I638	I639	I640	I641	I642	I643	I644	I645	I646	I647	I648	I649	I650	I651	I652	I653	I654	I655	I656	I657	I658	I659	I660	I661	I662	I663	I664	I665	I666	I667	I668	I669	I670	I671	I672	I673	I674	I675	I676	I677	I678	I679	I680	I681	I682	I683	I684	I685	I686	I687	I688	I689	I690	I691	I692	I693	I694	I695	I696	I697	I698	I699	I700	I701	I702	I703	I704	I705	I706	I707	I708	I709	I710	I711	I712	I713	I714	I715	I716	I717	I718	I719	I720	I721	I722	I723	I724	I725	I726	I727	I728	I729	I730	I731	I732	I733	I734	I735	I736	I737	I738	I739	I740	I741	I742	I743	I744	I745	I746	I747	I748	I749	I750	I751	I752	I753	I754	I755	I756	I757	I758	I759	I760	I761	I762	I763	I764	I765	I766	I767	I768	I769	I770	I771	I772	I773	I774	I775	I776	I777	I778	I779	I780	I781	I782	I783	I784	I785	I786	I787	I788	I789	I790	I791	I792	I793	I794	I795	I796	I797	I798	I799	I800	I801	I802	I803	I804	I805	I806	I807	I808	I809	I810	I811	I812	I813	I814	I815	I816	I817	I818	I819	I820	I821	I822	I823	I824	I825	I826	I827	I828	I829	I830	I831	I832	I833	I834	I835	I836	I837	I838	I839	I840	I841	I842	I843	I844	I845	I846	I847	I848	I849	I850	I851	I852	I853	I854	I855	I856	I857	I858	I859	I860	I861	I862	I863	I864	I865	I866	I867	I868	I869	I870	I871	I872	I873	I874	I875	I876	I877	I878	I879	I880	I881	I882	I883	I884	I885	I886	I887	I888	I889	I890	I891	I892	I893	I894	I895	I896	I897	I898	I899	I900	I901	I902	I903	I904	I905	I906	I907	I908	I909	I910	I911	I912	I913	I914	I915	I916	I917	I918	I919	I920	I921	I922	I923	I924	I925	I926	I927	I928	I929	I930	I931	I932	I933	I934	I935	I936	I937	I938	I939	I940	I941	I942	I943	I944	I945	I946	I947	I948	I949	I950	I951	I952	I953	I954	I955	I956	I957	I958	I959	I960	I961	I962	I963	I964	I965	I966	I967	I968	I969	I970	I971	I972	I973	I974	I975	I976	I977	I978	I979	I980	I981	I982	I983	I984	I985	I986	I987	I988	I989	I990	I991	I992	I993	I994	I995	I996	I997	I998	I999	I1000	I1001	I1002	I1003	I1004	I1005	I1006	I1007	I1008	I1009	I1010	I1011	I1012	I1013	I1014	I1015	I1016	I1017	I1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Adr. **STRCMP RA,RB**

Speichere RA nach r14 (weil RA nicht inkrementiert werden kann)

420	X	X	X	ZA	ADD	RAMA	X	I R	14	M R	H	H	C	0	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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**Speichere [r14] nach r15. Vergleiche [RB] mit r15.**

421	DIS	X	X	X	ZB	ADD	NOP	X	X	14	M R	A B	H	C 0	X	H	H	X	X	CONT	X	H	H	H	H	H	R
422	DIS	X	D	X	DZ	ADD	RAMA	X	X	15	M R	H	H	C 0	X	H	H	X	X	CONT	X	H	H	H	H	H	R
423	DIS	X	X	X											X	H	H	X	X	CONT	X						
	DIS	X	D	X											X				X	CONT	X						

**Springe zu IFETCH, wenn [r14] ungleich [RB]**

	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X		IFETCH	H	H	H	H	H	R
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Springe zu IFETCH, wenn r15 gleich 0

	DIS	X	X	X											X		X	CONT	X	H	H	H	H	H	R
	DIS	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X		IFETCH	H	H	H	H	H	R		

Inkrementiere  $r_{14}$  und  $RB_1$  weiter

	DIS	X	X	X		X	H	H	X	X	CONT	X	H	H	H	H	H	H
	DIS	X	X	X		X	H	H	X				H	H	H	H	H	H



# STRCMP RA, RB

I0	I1	I2	I3	I4	I5	I6	I7	I8	I9	I10	I11	I12	I13	I14	I15	I16	I17	I18	I19	I20	I21	I22	I23	I24	I25	I26	I27	I28	I29	I30	I31	I32	I33	I34	I35	I36	I37	I38	I39	I40	I41	I42	I43	I44	I45	I46	I47	I48	I49	I50	I51	I52	I53	I54	I55	I56	I57	I58	I59	I60	I61	I62	I63	I64	I65	I66	I67	I68	I69	I70	I71	I72	I73	I74	I75	I76	I77	I78	I79	I80	I81	I82	I83	I84	I85	I86	I87	I88	I89	I90	I91	I92	I93	I94	I95	I96	I97	I98	I99	I100	I101	I102	I103	I104	I105	I106	I107	I108	I109	I110	I111	I112	I113	I114	I115	I116	I117	I118	I119	I120	I121	I122	I123	I124	I125	I126	I127	I128	I129	I130	I131	I132	I133	I134	I135	I136	I137	I138	I139	I140	I141	I142	I143	I144	I145	I146	I147	I148	I149	I150	I151	I152	I153	I154	I155	I156	I157	I158	I159	I160	I161	I162	I163	I164	I165	I166	I167	I168	I169	I170	I171	I172	I173	I174	I175	I176	I177	I178	I179	I180	I181	I182	I183	I184	I185	I186	I187	I188	I189	I190	I191	I192	I193	I194	I195	I196	I197	I198	I199	I200	I201	I202	I203	I204	I205	I206	I207	I208	I209	I210	I211	I212	I213	I214	I215	I216	I217	I218	I219	I220	I221	I222	I223	I224	I225	I226	I227	I228	I229	I230	I231	I232	I233	I234	I235	I236	I237	I238	I239	I240	I241	I242	I243	I244	I245	I246	I247	I248	I249	I250	I251	I252	I253	I254	I255	I256	I257	I258	I259	I260	I261	I262	I263	I264	I265	I266	I267	I268	I269	I270	I271	I272	I273	I274	I275	I276	I277	I278	I279	I280	I281	I282	I283	I284	I285	I286	I287	I288	I289	I290	I291	I292	I293	I294	I295	I296	I297	I298	I299	I300	I301	I302	I303	I304	I305	I306	I307	I308	I309	I310	I311	I312	I313	I314	I315	I316	I317	I318	I319	I320	I321	I322	I323	I324	I325	I326	I327	I328	I329	I330	I331	I332	I333	I334	I335	I336	I337	I338	I339	I340	I341	I342	I343	I344	I345	I346	I347	I348	I349	I350	I351	I352	I353	I354	I355	I356	I357	I358	I359	I360	I361	I362	I363	I364	I365	I366	I367	I368	I369	I370	I371	I372	I373	I374	I375	I376	I377	I378	I379	I380	I381	I382	I383	I384	I385	I386	I387	I388	I389	I390	I391	I392	I393	I394	I395	I396	I397	I398	I399	I400	I401	I402	I403	I404	I405	I406	I407	I408	I409	I410	I411	I412	I413	I414	I415	I416	I417	I418	I419	I420	I421	I422	I423	I424	I425	I426	I427	I428	I429	I430	I431	I432	I433	I434	I435	I436	I437	I438	I439	I440	I441	I442	I443	I444	I445	I446	I447	I448	I449	I450	I451	I452	I453	I454	I455	I456	I457	I458	I459	I460	I461	I462	I463	I464	I465	I466	I467	I468	I469	I470	I471	I472	I473	I474	I475	I476	I477	I478	I479	I480	I481	I482	I483	I484	I485	I486	I487	I488	I489	I490	I491	I492	I493	I494	I495	I496	I497	I498	I499	I500	I501	I502	I503	I504	I505	I506	I507	I508	I509	I510	I511	I512	I513	I514	I515	I516	I517	I518	I519	I520	I521	I522	I523	I524	I525	I526	I527	I528	I529	I530	I531	I532	I533	I534	I535	I536	I537	I538	I539	I540	I541	I542	I543	I544	I545	I546	I547	I548	I549	I550	I551	I552	I553	I554	I555	I556	I557	I558	I559	I560	I561	I562	I563	I564	I565	I566	I567	I568	I569	I570	I571	I572	I573	I574	I575	I576	I577	I578	I579	I580	I581	I582	I583	I584	I585	I586	I587	I588	I589	I590	I591	I592	I593	I594	I595	I596	I597	I598	I599	I600	I601	I602	I603	I604	I605	I606	I607	I608	I609	I610	I611	I612	I613	I614	I615	I616	I617	I618	I619	I620	I621	I622	I623	I624	I625	I626	I627	I628	I629	I630	I631	I632	I633	I634	I635	I636	I637	I638	I639	I640	I641	I642	I643	I644	I645	I646	I647	I648	I649	I650	I651	I652	I653	I654	I655	I656	I657	I658	I659	I660	I661	I662	I663	I664	I665	I666	I667	I668	I669	I670	I671	I672	I673	I674	I675	I676	I677	I678	I679	I680	I681	I682	I683	I684	I685	I686	I687	I688	I689	I690	I691	I692	I693	I694	I695	I696	I697	I698	I699	I700	I701	I702	I703	I704	I705	I706	I707	I708	I709	I710	I711	I712	I713	I714	I715	I716	I717	I718	I719	I720	I721	I722	I723	I724	I725	I726	I727	I728	I729	I730	I731	I732	I733	I734	I735	I736	I737	I738	I739	I740	I741	I742	I743	I744	I745	I746	I747	I748	I749	I750	I751	I752	I753	I754	I755	I756	I757	I758	I759	I760	I761	I762	I763	I764	I765	I766	I767	I768	I769	I770	I771	I772	I773	I774	I775	I776	I777	I778	I779	I780	I781	I782	I783	I784	I785	I786	I787	I788	I789	I790	I791	I792	I793	I794	I795	I796	I797	I798	I799	I800	I801	I802	I803	I804	I805	I806	I807	I808	I809	I810	I811	I812	I813	I814	I815	I816	I817	I818	I819	I820	I821	I822	I823	I824	I825	I826	I827	I828	I829	I830	I831	I832	I833	I834	I835	I836	I837	I838	I839	I840	I841	I842	I843	I844	I845	I846	I847	I848	I849	I850	I851	I852	I853	I854	I855	I856	I857	I858	I859	I860	I861	I862	I863	I864	I865	I866	I867	I868	I869	I870	I871	I872	I873	I874	I875	I876	I877	I878	I879	I880	I881	I882	I883	I884	I885	I886	I887	I888	I889	I890	I891	I892	I893	I894	I895	I896	I897	I898	I899	I900	I901	I902	I903	I904	I905	I906	I907	I908	I909	I910	I911	I912	I913	I914	I915	I916	I917	I918	I919	I920	I921	I922	I923	I924	I925	I926	I927	I928	I929	I930	I931	I932	I933	I934	I935	I936	I937	I938	I939	I940	I941	I942	I943	I944	I945	I946	I947	I948	I949	I950	I951	I952	I953	I954	I955	I956	I957	I958	I959	I960	I961	I962	I963	I964	I965	I966	I967	I968	I969	I970	I971	I972	I973	I974	I975	I976	I977	I978	I979	I980	I981	I982	I983	I984	I985	I986	I987	I988	I989	I990	I991	I992	I993	I994	I995	I996	I997	I998	I999	I1000	I1001	I1002	I1003	I1004	I1005	I1006	I1007	I1008	I1009	I1010	I1011	I1012	I1013	I1014	I1015	I1016	I1017	I1018	I1019	I1020	I1021	I1022	I1023	I1024	I1025	I1026	I1027	I1028	I1029	I1030	I1031	I1032	I1033	I1034	I1035	I1036	I1037	I1038	I1039	I1040	I1041	I1042	I1043	I1044	I1045	I1046	I1047	I1048	I1049	I1050	I1051	I1052	I1053	I1054	I1055	I1056	I1057	I1058	I1059	I1060	I1061	I1062	I1063	I1064	I1065	I1066	I1067	I1068	I1069	I1070	I1071	I1072	I1073	I1074	I1075	I1076	I1077	I1078	I1079	I1080	I1081	I1082	I1083	I1084	I1085	I1086	I1087	I1088	I1089	I1090	I1091	I1092	I1093	I1094	I1095	I1096	I1097	I1098	I1099	I1100	I1101	I1102	I1103	I1104	I1105	I1106	I1107	I1108	I1109	I1110	I1111	I1112	I1113	I1114	I1115	I1116	I1117	I1118	I1119	I1120	I1121	I1122	I1123	I1124	I1125	I1126	I1127	I1128	I1129	I1130	I1131	I1132	I1133	I1134	I1135	I1136	I1137	I1138	I1139	I1140	I1141	I1142	I1143	I1144	I1145	I1146	I1147	I1148	I1149	I1150	I1151	I1152	I1153	I1154	I1155	I1156	I1157	I1158	I1159	I1160	I1161	I1162	I1163	I1164	I1165	I1166	I1167	I1168	I1169	I1170	I1171	I1172	I1173	I1174	I1175	I1176	I1177	I1178	I1179	I1180	I1181	I1182	I1183	I1184	I1185	I1186	I1187	I1188	I1189	I1190	I1191	I1192	I1193	I1194	I1195	I1196	I1197	I1198	I1199	I1200	I1201	I1202	I1203	I1204	I1205	I1206	I1207	I1208	I1209	I1210	I1211	I1212	I1213	I1214	I1215	I1216	I1217	I1218	I1219	I1220	I1221	I1222	I1223	I1224	I1225	I1226	I1227	I1228	I1229	I1230	I1231	I1232	I1233	I1234	I1235	I1236	I1237	I1238	I1239	I1240	I1241	I1242	I1243	I1244	I1245	I1246	I1247	I1248	I1249	I1250	I1251	I1252	I1253	I1254	I1255	I1256	I1257	I1258	I1259	I1260	I1261	I1262	I1263	I1264	I1265	I1266	I1267	I1268	I1269	I1270	I1271	I1272	I1273	I1274	I1275	I1276	I1277	I1278	I1279	I1280	I1281	I1282	I1283	I1284	I1285	I1286	I1287	I1288	I1289	I1290	I1291	I1292	I1293	I1294	I1295	I1296	I1297	I1298	I1299	I1300	I1301	I1302	I1303	I1304	I1305	I1306	I1307	I1308	I1309	I1310	I1311	I1312	I1313	I1314	I1315	I1316	I1317	I1318	I1319	I1320	I1321	I1322	I1323	I1324	I1325	I1326	I1327	I1328	I1329	I1330	I1331	I1332	I1333	I1334	I1335	I1336	I1337	I1338	I1339	I1340	I1341	I1342	I1343	I1344	I1345	I1346	I1347	I1348	I1349	I1350	I1351	I1352	I1353	I1354	I1355	I1356	I1357	I1358	I1359	I1360	I1361	I1362	I1363	I1364	I1365	I1366	I1367	I1368	I1369	I1370	I1371	I1372	I1373	I1374	I1375	I1376	I1377	I1378	I1379	I1380	I1381	I1382	I1383	I1384	I1385	I1386	I1387	I1388	I1389	I1390	I1391	I1392	I1393	I1394	I1395	I1396	I1397	I1398	I1399	I1400	I1401	I1402	I1403	I1404	I1405	I1406	I1407	I1408	I1409	I1410	I1411	I1412	I1413	I1414	I1415	I1416	I1417	I1418	I1419	I1420	I1421	I1422	I1423	I1424	I1425	I1426	I1427	I1428	I1429	I1430	I1431	I1432	I1433	I1434	I1435	I1436	I1437	I1438	I1439	I1440	I1441	I1442	I1443	I1444	I1445	I1446	I1447	I1448	I1449	I1450	I1451	I1452	I1453	I1454	I1455	I1456	I1457	I1458	I1459	I1460	I1461	I1462	I1463	I1464	I1465	I1466	I1467	I1468	I1469	I1470	I1471	I1472	I1473	I1474	I1475	I1476	I1477	I1478	I1479	I1480	I1481	I1482	I1483	I1484	I1485	I1486	I1487	I1488	I1489	I1490	I1491	I1492	I1493	I1494	I1495	I1496	I1497	I1498
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Adr. **STRCMP RA,RB**

Speichere RA nach r14 (weil RA nicht inkrementiert werden kann)

420	516	X	X	X	ZA	ADD	RAMA	X	I R	14	M R	H	H	C10	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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**Speichere [r14] nach r15. Vergleiche [RB] mit r15.**

421	E1S	X	X	X	ZB	ADD	NOP	X	X	14	M R	A B	H	C	0	X	H	H	X	X	CONT	X	H	H	H	H	H	R	
422	E1S	X	D	X	DZ	ADD	RAMA	X	X	15	M R	H	H	C	0	X	H	H	X	X	CONT	X	H	H	H	H	H	R	
423	E1S	X	X	X	ZB	ADD	NOP	X	X	X	I R	A B	H	C	0	X	H	H	X	X	CONT	X							
	E1S	X	D	X												X					X	CONT	X						

Springe zu IFETCH, wenn [r14] ungleich [RB]

DIS	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X		IFETCH	H	H	H	H	H	R
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Springe zu IFETCH, wenn r15 gleich 0

	DIS	X	X	X											X		X	CONT	X	H	H	H	H	H	R
	DIS	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X		IFETCH	H	H	H	H	H	R		

Inkrementiere r14 und RB, weiter

	DIS	X	X	X		X	H	H	X	X	CONT	X	H	H	H	H	H	R
	DIS	X	X	X		X	H	H	X				H	H	H	H	H	R

# STRCMP RA, RB

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Adr. **STRCMP RA,RB**

Speichere RA nach r14 (weil RA nicht inkrementiert werden kann)

420	516	X	X	X	ZA	ADD	RAMA	X	I R	14	M R	H	H	CIO	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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**Speichere [r14] nach r15. Vergleiche [RB] mit r15.**

421	E1S	X	X	X	ZB	ADD	NOP	X	X	14	M R	A B	H	C	0	X	H	H	X	X	CONT	X	H	H	H	H	H	R		
422	E1S	X	D	X	DZ	ADD	RAMA	X	X	15	M R	H	H	C	0	X	H	H	X	X	CONT	X	H	H	H	H	H	R		
423	E1S	X	X	X	ZB	ADD	NOP	X	X	X	I R	A B	H	C	0	X	H	H	X	X	CONT	X	H	H	H	H	H	H		
	E1S	X	D	X												X					X	CONT	X							

Springe zu IFETCH, wenn [r14] ungleich [RB]

DIS	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X		IFETCH	H	H	H	H	H	R
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Springe zu IFETCH, wenn r15 gleich 0

	DIS	X	X	X											X		X	CONT	X	H	H	H	H	H	R
	DIS	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X		IFETCH	H	H	H	H	H	R		

Inkrementiere r14 und RB, weiter

	DIS	X	X	X		X	H	H	X	X	CONT	X	H	H	H	H	H	R
	DIS	X	X	X		X	H	H	X				H	H	H	H	H	R

# STRCMP RA, RB

[illegible]

Adr. **STRCMP RA,RB**

Speichere RA nach r14 (weil RA nicht inkrementiert werden kann)

420	516	X	X	X	ZA	ADD	RAMA	X	I R	14	M R	H	H	CIO	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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**Speichere [r14] nach r15. Vergleiche [RB] mit r15.**

421		X	X	X	ZB	ADD	NOP	X	X	14	M R	A B	H	CIO	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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422		X	D	X	DZ	ADD	RAMA	X	X	15	M R	H	H	C	0	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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423		X	X	X	ZB	ADD	NOP	X	X	X	I R	A B	H	CIO	X	H	H	X	X	CONT	X	H	H	H	H	H	H
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424	DIS	X	D	X		X		X	CONT	X	
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Springe zu IFETCH, wenn [r14] ungleich [RB]

DIS	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X		IFETCH	H	H	H	H	H	R
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Springe zu IFETCH, wenn r15 gleich 0

	DIS	X	X	X		X		X	CONT	X	H	H	H	H	H	R
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PC	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X		IFETCH	H	H	H	H	H	R
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Inkrementiere r14 und RB, weiter

	X	X	X		X	H	H	X	X	CONT	X	H	H	H	H	H	R
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	SH	X	X	X		X	H	H	X		H	H	H	H	H	R
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# STRCMP RA, RB

76	75	74	73	72	71	70	69	68	67	66	65	64	63	62	61	60	59	58	57	56	55	54	53	52	51	50	49	48	47	46	45	44	43	42	41	40	39	38	37	36	35	34	33	32	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0			
IE	I3	I2	I1	I0	KMUX	K15	K14	K13	K12	K11	K10	K9	K8	K7	K6	K5	K4	K3	K2	K1	K0	G7	G6	G5	G4	G3	G2	G1	G0	A3	A2	A1	A0	ASEL	E3	E2	E1	E0	RSFL	ABUS*	EBUS*	I12	I11	I10	I9	I8	I7	I6	CEMUJ*	CEM*	I5	I4	I3	I2	I1	I0	CCEN*	D3	D2	D1	D0	D11	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0	EZ_LD*	EZ_ED*	EZ_INC*	EZ_EA*	TR_LD*	MWE*
Interrupt					Konstante																Src		Func		Dest		RA Addr		RB Addr		Y-MUX		CIN-MUX		Schiebesteu- erung				Statusregister Test				AM2910 Befehle		Direktdaten																																		

Adr. **STRCMP RA,RB**

Speichere RA nach r14 (weil RA nicht inkrementiert werden kann)

420	516	X	X	X	ZA	ADD	RAMA	X	I R	14	M R	H	H	C10	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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**Speichere [r14] nach r15. Vergleiche [RB] mit r15.**

421	E15	X	X	X	ZB	ADD	NOP	X	X	14	M R	A B	H	C10	X	H	H	X	X	CONT	X	H	H	H	H	H	R
422	E15	X	D	X	DZ	ADD	RAMA	X	X	15	M R	H	H	C10	X	H	H	X	X	CONT	X	H	H	H	H	H	R
423	E15	X	X	X	ZB	ADD	NOP	X	X	X	I R	A B	H	C10	X	H	H	X	X	CONT	X	H	H	H	H	H	H
424	E15	X	D	X	DA	SUBR	NOP	15	M R	X	X	H	H	C11	X				X	CONT	X						

Springe zu IFETCH, wenn [r14] ungleich [RB]

DIS	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X		IFETCH	H	H	H	H	H	R
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Springe zu IFETCH, wenn r15 gleich 0

	DIS	X	X	X											X		X	CONT	X	H	H	H	H	H	R
	DIS	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X		IFETCH	H	H	H	H	H	R		

Inkrementiere r14 und RB, weiter

	DIS	X	X	X		X	H	H	X	X	CONT	X	H	H	H	H	H	R
	DIS	X	X	X		X	H	H	X				H	H	H	H	H	R



# STRCMP RA, RB

I0	I1	I2	I3	I4	I5	I6	I7	I8	I9	I10	I11	I12	I13	I14	I15	I16	I17	I18	I19	I20	I21	I22	I23	I24	I25	I26	I27	I28	I29	I30	I31	I32	I33	I34	I35	I36	I37	I38	I39	I40	I41	I42	I43	I44	I45	I46	I47	I48	I49	I50	I51	I52	I53	I54	I55	I56	I57	I58	I59	I60	I61	I62	I63	I64	I65	I66	I67	I68	I69	I70	I71	I72	I73	I74	I75	I76	I77	I78	I79	I80	I81	I82	I83	I84	I85	I86	I87	I88	I89	I90	I91	I92	I93	I94	I95	I96	I97	I98	I99	I100	I101	I102	I103	I104	I105	I106	I107	I108	I109	I110	I111	I112	I113	I114	I115	I116	I117	I118	I119	I120	I121	I122	I123	I124	I125	I126	I127	I128	I129	I130	I131	I132	I133	I134	I135	I136	I137	I138	I139	I140	I141	I142	I143	I144	I145	I146	I147	I148	I149	I150	I151	I152	I153	I154	I155	I156	I157	I158	I159	I160	I161	I162	I163	I164	I165	I166	I167	I168	I169	I170	I171	I172	I173	I174	I175	I176	I177	I178	I179	I180	I181	I182	I183	I184	I185	I186	I187	I188	I189	I190	I191	I192	I193	I194	I195	I196	I197	I198	I199	I200	I201	I202	I203	I204	I205	I206	I207	I208	I209	I210	I211	I212	I213	I214	I215	I216	I217	I218	I219	I220	I221	I222	I223	I224	I225	I226	I227	I228	I229	I230	I231	I232	I233	I234	I235	I236	I237	I238	I239	I240	I241	I242	I243	I244	I245	I246	I247	I248	I249	I250	I251	I252	I253	I254	I255	I256	I257	I258	I259	I260	I261	I262	I263	I264	I265	I266	I267	I268	I269	I270	I271	I272	I273	I274	I275	I276	I277	I278	I279	I280	I281	I282	I283	I284	I285	I286	I287	I288	I289	I290	I291	I292	I293	I294	I295	I296	I297	I298	I299	I300	I301	I302	I303	I304	I305	I306	I307	I308	I309	I310	I311	I312	I313	I314	I315	I316	I317	I318	I319	I320	I321	I322	I323	I324	I325	I326	I327	I328	I329	I330	I331	I332	I333	I334	I335	I336	I337	I338	I339	I340	I341	I342	I343	I344	I345	I346	I347	I348	I349	I350	I351	I352	I353	I354	I355	I356	I357	I358	I359	I360	I361	I362	I363	I364	I365	I366	I367	I368	I369	I370	I371	I372	I373	I374	I375	I376	I377	I378	I379	I380	I381	I382	I383	I384	I385	I386	I387	I388	I389	I390	I391	I392	I393	I394	I395	I396	I397	I398	I399	I400	I401	I402	I403	I404	I405	I406	I407	I408	I409	I410	I411	I412	I413	I414	I415	I416	I417	I418	I419	I420	I421	I422	I423	I424	I425	I426	I427	I428	I429	I430	I431	I432	I433	I434	I435	I436	I437	I438	I439	I440	I441	I442	I443	I444	I445	I446	I447	I448	I449	I450	I451	I452	I453	I454	I455	I456	I457	I458	I459	I460	I461	I462	I463	I464	I465	I466	I467	I468	I469	I470	I471	I472	I473	I474	I475	I476	I477	I478	I479	I480	I481	I482	I483	I484	I485	I486	I487	I488	I489	I490	I491	I492	I493	I494	I495	I496	I497	I498	I499	I500	I501	I502	I503	I504	I505	I506	I507	I508	I509	I510	I511	I512	I513	I514	I515	I516	I517	I518	I519	I520	I521	I522	I523	I524	I525	I526	I527	I528	I529	I530	I531	I532	I533	I534	I535	I536	I537	I538	I539	I540	I541	I542	I543	I544	I545	I546	I547	I548	I549	I550	I551	I552	I553	I554	I555	I556	I557	I558	I559	I560	I561	I562	I563	I564	I565	I566	I567	I568	I569	I570	I571	I572	I573	I574	I575	I576	I577	I578	I579	I580	I581	I582	I583	I584	I585	I586	I587	I588	I589	I590	I591	I592	I593	I594	I595	I596	I597	I598	I599	I600	I601	I602	I603	I604	I605	I606	I607	I608	I609	I610	I611	I612	I613	I614	I615	I616	I617	I618	I619	I620	I621	I622	I623	I624	I625	I626	I627	I628	I629	I630	I631	I632	I633	I634	I635	I636	I637	I638	I639	I640	I641	I642	I643	I644	I645	I646	I647	I648	I649	I650	I651	I652	I653	I654	I655	I656	I657	I658	I659	I660	I661	I662	I663	I664	I665	I666	I667	I668	I669	I670	I671	I672	I673	I674	I675	I676	I677	I678	I679	I680	I681	I682	I683	I684	I685	I686	I687	I688	I689	I690	I691	I692	I693	I694	I695	I696	I697	I698	I699	I700	I701	I702	I703	I704	I705	I706	I707	I708	I709	I710	I711	I712	I713	I714	I715	I716	I717	I718	I719	I720	I721	I722	I723	I724	I725	I726	I727	I728	I729	I730	I731	I732	I733	I734	I735	I736	I737	I738	I739	I740	I741	I742	I743	I744	I745	I746	I747	I748	I749	I750	I751	I752	I753	I754	I755	I756	I757	I758	I759	I760	I761	I762	I763	I764	I765	I766	I767	I768	I769	I770	I771	I772	I773	I774	I775	I776	I777	I778	I779	I780	I781	I782	I783	I784	I785	I786	I787	I788	I789	I790	I791	I792	I793	I794	I795	I796	I797	I798	I799	I800	I801	I802	I803	I804	I805	I806	I807	I808	I809	I810	I811	I812	I813	I814	I815	I816	I817	I818	I819	I820	I821	I822	I823	I824	I825	I826	I827	I828	I829	I830	I831	I832	I833	I834	I835	I836	I837	I838	I839	I840	I841	I842	I843	I844	I845	I846	I847	I848	I849	I850	I851	I852	I853	I854	I855	I856	I857	I858	I859	I860	I861	I862	I863	I864	I865	I866	I867	I868	I869	I870	I871	I872	I873	I874	I875	I876	I877	I878	I879	I880	I881	I882	I883	I884	I885	I886	I887	I888	I889	I890	I891	I892	I893	I894	I895	I896	I897	I898	I899	I900	I901	I902	I903	I904	I905	I906	I907	I908	I909	I910	I911	I912	I913	I914	I915	I916	I917	I918	I919	I920	I921	I922	I923	I924	I925	I926	I927	I928	I929	I930	I931	I932	I933	I934	I935	I936	I937	I938	I939	I940	I941	I942	I943	I944	I945	I946	I947	I948	I949	I950	I951	I952	I953	I954	I955	I956	I957	I958	I959	I960	I961	I962	I963	I964	I965	I966	I967	I968	I969	I970	I971	I972	I973	I974	I975	I976	I977	I978	I979	I980	I981	I982	I983	I984	I985	I986	I987	I988	I989	I990	I991	I992	I993	I994	I995	I996	I997	I998	I999	I1000	I1001	I1002	I1003	I1004	I1005	I1006	I1007	I1008	I1009	I1010	I1011	I1012	I1013	I1014	I1015	I1016	I1017	I1018	I1019	I1020	I1021	I1022	I1023	I1024	I1025	I1026	I1027	I1028	I1029	I1030	I1031	I1032	I1033	I1034	I1035	I1036	I1037	I1038	I1039	I1040	I1041	I1042	I1043	I1044	I1045	I1046	I1047	I1048	I1049	I1050	I1051	I1052	I1053	I1054	I1055	I1056	I1057	I1058	I1059	I1060	I1061	I1062	I1063	I1064	I1065	I1066	I1067	I1068	I1069	I1070	I1071	I1072	I1073	I1074	I1075	I1076	I1077	I1078	I1079	I1080	I1081	I1082	I1083	I1084	I1085	I1086	I1087	I1088	I1089	I1090	I1091	I1092	I1093	I1094	I1095	I1096	I1097	I1098	I1099	I1100	I1101	I1102	I1103	I1104	I1105	I1106	I1107	I1108	I1109	I1110	I1111	I1112	I1113	I1114	I1115	I1116	I1117	I1118	I1119	I1120	I1121	I1122	I1123	I1124	I1125	I1126	I1127	I1128	I1129	I1130	I1131	I1132	I1133	I1134	I1135	I1136	I1137	I1138	I1139	I1140	I1141	I1142	I1143	I1144	I1145	I1146	I1147	I1148	I1149	I1150	I1151	I1152	I1153	I1154	I1155	I1156	I1157	I1158	I1159	I1160	I1161	I1162	I1163	I1164	I1165	I1166	I1167	I1168	I1169	I1170	I1171	I1172	I1173	I1174	I1175	I1176	I1177	I1178	I1179	I1180	I1181	I1182	I1183	I1184	I1185	I1186	I1187	I1188	I1189	I1190	I1191	I1192	I1193	I1194	I1195	I1196	I1197	I1198	I1199	I1200	I1201	I1202	I1203	I1204	I1205	I1206	I1207	I1208	I1209	I1210	I1211	I1212	I1213	I1214	I1215	I1216	I1217	I1218	I1219	I1220	I1221	I1222	I1223	I1224	I1225	I1226	I1227	I1228	I1229	I1230	I1231	I1232	I1233	I1234	I1235	I1236	I1237	I1238	I1239	I1240	I1241	I1242	I1243	I1244	I1245	I1246	I1247	I1248	I1249	I1250	I1251	I1252	I1253	I1254	I1255	I1256	I1257	I1258	I1259	I1260	I1261	I1262	I1263	I1264	I1265	I1266	I1267	I1268	I1269	I1270	I1271	I1272	I1273	I1274	I1275	I1276	I1277	I1278	I1279	I1280	I1281	I1282	I1283	I1284	I1285	I1286	I1287	I1288	I1289	I1290	I1291	I1292	I1293	I1294	I1295	I1296	I1297	I1298	I1299	I1300	I1301	I1302	I1303	I1304	I1305	I1306	I1307	I1308	I1309	I1310	I1311	I1312	I1313	I1314	I1315	I1316	I1317	I1318	I1319	I1320	I1321	I1322	I1323	I1324	I1325	I1326	I1327	I1328	I1329	I1330	I1331	I1332	I1333	I1334	I1335	I1336	I1337	I1338	I1339	I1340	I1341	I1342	I1343	I1344	I1345	I1346	I1347	I1348	I1349	I1350	I1351	I1352	I1353	I1354	I1355	I1356	I1357	I1358	I1359	I1360	I1361	I1362	I1363	I1364	I1365	I1366	I1367	I1368	I1369	I1370	I1371	I1372	I1373	I1374	I1375	I1376	I1377	I1378	I1379	I1380	I1381	I1382	I1383	I1384	I1385	I1386	I1387	I1388	I1389	I1390	I1391	I1392	I1393	I1394	I1395	I1396	I1397	I1398	I1399	I1400	I1401	I1402	I1403	I1404	I1405	I1406	I1407	I1408	I1409	I1410	I1411	I1412	I1413	I1414	I1415	I1416	I1417	I1418	I1419	I1420	I1421	I1422	I1423	I1424	I1425	I1426	I1427	I1428	I1429	I1430	I1431	I1432	I1433	I1434	I1435	I1436	I1437	I1438	I1439	I1440	I1441	I1442	I1443	I1444	I1445	I1446	I1447	I1448	I1449	I1450	I1451	I1452	I1453	I1454	I1455	I1456	I1457	I1458	I1459	I1460	I1461	I1462	I1463	I1464	I1465	I1466	I1467	I1468	I1469	I1470	I1471	I1472	I1473	I1474	I1475	I1476	I1477	I1478	I1479	I1480	I1481	I1482	I1483	I1484	I1485	I1486	I1487	I1488	I1489	I1490	I1491	I1492	I1493	I1494	I1495	I1496	I1497	I1498
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Adr. **STRCMP RA,RB**

Speichere RA nach r14 (weil RA nicht inkrementiert werden kann)

420	516	X	X	X	ZA	ADD	RAMA	X	I R	14	M R	H	H	C10	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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**Speichere [r14] nach r15. Vergleiche [RB] mit r15.**

421	E15	X	X	X	ZB	ADD	NOP	X	X	14	M R	A B	H	C10	X	H	H	X	X	CONT	X	H	H	H	H	H	R
422	E15	X	D	X	DZ	ADD	RAMA	X	X	15	M R	H	H	C10	X	H	H	X	X	CONT	X	H	H	H	H	H	R
423	E15	X	X	X	ZB	ADD	NOP	X	X	X	I R	A B	H	C10	X	H	H	X	X	CONT	X	H	H	H	H	H	H
424	E15	X	D	X	DA	SUBR	NOP	15	M R	X	X	H	H	C11	X	H	L	LOAD	X	CONT	X						

Springe zu IFETCH, wenn [r14] ungleich [RB]

DIS	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X		IFETCH	H	H	H	H	H	R
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Springe zu IFETCH, wenn r15 gleich 0

	DIS	X	X	X											X		X	CONT	X	H	H	H	H	H	R
	DIS	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X		IFETCH	H	H	H	H	H	R		

Inkrementiere r14 und RB, weiter

	DIS	X	X	X		X	H	H	X	X	CONT	X	H	H	H	H	H	R
	DIS	X	X	X		X	H	H	X				H	H	H	H	H	R

# STRCMP RA, RB

I0	I1	I2	I3	I4	I5	I6	I7	I8	I9	I10	I11	I12	I13	I14	I15	I16	I17	I18	I19	I20	I21	I22	I23	I24	I25	I26	I27	I28	I29	I30	I31	I32	I33	I34	I35	I36	I37	I38	I39	I40	I41	I42	I43	I44	I45	I46	I47	I48	I49	I50	I51	I52	I53	I54	I55	I56	I57	I58	I59	I60	I61	I62	I63	I64	I65	I66	I67	I68	I69	I70	I71	I72	I73	I74	I75	I76	I77	I78	I79	I80	I81	I82	I83	I84	I85	I86	I87	I88	I89	I90	I91	I92	I93	I94	I95	I96	I97	I98	I99	I100	I101	I102	I103	I104	I105	I106	I107	I108	I109	I110	I111	I112	I113	I114	I115	I116	I117	I118	I119	I120	I121	I122	I123	I124	I125	I126	I127	I128	I129	I130	I131	I132	I133	I134	I135	I136	I137	I138	I139	I140	I141	I142	I143	I144	I145	I146	I147	I148	I149	I150	I151	I152	I153	I154	I155	I156	I157	I158	I159	I160	I161	I162	I163	I164	I165	I166	I167	I168	I169	I170	I171	I172	I173	I174	I175	I176	I177	I178	I179	I180	I181	I182	I183	I184	I185	I186	I187	I188	I189	I190	I191	I192	I193	I194	I195	I196	I197	I198	I199	I200	I201	I202	I203	I204	I205	I206	I207	I208	I209	I210	I211	I212	I213	I214	I215	I216	I217	I218	I219	I220	I221	I222	I223	I224	I225	I226	I227	I228	I229	I230	I231	I232	I233	I234	I235	I236	I237	I238	I239	I240	I241	I242	I243	I244	I245	I246	I247	I248	I249	I250	I251	I252	I253	I254	I255	I256	I257	I258	I259	I260	I261	I262	I263	I264	I265	I266	I267	I268	I269	I270	I271	I272	I273	I274	I275	I276	I277	I278	I279	I280	I281	I282	I283	I284	I285	I286	I287	I288	I289	I290	I291	I292	I293	I294	I295	I296	I297	I298	I299	I300	I301	I302	I303	I304	I305	I306	I307	I308	I309	I310	I311	I312	I313	I314	I315	I316	I317	I318	I319	I320	I321	I322	I323	I324	I325	I326	I327	I328	I329	I330	I331	I332	I333	I334	I335	I336	I337	I338	I339	I340	I341	I342	I343	I344	I345	I346	I347	I348	I349	I350	I351	I352	I353	I354	I355	I356	I357	I358	I359	I360	I361	I362	I363	I364	I365	I366	I367	I368	I369	I370	I371	I372	I373	I374	I375	I376	I377	I378	I379	I380	I381	I382	I383	I384	I385	I386	I387	I388	I389	I390	I391	I392	I393	I394	I395	I396	I397	I398	I399	I400	I401	I402	I403	I404	I405	I406	I407	I408	I409	I410	I411	I412	I413	I414	I415	I416	I417	I418	I419	I420	I421	I422	I423	I424	I425	I426	I427	I428	I429	I430	I431	I432	I433	I434	I435	I436	I437	I438	I439	I440	I441	I442	I443	I444	I445	I446	I447	I448	I449	I450	I451	I452	I453	I454	I455	I456	I457	I458	I459	I460	I461	I462	I463	I464	I465	I466	I467	I468	I469	I470	I471	I472	I473	I474	I475	I476	I477	I478	I479	I480	I481	I482	I483	I484	I485	I486	I487	I488	I489	I490	I491	I492	I493	I494	I495	I496	I497	I498	I499	I500	I501	I502	I503	I504	I505	I506	I507	I508	I509	I510	I511	I512	I513	I514	I515	I516	I517	I518	I519	I520	I521	I522	I523	I524	I525	I526	I527	I528	I529	I530	I531	I532	I533	I534	I535	I536	I537	I538	I539	I540	I541	I542	I543	I544	I545	I546	I547	I548	I549	I550	I551	I552	I553	I554	I555	I556	I557	I558	I559	I560	I561	I562	I563	I564	I565	I566	I567	I568	I569	I570	I571	I572	I573	I574	I575	I576	I577	I578	I579	I580	I581	I582	I583	I584	I585	I586	I587	I588	I589	I590	I591	I592	I593	I594	I595	I596	I597	I598	I599	I600	I601	I602	I603	I604	I605	I606	I607	I608	I609	I610	I611	I612	I613	I614	I615	I616	I617	I618	I619	I620	I621	I622	I623	I624	I625	I626	I627	I628	I629	I630	I631	I632	I633	I634	I635	I636	I637	I638	I639	I640	I641	I642	I643	I644	I645	I646	I647	I648	I649	I650	I651	I652	I653	I654	I655	I656	I657	I658	I659	I660	I661	I662	I663	I664	I665	I666	I667	I668	I669	I670	I671	I672	I673	I674	I675	I676	I677	I678	I679	I680	I681	I682	I683	I684	I685	I686	I687	I688	I689	I690	I691	I692	I693	I694	I695	I696	I697	I698	I699	I700	I701	I702	I703	I704	I705	I706	I707	I708	I709	I710	I711	I712	I713	I714	I715	I716	I717	I718	I719	I720	I721	I722	I723	I724	I725	I726	I727	I728	I729	I730	I731	I732	I733	I734	I735	I736	I737	I738	I739	I740	I741	I742	I743	I744	I745	I746	I747	I748	I749	I750	I751	I752	I753	I754	I755	I756	I757	I758	I759	I760	I761	I762	I763	I764	I765	I766	I767	I768	I769	I770	I771	I772	I773	I774	I775	I776	I777	I778	I779	I780	I781	I782	I783	I784	I785	I786	I787	I788	I789	I790	I791	I792	I793	I794	I795	I796	I797	I798	I799	I800	I801	I802	I803	I804	I805	I806	I807	I808	I809	I810	I811	I812	I813	I814	I815	I816	I817	I818	I819	I820	I821	I822	I823	I824	I825	I826	I827	I828	I829	I830	I831	I832	I833	I834	I835	I836	I837	I838	I839	I840	I841	I842	I843	I844	I845	I846	I847	I848	I849	I850	I851	I852	I853	I854	I855	I856	I857	I858	I859	I860	I861	I862	I863	I864	I865	I866	I867	I868	I869	I870	I871	I872	I873	I874	I875	I876	I877	I878	I879	I880	I881	I882	I883	I884	I885	I886	I887	I888	I889	I890	I891	I892	I893	I894	I895	I896	I897	I898	I899	I900	I901	I902	I903	I904	I905	I906	I907	I908	I909	I910	I911	I912	I913	I914	I915	I916	I917	I918	I919	I920	I921	I922	I923	I924	I925	I926	I927	I928	I929	I930	I931	I932	I933	I934	I935	I936	I937	I938	I939	I940	I941	I942	I943	I944	I945	I946	I947	I948	I949	I950	I951	I952	I953	I954	I955	I956	I957	I958	I959	I960	I961	I962	I963	I964	I965	I966	I967	I968	I969	I970	I971	I972	I973	I974	I975	I976	I977	I978	I979	I980	I981	I982	I983	I984	I985	I986	I987	I988	I989	I990	I991	I992	I993	I994	I995	I996	I997	I998	I999	I1000	I1001	I1002	I1003	I1004	I1005	I1006	I1007	I1008	I1009	I1010	I1011	I1012	I1013	I1014	I1015	I1016	I1017	I1018	I1019	I1020	I1021	I1022	I1023	I1024	I1025	I1026	I1027	I1028	I1029	I1030	I1031	I1032	I1033	I1034	I1035	I1036	I1037	I1038	I1039	I1040	I1041	I1042	I1043	I1044	I1045	I1046	I1047	I1048	I1049	I1050	I1051	I1052	I1053	I1054	I1055	I1056	I1057	I1058	I1059	I1060	I1061	I1062	I1063	I1064	I1065	I1066	I1067	I1068	I1069	I1070	I1071	I1072	I1073	I1074	I1075	I1076	I1077	I1078	I1079	I1080	I1081	I1082	I1083	I1084	I1085	I1086	I1087	I1088	I1089	I1090	I1091	I1092	I1093	I1094	I1095	I1096	I1097	I1098	I1099	I1100	I1101	I1102	I1103	I1104	I1105	I1106	I1107	I1108	I1109	I1110	I1111	I1112	I1113	I1114	I1115	I1116	I1117	I1118	I1119	I1120	I1121	I1122	I1123	I1124	I1125	I1126	I1127	I1128	I1129	I1130	I1131	I1132	I1133	I1134	I1135	I1136	I1137	I1138	I1139	I1140	I1141	I1142	I1143	I1144	I1145	I1146	I1147	I1148	I1149	I1150	I1151	I1152	I1153	I1154	I1155	I1156	I1157	I1158	I1159	I1160	I1161	I1162	I1163	I1164	I1165	I1166	I1167	I1168	I1169	I1170	I1171	I1172	I1173	I1174	I1175	I1176	I1177	I1178	I1179	I1180	I1181	I1182	I1183	I1184	I1185	I1186	I1187	I1188	I1189	I1190	I1191	I1192	I1193	I1194	I1195	I1196	I1197	I1198	I1199	I1200	I1201	I1202	I1203	I1204	I1205	I1206	I1207	I1208	I1209	I1210	I1211	I1212	I1213	I1214	I1215	I1216	I1217	I1218	I1219	I1220	I1221	I1222	I1223	I1224	I1225	I1226	I1227	I1228	I1229	I1230	I1231	I1232	I1233	I1234	I1235	I1236	I1237	I1238	I1239	I1240	I1241	I1242	I1243	I1244	I1245	I1246	I1247	I1248	I1249	I1250	I1251	I1252	I1253	I1254	I1255	I1256	I1257	I1258	I1259	I1260	I1261	I1262	I1263	I1264	I1265	I1266	I1267	I1268	I1269	I1270	I1271	I1272	I1273	I1274	I1275	I1276	I1277	I1278	I1279	I1280	I1281	I1282	I1283	I1284	I1285	I1286	I1287	I1288	I1289	I1290	I1291	I1292	I1293	I1294	I1295	I1296	I1297	I1298	I1299	I1300	I1301	I1302	I1303	I1304	I1305	I1306	I1307	I1308	I1309	I1310	I1311	I1312	I1313	I1314	I1315	I1316	I1317	I1318	I1319	I1320	I1321	I1322	I1323	I1324	I1325	I1326	I1327	I1328	I1329	I1330	I1331	I1332	I1333	I1334	I1335	I1336	I1337	I1338	I1339	I1340	I1341	I1342	I1343	I1344	I1345	I1346	I1347	I1348	I1349	I1350	I1351	I1352	I1353	I1354	I1355	I1356	I1357	I1358	I1359	I1360	I1361	I1362	I1363	I1364	I1365	I1366	I1367	I1368	I1369	I1370	I1371	I1372	I1373	I1374	I1375	I1376	I1377	I1378	I1379	I1380	I1381	I1382	I1383	I1384	I1385	I1386	I1387	I1388	I1389	I1390	I1391	I1392	I1393	I1394	I1395	I1396	I1397	I1398	I1399	I1400	I1401	I1402	I1403	I1404	I1405	I1406	I1407	I1408	I1409	I1410	I1411	I1412	I1413	I1414	I1415	I1416	I1417	I1418	I1419	I1420	I1421	I1422	I1423	I1424	I1425	I1426	I1427	I1428	I1429	I1430	I1431	I1432	I1433	I1434	I1435	I1436	I1437	I1438	I1439	I1440	I1441	I1442	I1443	I1444	I1445	I1446	I1447	I1448	I1449	I1450	I1451	I1452	I1453	I1454	I1455	I1456	I1457	I1458	I1459	I1460	I1461	I1462	I1463	I1464	I1465	I1466	I1467	I1468	I1469	I1470	I1471	I1472	I1473	I1474	I1475	I1476	I1477	I1478	I1479	I1480	I1481	I1482	I1483	I1484	I1485	I1486	I1487	I1488	I1489	I1490	I1491	I1492	I1493	I1494	I1495	I1496	I1497	I1498
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Adr. **STRCMP RA,RB**

Speichere RA nach r14 (weil RA nicht inkrementiert werden kann)

420	516	X	X	X	ZA	ADD	RAMA	X	I R	14	M R	H	H	C10	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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**Speichere [r14] nach r15. Vergleiche [RB] mit r15.**

421	EIS	X	X	X	ZB	ADD	NOP	X	X	14	M R	A B	H	C10	X	H	H	X	X	CONT	X	H	H	H	H	H	R
422	EIS	X	D	X	DZ	ADD	RAMA	X	X	15	M R	H	H	C10	X	H	H	X	X	CONT	X	H	H	H	H	H	R
423	EIS	X	X	X	ZB	ADD	NOP	X	X	X	I R	A B	H	C10	X	H	H	X	X	CONT	X	H	H	H	H	H	H
424	EIS	X	D	X	DA	SUBR	NOP	15	M R	X	X	H	H	C11	X	H	L	LOAD	X	CONT	X	H	H	H	H	H	R

Springe zu IFETCH, wenn [r14] ungleich [RB]

DIS	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X		IFETCH	H	H	H	H	H	R
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Springe zu IFETCH, wenn r15 gleich 0

	DIS	X	X	X											X		X	CONT	X	H	H	H	H	H	R
	DIS	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X		IFETCH	H	H	H	H	H	R		

Inkrementiere r14 und RB, weiter

	DIS	X	X	X		X	H	H	X	X	CONT	X	H	H	H	H	H	R
	DIS	X	X	X		X	H	H	X				H	H	H	H	H	R

# STRCMP RA, RB

[illegible]

Adr. **STRCMP RA,RB**

Speichere RA nach r14 (weil RA nicht inkrementiert werden kann)

420	516	X	X	X	ZA	ADD	RAMA	X	I R	14	M R	H	H	C10	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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**Speichere [r14] nach r15. Vergleiche [RB] mit r15.**

421		X	X	X	ZB	ADD	NOP	X	X	14	M R	A B	H	CIO	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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422		X	D	X	DZ	ADD	RAMA	X	X	15	M R	H	H	C	0	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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423		X	X	X	ZB	ADD	NOP	X	X	X	I R	A B	H	CIO	X	H	H	X	X	CONT	X	H	H	H	H	H	H
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424	DIS	X	D	X	DA	SUBR	NOP	15	M R	X	X	H	H	CH	X	H	L	LOAD	X	CONT	X	H	H	H	H	H	R
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Springe zu IFETCH, wenn [r14] ungleich [RB]

425	DIS	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X		IFETCH	H	H	H	H	H	R
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Springe zu IFETCH, wenn r15 gleich 0

	DIS	X	X	X		X		X	CONT	X	H	H	H	H	H	R
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	MS	X	X	X	X	X	NOP	X	X	X	X	H	H	H	X	X		IFETCH	H	H	H	H	H	R
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Inkrementiere r14 und RB, weiter

	X	X	X		X	H	H	X	X	CONT	X	H	H	H	H	H	R
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	SIC	X	X	X		X	H	H	X		H	H	H	H	H	R
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# STRCMP RA, RB

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Adr. **STRCMP RA,RB**

Speichere RA nach r14 (weil RA nicht inkrementiert werden kann)

420	516	X	X	X	ZA	ADD	RAMA	X	I R	14	M R	H	H	C/O	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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**Speichere [r14] nach r15. Vergleiche [RB] mit r15.**

421	STC	X	X	X	ZB	ADD	NOP	X	X	14	M R	A B	H	C/O	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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422		X	D	X	DZ	ADD	RAMA	X	X	15	M R	H	H	C	0	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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423	DES	X	X	X	ZB	ADD	NOP	X	X	X	I R	A B	H	CIO	X	H	H	X	X	CONT	X	H	H	H	H	H	H
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424	DIS	X	D	X	DA	SUBR	NOP	15	M R	X	X	H	H	CH	X	H	L	LOAD	X	CONT	X	H	H	H	H	H	R
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Springe zu IFETCH, wenn [r14] ungleich [RB]

425	DIS	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X	H	H	MA-NoIZ	C	GJP	IFETCH	H	H	H	H	H	R
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Springe zu IFETCH, wenn r15 gleich 0

	DIS	X	X	X		X		X	CONT	X	H	H	H	H	H	R
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	MS	X	X	X	X	X	NOP	X	X	X	X	H	H	H	X	X		IFETCH	H	H	H	H	H	R
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Inkrementiere r14 und RB, weiter

	X	X	X		X	H	H	X	X	CONT	X	H	H	H	H	H	R
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	SIC	X	X	X		X	H	H	X		H	H	H	H	H	R
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# STRCMP RA, RB

I0	I1	I2	I3	I4	I5	I6	I7	I8	I9	I10	I11	I12	I13	I14	I15	I16	I17	I18	I19	I20	I21	I22	I23	I24	I25	I26	I27	I28	I29	I30	I31	I32	I33	I34	I35	I36	I37	I38	I39	I40	I41	I42	I43	I44	I45	I46	I47	I48	I49	I50	I51	I52	I53	I54	I55	I56	I57	I58	I59	I60	I61	I62	I63	I64	I65	I66	I67	I68	I69	I70	I71	I72	I73	I74	I75	I76	I77	I78	I79	I80	I81	I82	I83	I84	I85	I86	I87	I88	I89	I90	I91	I92	I93	I94	I95	I96	I97	I98	I99	I100	I101	I102	I103	I104	I105	I106	I107	I108	I109	I110	I111	I112	I113	I114	I115	I116	I117	I118	I119	I120	I121	I122	I123	I124	I125	I126	I127	I128	I129	I130	I131	I132	I133	I134	I135	I136	I137	I138	I139	I140	I141	I142	I143	I144	I145	I146	I147	I148	I149	I150	I151	I152	I153	I154	I155	I156	I157	I158	I159	I160	I161	I162	I163	I164	I165	I166	I167	I168	I169	I170	I171	I172	I173	I174	I175	I176	I177	I178	I179	I180	I181	I182	I183	I184	I185	I186	I187	I188	I189	I190	I191	I192	I193	I194	I195	I196	I197	I198	I199	I200	I201	I202	I203	I204	I205	I206	I207	I208	I209	I210	I211	I212	I213	I214	I215	I216	I217	I218	I219	I220	I221	I222	I223	I224	I225	I226	I227	I228	I229	I230	I231	I232	I233	I234	I235	I236	I237	I238	I239	I240	I241	I242	I243	I244	I245	I246	I247	I248	I249	I250	I251	I252	I253	I254	I255	I256	I257	I258	I259	I260	I261	I262	I263	I264	I265	I266	I267	I268	I269	I270	I271	I272	I273	I274	I275	I276	I277	I278	I279	I280	I281	I282	I283	I284	I285	I286	I287	I288	I289	I290	I291	I292	I293	I294	I295	I296	I297	I298	I299	I300	I301	I302	I303	I304	I305	I306	I307	I308	I309	I310	I311	I312	I313	I314	I315	I316	I317	I318	I319	I320	I321	I322	I323	I324	I325	I326	I327	I328	I329	I330	I331	I332	I333	I334	I335	I336	I337	I338	I339	I340	I341	I342	I343	I344	I345	I346	I347	I348	I349	I350	I351	I352	I353	I354	I355	I356	I357	I358	I359	I360	I361	I362	I363	I364	I365	I366	I367	I368	I369	I370	I371	I372	I373	I374	I375	I376	I377	I378	I379	I380	I381	I382	I383	I384	I385	I386	I387	I388	I389	I390	I391	I392	I393	I394	I395	I396	I397	I398	I399	I400	I401	I402	I403	I404	I405	I406	I407	I408	I409	I410	I411	I412	I413	I414	I415	I416	I417	I418	I419	I420	I421	I422	I423	I424	I425	I426	I427	I428	I429	I430	I431	I432	I433	I434	I435	I436	I437	I438	I439	I440	I441	I442	I443	I444	I445	I446	I447	I448	I449	I450	I451	I452	I453	I454	I455	I456	I457	I458	I459	I460	I461	I462	I463	I464	I465	I466	I467	I468	I469	I470	I471	I472	I473	I474	I475	I476	I477	I478	I479	I480	I481	I482	I483	I484	I485	I486	I487	I488	I489	I490	I491	I492	I493	I494	I495	I496	I497	I498	I499	I500	I501	I502	I503	I504	I505	I506	I507	I508	I509	I510	I511	I512	I513	I514	I515	I516	I517	I518	I519	I520	I521	I522	I523	I524	I525	I526	I527	I528	I529	I530	I531	I532	I533	I534	I535	I536	I537	I538	I539	I540	I541	I542	I543	I544	I545	I546	I547	I548	I549	I550	I551	I552	I553	I554	I555	I556	I557	I558	I559	I560	I561	I562	I563	I564	I565	I566	I567	I568	I569	I570	I571	I572	I573	I574	I575	I576	I577	I578	I579	I580	I581	I582	I583	I584	I585	I586	I587	I588	I589	I590	I591	I592	I593	I594	I595	I596	I597	I598	I599	I600	I601	I602	I603	I604	I605	I606	I607	I608	I609	I610	I611	I612	I613	I614	I615	I616	I617	I618	I619	I620	I621	I622	I623	I624	I625	I626	I627	I628	I629	I630	I631	I632	I633	I634	I635	I636	I637	I638	I639	I640	I641	I642	I643	I644	I645	I646	I647	I648	I649	I650	I651	I652	I653	I654	I655	I656	I657	I658	I659	I660	I661	I662	I663	I664	I665	I666	I667	I668	I669	I670	I671	I672	I673	I674	I675	I676	I677	I678	I679	I680	I681	I682	I683	I684	I685	I686	I687	I688	I689	I690	I691	I692	I693	I694	I695	I696	I697	I698	I699	I700	I701	I702	I703	I704	I705	I706	I707	I708	I709	I710	I711	I712	I713	I714	I715	I716	I717	I718	I719	I720	I721	I722	I723	I724	I725	I726	I727	I728	I729	I730	I731	I732	I733	I734	I735	I736	I737	I738	I739	I740	I741	I742	I743	I744	I745	I746	I747	I748	I749	I750	I751	I752	I753	I754	I755	I756	I757	I758	I759	I760	I761	I762	I763	I764	I765	I766	I767	I768	I769	I770	I771	I772	I773	I774	I775	I776	I777	I778	I779	I780	I781	I782	I783	I784	I785	I786	I787	I788	I789	I790	I791	I792	I793	I794	I795	I796	I797	I798	I799	I800	I801	I802	I803	I804	I805	I806	I807	I808	I809	I810	I811	I812	I813	I814	I815	I816	I817	I818	I819	I820	I821	I822	I823	I824	I825	I826	I827	I828	I829	I830	I831	I832	I833	I834	I835	I836	I837	I838	I839	I840	I841	I842	I843	I844	I845	I846	I847	I848	I849	I850	I851	I852	I853	I854	I855	I856	I857	I858	I859	I860	I861	I862	I863	I864	I865	I866	I867	I868	I869	I870	I871	I872	I873	I874	I875	I876	I877	I878	I879	I880	I881	I882	I883	I884	I885	I886	I887	I888	I889	I890	I891	I892	I893	I894	I895	I896	I897	I898	I899	I900	I901	I902	I903	I904	I905	I906	I907	I908	I909	I910	I911	I912	I913	I914	I915	I916	I917	I918	I919	I920	I921	I922	I923	I924	I925	I926	I927	I928	I929	I930	I931	I932	I933	I934	I935	I936	I937	I938	I939	I940	I941	I942	I943	I944	I945	I946	I947	I948	I949	I950	I951	I952	I953	I954	I955	I956	I957	I958	I959	I960	I961	I962	I963	I964	I965	I966	I967	I968	I969	I970	I971	I972	I973	I974	I975	I976	I977	I978	I979	I980	I981	I982	I983	I984	I985	I986	I987	I988	I989	I990	I991	I992	I993	I994	I995	I996	I997	I998	I999	I1000	I1001	I1002	I1003	I1004	I1005	I1006	I1007	I1008	I1009	I1010	I1011	I1012	I1013	I1014	I1015	I1016	I1017	I1018	I1019	I1020	I1021	I1022	I1023	I1024	I1025	I1026	I1027	I1028	I1029	I1030	I1031	I1032	I1033	I1034	I1035	I1036	I1037	I1038	I1039	I1040	I1041	I1042	I1043	I1044	I1045	I1046	I1047	I1048	I1049	I1050	I1051	I1052	I1053	I1054	I1055	I1056	I1057	I1058	I1059	I1060	I1061	I1062	I1063	I1064	I1065	I1066	I1067	I1068	I1069	I1070	I1071	I1072	I1073	I1074	I1075	I1076	I1077	I1078	I1079	I1080	I1081	I1082	I1083	I1084	I1085	I1086	I1087	I1088	I1089	I1090	I1091	I1092	I1093	I1094	I1095	I1096	I1097	I1098	I1099	I1100	I1101	I1102	I1103	I1104	I1105	I1106	I1107	I1108	I1109	I1110	I1111	I1112	I1113	I1114	I1115	I1116	I1117	I1118	I1119	I1120	I1121	I1122	I1123	I1124	I1125	I1126	I1127	I1128	I1129	I1130	I1131	I1132	I1133	I1134	I1135	I1136	I1137	I1138	I1139	I1140	I1141	I1142	I1143	I1144	I1145	I1146	I1147	I1148	I1149	I1150	I1151	I1152	I1153	I1154	I1155	I1156	I1157	I1158	I1159	I1160	I1161	I1162	I1163	I1164	I1165	I1166	I1167	I1168	I1169	I1170	I1171	I1172	I1173	I1174	I1175	I1176	I1177	I1178	I1179	I1180	I1181	I1182	I1183	I1184	I1185	I1186	I1187	I1188	I1189	I1190	I1191	I1192	I1193	I1194	I1195	I1196	I1197	I1198	I1199	I1200	I1201	I1202	I1203	I1204	I1205	I1206	I1207	I1208	I1209	I1210	I1211	I1212	I1213	I1214	I1215	I1216	I1217	I1218	I1219	I1220	I1221	I1222	I1223	I1224	I1225	I1226	I1227	I1228	I1229	I1230	I1231	I1232	I1233	I1234	I1235	I1236	I1237	I1238	I1239	I1240	I1241	I1242	I1243	I1244	I1245	I1246	I1247	I1248	I1249	I1250	I1251	I1252	I1253	I1254	I1255	I1256	I1257	I1258	I1259	I1260	I1261	I1262	I1263	I1264	I1265	I1266	I1267	I1268	I1269	I1270	I1271	I1272	I1273	I1274	I1275	I1276	I1277	I1278	I1279	I1280	I1281	I1282	I1283	I1284	I1285	I1286	I1287	I1288	I1289	I1290	I1291	I1292	I1293	I1294	I1295	I1296	I1297	I1298	I1299	I1300	I1301	I1302	I1303	I1304	I1305	I1306	I1307	I1308	I1309	I1310	I1311	I1312	I1313	I1314	I1315	I1316	I1317	I1318	I1319	I1320	I1321	I1322	I1323	I1324	I1325	I1326	I1327	I1328	I1329	I1330	I1331	I1332	I1333	I1334	I1335	I1336	I1337	I1338	I1339	I1340	I1341	I1342	I1343	I1344	I1345	I1346	I1347	I1348	I1349	I1350	I1351	I1352	I1353	I1354	I1355	I1356	I1357	I1358	I1359	I1360	I1361	I1362	I1363	I1364	I1365	I1366	I1367	I1368	I1369	I1370	I1371	I1372	I1373	I1374	I1375	I1376	I1377	I1378	I1379	I1380	I1381	I1382	I1383	I1384	I1385	I1386	I1387	I1388	I1389	I1390	I1391	I1392	I1393	I1394	I1395	I1396	I1397	I1398	I1399	I1400	I1401	I1402	I1403	I1404	I1405	I1406	I1407	I1408	I1409	I1410	I1411	I1412	I1413	I1414	I1415	I1416	I1417	I1418	I1419	I1420	I1421	I1422	I1423	I1424	I1425	I1426	I1427	I1428	I1429	I1430	I1431	I1432	I1433	I1434	I1435	I1436	I1437	I1438	I1439	I1440	I1441	I1442	I1443	I1444	I1445	I1446	I1447	I1448	I1449	I1450	I1451	I1452	I1453	I1454	I1455	I1456	I1457	I1458	I1459	I1460	I1461	I1462	I1463	I1464	I1465	I1466	I1467	I1468	I1469	I1470	I1471	I1472	I1473	I1474	I1475	I1476	I1477	I1478	I1479	I1480	I1481	I1482	I1483	I1484	I1485	I1486	I1487	I1488	I1489	I1490	I1491	I1492	I1493	I1494	I1495	I1496	I1497	I1498
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Adr. **STRCMP RA,RB**

Speichere RA nach r14 (weil RA nicht inkrementiert werden kann)

420	SI	X	X	X	ZA	ADD	RAMA	X	I R	14	M R	H	H	CIO	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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Speichere [r14] nach r15. Vergleiche [RB] mit r15.

421	EIS	X	X	X	ZB	ADD	NOP	X	X	14	M R	A B	H	C10	X	H	H	X	X	CONT	X	H	H	H	H	H	R
422	EIS	X	D	X	DZ	ADD	RAMA	X	X	15	M R	H	H	C10	X	H	H	X	X	CONT	X	H	H	H	H	H	R
423	EIS	X	X	X	ZB	ADD	NOP	X	X	X	I R	A B	H	C10	X	H	H	X	X	CONT	X	H	H	H	H	H	H
424	EIS	X	D	X	DA	SUBR	NOP	15	M R	X	X	H	H	C11	X	H	L	LOAD	X	CONT	X	H	H	H	H	H	R

Springe zu IFETCH, wenn [r14] ungleich [RB]

425		X	X	X	X	X	NOP	X	X	X	X	H	H	X	X	H	H	MA-NoIZ	C	GJP	IFETCH	H	H	H	H	H	R
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Springe zu IFETCH, wenn r15 gleich 0

426	DIS	X	X	X											X		X	CONT	X	H	H	H	H	H	R	
	DIS	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X					IFETCH	H	H	H	H	H	R

Inkrementiere r14 und RB, weiter

	DIS	X	X	X		X	H	H	X	X	CONT	X	H	H	H	H	H	R
	DIS	X	X	X		X	H	H	X				H	H	H	H	H	R

# STRCMP RA, RB

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Adr. **STRCMP RA,RB**

Speichere RA nach r14 (weil RA nicht inkrementiert werden kann)

420	516	X	X	X	ZA	ADD	RAMA	X	I R	14	M R	H	H	C/O	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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**Speichere [r14] nach r15. Vergleiche [RB] mit r15.**

421	STC	X	X	X	ZB	ADD	NOP	X	X	14	M R	A B	H	C/O	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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422		X	D	X	DZ	ADD	RAMA	X	X	15	M R	H	H	C	0	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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423	DES	X	X	X	ZB	ADD	NOP	X	X	X	I R	A B	H	CIO	X	H	H	X	X	CONT	X	H	H	H	H	H	H
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424	DIS	X	D	X	DA	SUBR	NOP	15	M R	X	X	H	H	CH	X	H	L	LOAD	X	CONT	X	H	H	H	H	H	R
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Springe zu IFETCH, wenn [r14] ungleich [RB]

425		X	X	X	X	X	NOP	X	X	X	X	H	H	X	X	H	H	MA-NoIZ	C	GJP	IFETCH	H	H	H	H	H	R
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Springe zu IFETCH, wenn r15 gleich 0

426	DIS	X	X	X	ZA	ADD	NOP	15	M R	X	X	H	H	C	O	X		X	CONT	X	H	H	H	H	R
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	MS	X	X	X	X	X	NOP	X	X	X	X	H	H	H	X	X		IFETCH	H	H	H	H	H	R
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Inkrementiere r14 und RB, weiter

	X	X	X		X	H	H	X	X	CONT	X	H	H	H	H	H	R
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	SIC	X	X	X		X	H	H	X		H	H	H	H	H	R
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# STRCMP RA, RB

I0	I1	I2	I3	I4	I5	I6	I7	I8	I9	I10	I11	I12	I13	I14	I15	I16	I17	I18	I19	I20	I21	I22	I23	I24	I25	I26	I27	I28	I29	I30	I31	I32	I33	I34	I35	I36	I37	I38	I39	I40	I41	I42	I43	I44	I45	I46	I47	I48	I49	I50	I51	I52	I53	I54	I55	I56	I57	I58	I59	I60	I61	I62	I63	I64	I65	I66	I67	I68	I69	I70	I71	I72	I73	I74	I75	I76	I77	I78	I79	I80	I81	I82	I83	I84	I85	I86	I87	I88	I89	I90	I91	I92	I93	I94	I95	I96	I97	I98	I99	I100	I101	I102	I103	I104	I105	I106	I107	I108	I109	I110	I111	I112	I113	I114	I115	I116	I117	I118	I119	I120	I121	I122	I123	I124	I125	I126	I127	I128	I129	I130	I131	I132	I133	I134	I135	I136	I137	I138	I139	I140	I141	I142	I143	I144	I145	I146	I147	I148	I149	I150	I151	I152	I153	I154	I155	I156	I157	I158	I159	I160	I161	I162	I163	I164	I165	I166	I167	I168	I169	I170	I171	I172	I173	I174	I175	I176	I177	I178	I179	I180	I181	I182	I183	I184	I185	I186	I187	I188	I189	I190	I191	I192	I193	I194	I195	I196	I197	I198	I199	I200	I201	I202	I203	I204	I205	I206	I207	I208	I209	I210	I211	I212	I213	I214	I215	I216	I217	I218	I219	I220	I221	I222	I223	I224	I225	I226	I227	I228	I229	I230	I231	I232	I233	I234	I235	I236	I237	I238	I239	I240	I241	I242	I243	I244	I245	I246	I247	I248	I249	I250	I251	I252	I253	I254	I255	I256	I257	I258	I259	I260	I261	I262	I263	I264	I265	I266	I267	I268	I269	I270	I271	I272	I273	I274	I275	I276	I277	I278	I279	I280	I281	I282	I283	I284	I285	I286	I287	I288	I289	I290	I291	I292	I293	I294	I295	I296	I297	I298	I299	I300	I301	I302	I303	I304	I305	I306	I307	I308	I309	I310	I311	I312	I313	I314	I315	I316	I317	I318	I319	I320	I321	I322	I323	I324	I325	I326	I327	I328	I329	I330	I331	I332	I333	I334	I335	I336	I337	I338	I339	I340	I341	I342	I343	I344	I345	I346	I347	I348	I349	I350	I351	I352	I353	I354	I355	I356	I357	I358	I359	I360	I361	I362	I363	I364	I365	I366	I367	I368	I369	I370	I371	I372	I373	I374	I375	I376	I377	I378	I379	I380	I381	I382	I383	I384	I385	I386	I387	I388	I389	I390	I391	I392	I393	I394	I395	I396	I397	I398	I399	I400	I401	I402	I403	I404	I405	I406	I407	I408	I409	I410	I411	I412	I413	I414	I415	I416	I417	I418	I419	I420	I421	I422	I423	I424	I425	I426	I427	I428	I429	I430	I431	I432	I433	I434	I435	I436	I437	I438	I439	I440	I441	I442	I443	I444	I445	I446	I447	I448	I449	I450	I451	I452	I453	I454	I455	I456	I457	I458	I459	I460	I461	I462	I463	I464	I465	I466	I467	I468	I469	I470	I471	I472	I473	I474	I475	I476	I477	I478	I479	I480	I481	I482	I483	I484	I485	I486	I487	I488	I489	I490	I491	I492	I493	I494	I495	I496	I497	I498	I499	I500	I501	I502	I503	I504	I505	I506	I507	I508	I509	I510	I511	I512	I513	I514	I515	I516	I517	I518	I519	I520	I521	I522	I523	I524	I525	I526	I527	I528	I529	I530	I531	I532	I533	I534	I535	I536	I537	I538	I539	I540	I541	I542	I543	I544	I545	I546	I547	I548	I549	I550	I551	I552	I553	I554	I555	I556	I557	I558	I559	I560	I561	I562	I563	I564	I565	I566	I567	I568	I569	I570	I571	I572	I573	I574	I575	I576	I577	I578	I579	I580	I581	I582	I583	I584	I585	I586	I587	I588	I589	I590	I591	I592	I593	I594	I595	I596	I597	I598	I599	I600	I601	I602	I603	I604	I605	I606	I607	I608	I609	I610	I611	I612	I613	I614	I615	I616	I617	I618	I619	I620	I621	I622	I623	I624	I625	I626	I627	I628	I629	I630	I631	I632	I633	I634	I635	I636	I637	I638	I639	I640	I641	I642	I643	I644	I645	I646	I647	I648	I649	I650	I651	I652	I653	I654	I655	I656	I657	I658	I659	I660	I661	I662	I663	I664	I665	I666	I667	I668	I669	I670	I671	I672	I673	I674	I675	I676	I677	I678	I679	I680	I681	I682	I683	I684	I685	I686	I687	I688	I689	I690	I691	I692	I693	I694	I695	I696	I697	I698	I699	I700	I701	I702	I703	I704	I705	I706	I707	I708	I709	I710	I711	I712	I713	I714	I715	I716	I717	I718	I719	I720	I721	I722	I723	I724	I725	I726	I727	I728	I729	I730	I731	I732	I733	I734	I735	I736	I737	I738	I739	I740	I741	I742	I743	I744	I745	I746	I747	I748	I749	I750	I751	I752	I753	I754	I755	I756	I757	I758	I759	I760	I761	I762	I763	I764	I765	I766	I767	I768	I769	I770	I771	I772	I773	I774	I775	I776	I777	I778	I779	I780	I781	I782	I783	I784	I785	I786	I787	I788	I789	I790	I791	I792	I793	I794	I795	I796	I797	I798	I799	I800	I801	I802	I803	I804	I805	I806	I807	I808	I809	I810	I811	I812	I813	I814	I815	I816	I817	I818	I819	I820	I821	I822	I823	I824	I825	I826	I827	I828	I829	I830	I831	I832	I833	I834	I835	I836	I837	I838	I839	I840	I841	I842	I843	I844	I845	I846	I847	I848	I849	I850	I851	I852	I853	I854	I855	I856	I857	I858	I859	I860	I861	I862	I863	I864	I865	I866	I867	I868	I869	I870	I871	I872	I873	I874	I875	I876	I877	I878	I879	I880	I881	I882	I883	I884	I885	I886	I887	I888	I889	I890	I891	I892	I893	I894	I895	I896	I897	I898	I899	I900	I901	I902	I903	I904	I905	I906	I907	I908	I909	I910	I911	I912	I913	I914	I915	I916	I917	I918	I919	I920	I921	I922	I923	I924	I925	I926	I927	I928	I929	I930	I931	I932	I933	I934	I935	I936	I937	I938	I939	I940	I941	I942	I943	I944	I945	I946	I947	I948	I949	I950	I951	I952	I953	I954	I955	I956	I957	I958	I959	I960	I961	I962	I963	I964	I965	I966	I967	I968	I969	I970	I971	I972	I973	I974	I975	I976	I977	I978	I979	I980	I981	I982	I983	I984	I985	I986	I987	I988	I989	I990	I991	I992	I993	I994	I995	I996	I997	I998	I999	I1000	I1001	I1002	I1003	I1004	I1005	I1006	I1007	I1008	I1009	I1010	I1011	I1012	I1013	I1014	I1015	I1016	I1017	I1018	I1019	I1020	I1021	I1022	I1023	I1024	I1025	I1026	I1027	I1028	I1029	I1030	I1031	I1032	I1033	I1034	I1035	I1036	I1037	I1038	I1039	I1040	I1041	I1042	I1043	I1044	I1045	I1046	I1047	I1048	I1049	I1050	I1051	I1052	I1053	I1054	I1055	I1056	I1057	I1058	I1059	I1060	I1061	I1062	I1063	I1064	I1065	I1066	I1067	I1068	I1069	I1070	I1071	I1072	I1073	I1074	I1075	I1076	I1077	I1078	I1079	I1080	I1081	I1082	I1083	I1084	I1085	I1086	I1087	I1088	I1089	I1090	I1091	I1092	I1093	I1094	I1095	I1096	I1097	I1098	I1099	I1100	I1101	I1102	I1103	I1104	I1105	I1106	I1107	I1108	I1109	I1110	I1111	I1112	I1113	I1114	I1115	I1116	I1117	I1118	I1119	I1120	I1121	I1122	I1123	I1124	I1125	I1126	I1127	I1128	I1129	I1130	I1131	I1132	I1133	I1134	I1135	I1136	I1137	I1138	I1139	I1140	I1141	I1142	I1143	I1144	I1145	I1146	I1147	I1148	I1149	I1150	I1151	I1152	I1153	I1154	I1155	I1156	I1157	I1158	I1159	I1160	I1161	I1162	I1163	I1164	I1165	I1166	I1167	I1168	I1169	I1170	I1171	I1172	I1173	I1174	I1175	I1176	I1177	I1178	I1179	I1180	I1181	I1182	I1183	I1184	I1185	I1186	I1187	I1188	I1189	I1190	I1191	I1192	I1193	I1194	I1195	I1196	I1197	I1198	I1199	I1200	I1201	I1202	I1203	I1204	I1205	I1206	I1207	I1208	I1209	I1210	I1211	I1212	I1213	I1214	I1215	I1216	I1217	I1218	I1219	I1220	I1221	I1222	I1223	I1224	I1225	I1226	I1227	I1228	I1229	I1230	I1231	I1232	I1233	I1234	I1235	I1236	I1237	I1238	I1239	I1240	I1241	I1242	I1243	I1244	I1245	I1246	I1247	I1248	I1249	I1250	I1251	I1252	I1253	I1254	I1255	I1256	I1257	I1258	I1259	I1260	I1261	I1262	I1263	I1264	I1265	I1266	I1267	I1268	I1269	I1270	I1271	I1272	I1273	I1274	I1275	I1276	I1277	I1278	I1279	I1280	I1281	I1282	I1283	I1284	I1285	I1286	I1287	I1288	I1289	I1290	I1291	I1292	I1293	I1294	I1295	I1296	I1297	I1298	I1299	I1300	I1301	I1302	I1303	I1304	I1305	I1306	I1307	I1308	I1309	I1310	I1311	I1312	I1313	I1314	I1315	I1316	I1317	I1318	I1319	I1320	I1321	I1322	I1323	I1324	I1325	I1326	I1327	I1328	I1329	I1330	I1331	I1332	I1333	I1334	I1335	I1336	I1337	I1338	I1339	I1340	I1341	I1342	I1343	I1344	I1345	I1346	I1347	I1348	I1349	I1350	I1351	I1352	I1353	I1354	I1355	I1356	I1357	I1358	I1359	I1360	I1361	I1362	I1363	I1364	I1365	I1366	I1367	I1368	I1369	I1370	I1371	I1372	I1373	I1374	I1375	I1376	I1377	I1378	I1379	I1380	I1381	I1382	I1383	I1384	I1385	I1386	I1387	I1388	I1389	I1390	I1391	I1392	I1393	I1394	I1395	I1396	I1397	I1398	I1399	I1400	I1401	I1402	I1403	I1404	I1405	I1406	I1407	I1408	I1409	I1410	I1411	I1412	I1413	I1414	I1415	I1416	I1417	I1418	I1419	I1420	I1421	I1422	I1423	I1424	I1425	I1426	I1427	I1428	I1429	I1430	I1431	I1432	I1433	I1434	I1435	I1436	I1437	I1438	I1439	I1440	I1441	I1442	I1443	I1444	I1445	I1446	I1447	I1448	I1449	I1450	I1451	I1452	I1453	I1454	I1455	I1456	I1457	I1458	I1459	I1460	I1461	I1462	I1463	I1464	I1465	I1466	I1467	I1468	I1469	I1470	I1471	I1472	I1473	I1474	I1475	I1476	I1477	I1478	I1479	I1480	I1481	I1482	I1483	I1484	I1485	I1486	I1487	I1488	I1489	I1490	I1491	I1492	I1493	I1494	I1495	I1496	I1497	I1498
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Adr. **STRCMP RA,RB**

Speichere RA nach r14 (weil RA nicht inkrementiert werden kann)

420	516	X	X	X	ZA	ADD	RAMA	X	I R	14	M R	H	H	CIO	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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Speichere [r14] nach r15. Vergleiche [RB] mit r15.

421	E15	X	X	X	ZB	ADD	NOP	X	X	14	M R	A B	H	C10	X	H	H	X	X	CONT	X	H	H	H	H	H	R
422	E15	X	D	X	DZ	ADD	RAMA	X	X	15	M R	H	H	C10	X	H	H	X	X	CONT	X	H	H	H	H	H	R
423	E15	X	X	X	ZB	ADD	NOP	X	X	X	I R	A B	H	C10	X	H	H	X	X	CONT	X	H	H	H	H	H	H
424	E15	X	D	X	DA	SUBR	NOP	15	M R	X	X	H	H	C11	X	H	L	LOAD	X	CONT	X	H	H	H	H	H	R

Springe zu IFETCH, wenn [r14] ungleich [RB]

425	DIS	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X	H	H	MA-NoZ	C	GJP	IFETCH	H	H	H	H	H	R
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Springe zu IFETCH, wenn r15 gleich 0

426	DIS	X	X	X	ZA	ADD	NOP	15	M H	X	X	H	H	C	0	X	H	L	LOAD	X	CONT	X	H	H	H	H	H	R		
	DIS	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X	X					IFETCH				H	H	H	H	H	R

Inkrementiere r14 und RB, weiter

	DIS	X	X	X		X	H	H	X	X	CONT	X	H	H	H	H	H	R
	DIS	X	X	X		X	H	H	X				H	H	H	H	H	R

# STRCMP RA, RB

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Adr. **STRCMP RA,RB**

Speichere RA nach r14 (weil RA nicht inkrementiert werden kann)

420	516	X	X	X	ZA	ADD	RAMA	X	I R	14	M R	H	H	C10	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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**Speichere [r14] nach r15. Vergleiche [RB] mit r15.**

421	STC	X	X	X	ZB	ADD	NOP	X	X	14	M R	A B	H	C/O	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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422		X	D	X	DZ	ADD	RAMA	X	X	15	M R	H	H	C	0	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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423	DES	X	X	X	ZB	ADD	NOP	X	X	X	I R	A B	H	CIO	X	H	H	X	X	CONT	X	H	H	H	H	H	H
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424	DIS	X	D	X	DA	SUBR	NOP	15	M R	X	X	H	H	CH	X	H	L	LOAD	X	CONT	X	H	H	H	H	H	R
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Springe zu IFETCH, wenn [r14] ungleich [RB]

425	DIS	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X	H	H	MA-NoIZ	C	GJP	IFETCH	H	H	H	H	H	R
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Springe zu IFETCH, wenn r15 gleich 0

426	DIS	X	X	X	ZA	ADD	NOP	15	M R	X	X	H	H	C	O	X	H	L	LOAD	X	CONT	X	H	H	H	H	R
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427	MS	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X		IFETCH	H	H	H	H	H	R
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Inkrementiere r14 und RB, weiter

	X	X	X		X	H	H	X	X	CONT	X	H	H	H	H	H	R
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	SIC	X	X	X		X	H	H	X		H	H	H	H	H	R
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# STRCMP RA, RB

I0	I1	I2	I3	I4	I5	I6	I7	I8	I9	I10	I11	I12	I13	I14	I15	I16	I17	I18	I19	I20	I21	I22	I23	I24	I25	I26	I27	I28	I29	I30	I31	I32	I33	I34	I35	I36	I37	I38	I39	I40	I41	I42	I43	I44	I45	I46	I47	I48	I49	I50	I51	I52	I53	I54	I55	I56	I57	I58	I59	I60	I61	I62	I63	I64	I65	I66	I67	I68	I69	I70	I71	I72	I73	I74	I75	I76	I77	I78	I79	I80	I81	I82	I83	I84	I85	I86	I87	I88	I89	I90	I91	I92	I93	I94	I95	I96	I97	I98	I99	I100	I101	I102	I103	I104	I105	I106	I107	I108	I109	I110	I111	I112	I113	I114	I115	I116	I117	I118	I119	I120	I121	I122	I123	I124	I125	I126	I127	I128	I129	I130	I131	I132	I133	I134	I135	I136	I137	I138	I139	I140	I141	I142	I143	I144	I145	I146	I147	I148	I149	I150	I151	I152	I153	I154	I155	I156	I157	I158	I159	I160	I161	I162	I163	I164	I165	I166	I167	I168	I169	I170	I171	I172	I173	I174	I175	I176	I177	I178	I179	I180	I181	I182	I183	I184	I185	I186	I187	I188	I189	I190	I191	I192	I193	I194	I195	I196	I197	I198	I199	I200	I201	I202	I203	I204	I205	I206	I207	I208	I209	I210	I211	I212	I213	I214	I215	I216	I217	I218	I219	I220	I221	I222	I223	I224	I225	I226	I227	I228	I229	I230	I231	I232	I233	I234	I235	I236	I237	I238	I239	I240	I241	I242	I243	I244	I245	I246	I247	I248	I249	I250	I251	I252	I253	I254	I255	I256	I257	I258	I259	I260	I261	I262	I263	I264	I265	I266	I267	I268	I269	I270	I271	I272	I273	I274	I275	I276	I277	I278	I279	I280	I281	I282	I283	I284	I285	I286	I287	I288	I289	I290	I291	I292	I293	I294	I295	I296	I297	I298	I299	I300	I301	I302	I303	I304	I305	I306	I307	I308	I309	I310	I311	I312	I313	I314	I315	I316	I317	I318	I319	I320	I321	I322	I323	I324	I325	I326	I327	I328	I329	I330	I331	I332	I333	I334	I335	I336	I337	I338	I339	I340	I341	I342	I343	I344	I345	I346	I347	I348	I349	I350	I351	I352	I353	I354	I355	I356	I357	I358	I359	I360	I361	I362	I363	I364	I365	I366	I367	I368	I369	I370	I371	I372	I373	I374	I375	I376	I377	I378	I379	I380	I381	I382	I383	I384	I385	I386	I387	I388	I389	I390	I391	I392	I393	I394	I395	I396	I397	I398	I399	I400	I401	I402	I403	I404	I405	I406	I407	I408	I409	I410	I411	I412	I413	I414	I415	I416	I417	I418	I419	I420	I421	I422	I423	I424	I425	I426	I427	I428	I429	I430	I431	I432	I433	I434	I435	I436	I437	I438	I439	I440	I441	I442	I443	I444	I445	I446	I447	I448	I449	I450	I451	I452	I453	I454	I455	I456	I457	I458	I459	I460	I461	I462	I463	I464	I465	I466	I467	I468	I469	I470	I471	I472	I473	I474	I475	I476	I477	I478	I479	I480	I481	I482	I483	I484	I485	I486	I487	I488	I489	I490	I491	I492	I493	I494	I495	I496	I497	I498	I499	I500	I501	I502	I503	I504	I505	I506	I507	I508	I509	I510	I511	I512	I513	I514	I515	I516	I517	I518	I519	I520	I521	I522	I523	I524	I525	I526	I527	I528	I529	I530	I531	I532	I533	I534	I535	I536	I537	I538	I539	I540	I541	I542	I543	I544	I545	I546	I547	I548	I549	I550	I551	I552	I553	I554	I555	I556	I557	I558	I559	I560	I561	I562	I563	I564	I565	I566	I567	I568	I569	I570	I571	I572	I573	I574	I575	I576	I577	I578	I579	I580	I581	I582	I583	I584	I585	I586	I587	I588	I589	I590	I591	I592	I593	I594	I595	I596	I597	I598	I599	I600	I601	I602	I603	I604	I605	I606	I607	I608	I609	I610	I611	I612	I613	I614	I615	I616	I617	I618	I619	I620	I621	I622	I623	I624	I625	I626	I627	I628	I629	I630	I631	I632	I633	I634	I635	I636	I637	I638	I639	I640	I641	I642	I643	I644	I645	I646	I647	I648	I649	I650	I651	I652	I653	I654	I655	I656	I657	I658	I659	I660	I661	I662	I663	I664	I665	I666	I667	I668	I669	I670	I671	I672	I673	I674	I675	I676	I677	I678	I679	I680	I681	I682	I683	I684	I685	I686	I687	I688	I689	I690	I691	I692	I693	I694	I695	I696	I697	I698	I699	I700	I701	I702	I703	I704	I705	I706	I707	I708	I709	I710	I711	I712	I713	I714	I715	I716	I717	I718	I719	I720	I721	I722	I723	I724	I725	I726	I727	I728	I729	I730	I731	I732	I733	I734	I735	I736	I737	I738	I739	I740	I741	I742	I743	I744	I745	I746	I747	I748	I749	I750	I751	I752	I753	I754	I755	I756	I757	I758	I759	I760	I761	I762	I763	I764	I765	I766	I767	I768	I769	I770	I771	I772	I773	I774	I775	I776	I777	I778	I779	I780	I781	I782	I783	I784	I785	I786	I787	I788	I789	I790	I791	I792	I793	I794	I795	I796	I797	I798	I799	I800	I801	I802	I803	I804	I805	I806	I807	I808	I809	I810	I811	I812	I813	I814	I815	I816	I817	I818	I819	I820	I821	I822	I823	I824	I825	I826	I827	I828	I829	I830	I831	I832	I833	I834	I835	I836	I837	I838	I839	I840	I841	I842	I843	I844	I845	I846	I847	I848	I849	I850	I851	I852	I853	I854	I855	I856	I857	I858	I859	I860	I861	I862	I863	I864	I865	I866	I867	I868	I869	I870	I871	I872	I873	I874	I875	I876	I877	I878	I879	I880	I881	I882	I883	I884	I885	I886	I887	I888	I889	I890	I891	I892	I893	I894	I895	I896	I897	I898	I899	I900	I901	I902	I903	I904	I905	I906	I907	I908	I909	I910	I911	I912	I913	I914	I915	I916	I917	I918	I919	I920	I921	I922	I923	I924	I925	I926	I927	I928	I929	I930	I931	I932	I933	I934	I935	I936	I937	I938	I939	I940	I941	I942	I943	I944	I945	I946	I947	I948	I949	I950	I951	I952	I953	I954	I955	I956	I957	I958	I959	I960	I961	I962	I963	I964	I965	I966	I967	I968	I969	I970	I971	I972	I973	I974	I975	I976	I977	I978	I979	I980	I981	I982	I983	I984	I985	I986	I987	I988	I989	I990	I991	I992	I993	I994	I995	I996	I997	I998	I999	I1000	I1001	I1002	I1003	I1004	I1005	I1006	I1007	I1008	I1009	I1010	I1011	I1012	I1013	I1014	I1015	I1016	I1017	I1018	I1019	I1020	I1021	I1022	I1023	I1024	I1025	I1026	I1027	I1028	I1029	I1030	I1031	I1032	I1033	I1034	I1035	I1036	I1037	I1038	I1039	I1040	I1041	I1042	I1043	I1044	I1045	I1046	I1047	I1048	I1049	I1050	I1051	I1052	I1053	I1054	I1055	I1056	I1057	I1058	I1059	I1060	I1061	I1062	I1063	I1064	I1065	I1066	I1067	I1068	I1069	I1070	I1071	I1072	I1073	I1074	I1075	I1076	I1077	I1078	I1079	I1080	I1081	I1082	I1083	I1084	I1085	I1086	I1087	I1088	I1089	I1090	I1091	I1092	I1093	I1094	I1095	I1096	I1097	I1098	I1099	I1100	I1101	I1102	I1103	I1104	I1105	I1106	I1107	I1108	I1109	I1110	I1111	I1112	I1113	I1114	I1115	I1116	I1117	I1118	I1119	I1120	I1121	I1122	I1123	I1124	I1125	I1126	I1127	I1128	I1129	I1130	I1131	I1132	I1133	I1134	I1135	I1136	I1137	I1138	I1139	I1140	I1141	I1142	I1143	I1144	I1145	I1146	I1147	I1148	I1149	I1150	I1151	I1152	I1153	I1154	I1155	I1156	I1157	I1158	I1159	I1160	I1161	I1162	I1163	I1164	I1165	I1166	I1167	I1168	I1169	I1170	I1171	I1172	I1173	I1174	I1175	I1176	I1177	I1178	I1179	I1180	I1181	I1182	I1183	I1184	I1185	I1186	I1187	I1188	I1189	I1190	I1191	I1192	I1193	I1194	I1195	I1196	I1197	I1198	I1199	I1200	I1201	I1202	I1203	I1204	I1205	I1206	I1207	I1208	I1209	I1210	I1211	I1212	I1213	I1214	I1215	I1216	I1217	I1218	I1219	I1220	I1221	I1222	I1223	I1224	I1225	I1226	I1227	I1228	I1229	I1230	I1231	I1232	I1233	I1234	I1235	I1236	I1237	I1238	I1239	I1240	I1241	I1242	I1243	I1244	I1245	I1246	I1247	I1248	I1249	I1250	I1251	I1252	I1253	I1254	I1255	I1256	I1257	I1258	I1259	I1260	I1261	I1262	I1263	I1264	I1265	I1266	I1267	I1268	I1269	I1270	I1271	I1272	I1273	I1274	I1275	I1276	I1277	I1278	I1279	I1280	I1281	I1282	I1283	I1284	I1285	I1286	I1287	I1288	I1289	I1290	I1291	I1292	I1293	I1294	I1295	I1296	I1297	I1298	I1299	I1300	I1301	I1302	I1303	I1304	I1305	I1306	I1307	I1308	I1309	I1310	I1311	I1312	I1313	I1314	I1315	I1316	I1317	I1318	I1319	I1320	I1321	I1322	I1323	I1324	I1325	I1326	I1327	I1328	I1329	I1330	I1331	I1332	I1333	I1334	I1335	I1336	I1337	I1338	I1339	I1340	I1341	I1342	I1343	I1344	I1345	I1346	I1347	I1348	I1349	I1350	I1351	I1352	I1353	I1354	I1355	I1356	I1357	I1358	I1359	I1360	I1361	I1362	I1363	I1364	I1365	I1366	I1367	I1368	I1369	I1370	I1371	I1372	I1373	I1374	I1375	I1376	I1377	I1378	I1379	I1380	I1381	I1382	I1383	I1384	I1385	I1386	I1387	I1388	I1389	I1390	I1391	I1392	I1393	I1394	I1395	I1396	I1397	I1398	I1399	I1400	I1401	I1402	I1403	I1404	I1405	I1406	I1407	I1408	I1409	I1410	I1411	I1412	I1413	I1414	I1415	I1416	I1417	I1418	I1419	I1420	I1421	I1422	I1423	I1424	I1425	I1426	I1427	I1428	I1429	I1430	I1431	I1432	I1433	I1434	I1435	I1436	I1437	I1438	I1439	I1440	I1441	I1442	I1443	I1444	I1445	I1446	I1447	I1448	I1449	I1450	I1451	I1452	I1453	I1454	I1455	I1456	I1457	I1458	I1459	I1460	I1461	I1462	I1463	I1464	I1465	I1466	I1467	I1468	I1469	I1470	I1471	I1472	I1473	I1474	I1475	I1476	I1477	I1478	I1479	I1480	I1481	I1482	I1483	I1484	I1485	I1486	I1487	I1488	I1489	I1490	I1491	I1492	I1493	I1494	I1495	I1496	I1497	I1498
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Adr. **STRCMP RA,RB**

Speichere RA nach r14 (weil RA nicht inkrementiert werden kann)

420	516	X	X	X	ZA	ADD	RAMA	X	I R	14	M R	H	H	C10	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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**Speichere [r14] nach r15. Vergleiche [RB] mit r15.**

421		X	X	X	ZB	ADD	NOP	X	X	14	M R	A B	H	CIO	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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422		X	D	X	DZ	ADD	RAMA	X	X	15	M R	H	H	C	0	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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423		X	X	X	ZB	ADD	NOP	X	X	X	I R	A B	H	CIO	X	H	H	X	X	CONT	X	H	H	H	H	H	H
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424	DIS	X	D	X	DA	SUBR	NOP	15	M R	X	X	H	H	CH	X	H	L	LOAD	X	CONT	X	H	H	H	H	H	R
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Springe zu IFETCH, wenn [r14] ungleich [RB]

425		X	X	X	X	X	NOP	X	X	X	X	H	H	X	X	H	H	MA-NoIZ	C	GJP	IFETCH	H	H	H	H	H	R
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Springe zu IFETCH, wenn r15 gleich 0

426	DIS	X	X	X	ZA	ADD	NOP	15	M R	X	X	H	H	C	O	X	H	L	LOAD	X	CONT	X	H	H	H	H	R
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427	MS	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X	H	H	MA-Zero	C	CJP	IFETCH	H	H	H	H	H	R
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Inkrementiere r14 und RB, weiter

	X	X	X		X	H	H	X	X	CONT	X	H	H	H	H	H	R
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	SIC	X	X	X		X	H	H	X		H	H	H	H	H	R
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# STRCMP RA, RB

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Adr. **STRCMP RA,RB**

Speichere RA nach r14 (weil RA nicht inkrementiert werden kann)

420	SI	X	X	X	ZA	ADD	RAMA	X	I R	14	M R	H	H	C/O	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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**Speichere [r14] nach r15. Vergleiche [RB] mit r15.**

421	STC	X	X	X	ZB	ADD	NOP	X	X	14	M R	A B	H	C/O	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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422		X	D	X	DZ	ADD	RAMA	X	X	15	M R	H	H	C	0	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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423	DES	X	X	X	ZB	ADD	NOP	X	X	X	I R	A B	H	CIO	X	H	H	X	X	CONT	X	H	H	H	H	H	H
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424	DIS	X	D	X	DA	SUBR	NOP	15	M R	X	X	H	H	CH	X	H	L	LOAD	X	CONT	X	H	H	H	H	H	R
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Springe zu IFETCH, wenn [r14] ungleich [RB]

425		X	X	X	X	X	NOP	X	X	X	X	H	H	X	X	H	H	MA-NoIZ	C	GJP	IFETCH	H	H	H	H	H	R
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Springe zu IFETCH, wenn r15 gleich 0

426	DIS	X	X	X	ZA	ADD	NOP	15	M R	X	X	H	H	C	O	X	H	L	LOAD	X	CONT	X	H	H	H	H	R
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427	MS	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X	H	H	MA-Zero	C	CJP	IFETCH	H	H	H	H	H	R
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Inkrementiere r14 und RB, weiter

428	X	X	X		X	H	H	X	X	CONT	X	H	H	H	H	H	R
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	SIC	X	X	X		X	H	H	X		H	H	H	H	H	R
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# STRCMP RA, RB

76	75	74	73	72	71	70	69	68	67	66	65	64	63	62	61	60	59	58	57	56	55	54	53	52	51	50	49	48	47	46	45	44	43	42	41	40	39	38	37	36	35	34	33	32	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0			
IE	I3	I2	I1	I0	KMUX	K15	K14	K13	K12	K11	K10	K9	K8	K7	K6	K5	K4	K3	K2	K1	K0	G7	G6	G5	G4	G3	G2	G1	G0	A3	A2	A1	A0	ASEL	E3	E2	E1	E0	RSFL	ABUS*	EBUS*	I12	I11	I10	I9	I8	I7	I6	CEMUJ*	CEM*	I5	I4	I3	I2	I1	I0	CCEN*	D3	D2	D1	D0	D11	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0	EZ_LD*	EZ_ED*	EZ_INC*	EZ_EA*	TR_LD*	MWE*
Interrupt					Konstante																Src		Func		Dest		RA Addr		RB Addr		Y-MUX		CIN MUX		Schiebesteu- erung				Statusregister Test				AM2910 Befehle		Direktdaten																																		

Adr. **STRCMP RA,RB**

Speichere RA nach r14 (weil RA nicht inkrementiert werden kann)

420	516	X	X	X	ZA	ADD	RAMA	X	I R	14	M R	H	H	C10	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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**Speichere [r14] nach r15. Vergleiche [RB] mit r15.**

421	STC	X	X	X	ZB	ADD	NOP	X	X	14	M R	A B	H	C/O	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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422		X	D	X	DZ	ADD	RAMA	X	X	15	M R	H	H	C	0	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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423	DES	X	X	X	ZB	ADD	NOP	X	X	X	I R	A B	H	CIO	X	H	H	X	X	CONT	X	H	H	H	H	H	H
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424	DIS	X	D	X	DA	SUBR	NOP	15	M R	X	X	H	H	CH	X	H	L	LOAD	X	CONT	X	H	H	H	H	H	R
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Springe zu IFETCH, wenn [r14] ungleich [RB]

425		X	X	X	X	X	NOP	X	X	X	X	H	H	X	X	H	H	MA-NoIZ	C	GJP	IFETCH	H	H	H	H	H	R
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Springe zu IFETCH, wenn r15 gleich 0

426	DIS	X	X	X	ZA	ADD	NOP	15	M R	X	X	H	H	C	O	X	H	L	LOAD	X	CONT	X	H	H	H	H	R
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427	MS	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X	H	H	MA-Zero	C	CJP	IFETCH	H	H	H	H	H	R
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Inkrementiere r14 und RB, weiter

428	X	X	X	ZB	ADD	RAMA	X	X	14	M R	H	H	CH	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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	SIC	X	X	X		X	H	H	X		H	H	H	H	H	R
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# STRCMP RA, RB

I0	I1	I2	I3	I4	I5	I6	I7	I8	I9	I10	I11	I12	I13	I14	I15	I16	I17	I18	I19	I20	I21	I22	I23	I24	I25	I26	I27	I28	I29	I30	I31	I32	I33	I34	I35	I36	I37	I38	I39	I40	I41	I42	I43	I44	I45	I46	I47	I48	I49	I50	I51	I52	I53	I54	I55	I56	I57	I58	I59	I60	I61	I62	I63	I64	I65	I66	I67	I68	I69	I70	I71	I72	I73	I74	I75	I76	I77	I78	I79	I80	I81	I82	I83	I84	I85	I86	I87	I88	I89	I90	I91	I92	I93	I94	I95	I96	I97	I98	I99	I100	I101	I102	I103	I104	I105	I106	I107	I108	I109	I110	I111	I112	I113	I114	I115	I116	I117	I118	I119	I120	I121	I122	I123	I124	I125	I126	I127	I128	I129	I130	I131	I132	I133	I134	I135	I136	I137	I138	I139	I140	I141	I142	I143	I144	I145	I146	I147	I148	I149	I150	I151	I152	I153	I154	I155	I156	I157	I158	I159	I160	I161	I162	I163	I164	I165	I166	I167	I168	I169	I170	I171	I172	I173	I174	I175	I176	I177	I178	I179	I180	I181	I182	I183	I184	I185	I186	I187	I188	I189	I190	I191	I192	I193	I194	I195	I196	I197	I198	I199	I200	I201	I202	I203	I204	I205	I206	I207	I208	I209	I210	I211	I212	I213	I214	I215	I216	I217	I218	I219	I220	I221	I222	I223	I224	I225	I226	I227	I228	I229	I230	I231	I232	I233	I234	I235	I236	I237	I238	I239	I240	I241	I242	I243	I244	I245	I246	I247	I248	I249	I250	I251	I252	I253	I254	I255	I256	I257	I258	I259	I260	I261	I262	I263	I264	I265	I266	I267	I268	I269	I270	I271	I272	I273	I274	I275	I276	I277	I278	I279	I280	I281	I282	I283	I284	I285	I286	I287	I288	I289	I290	I291	I292	I293	I294	I295	I296	I297	I298	I299	I300	I301	I302	I303	I304	I305	I306	I307	I308	I309	I310	I311	I312	I313	I314	I315	I316	I317	I318	I319	I320	I321	I322	I323	I324	I325	I326	I327	I328	I329	I330	I331	I332	I333	I334	I335	I336	I337	I338	I339	I340	I341	I342	I343	I344	I345	I346	I347	I348	I349	I350	I351	I352	I353	I354	I355	I356	I357	I358	I359	I360	I361	I362	I363	I364	I365	I366	I367	I368	I369	I370	I371	I372	I373	I374	I375	I376	I377	I378	I379	I380	I381	I382	I383	I384	I385	I386	I387	I388	I389	I390	I391	I392	I393	I394	I395	I396	I397	I398	I399	I400	I401	I402	I403	I404	I405	I406	I407	I408	I409	I410	I411	I412	I413	I414	I415	I416	I417	I418	I419	I420	I421	I422	I423	I424	I425	I426	I427	I428	I429	I430	I431	I432	I433	I434	I435	I436	I437	I438	I439	I440	I441	I442	I443	I444	I445	I446	I447	I448	I449	I450	I451	I452	I453	I454	I455	I456	I457	I458	I459	I460	I461	I462	I463	I464	I465	I466	I467	I468	I469	I470	I471	I472	I473	I474	I475	I476	I477	I478	I479	I480	I481	I482	I483	I484	I485	I486	I487	I488	I489	I490	I491	I492	I493	I494	I495	I496	I497	I498	I499	I500	I501	I502	I503	I504	I505	I506	I507	I508	I509	I510	I511	I512	I513	I514	I515	I516	I517	I518	I519	I520	I521	I522	I523	I524	I525	I526	I527	I528	I529	I530	I531	I532	I533	I534	I535	I536	I537	I538	I539	I540	I541	I542	I543	I544	I545	I546	I547	I548	I549	I550	I551	I552	I553	I554	I555	I556	I557	I558	I559	I560	I561	I562	I563	I564	I565	I566	I567	I568	I569	I570	I571	I572	I573	I574	I575	I576	I577	I578	I579	I580	I581	I582	I583	I584	I585	I586	I587	I588	I589	I590	I591	I592	I593	I594	I595	I596	I597	I598	I599	I600	I601	I602	I603	I604	I605	I606	I607	I608	I609	I610	I611	I612	I613	I614	I615	I616	I617	I618	I619	I620	I621	I622	I623	I624	I625	I626	I627	I628	I629	I630	I631	I632	I633	I634	I635	I636	I637	I638	I639	I640	I641	I642	I643	I644	I645	I646	I647	I648	I649	I650	I651	I652	I653	I654	I655	I656	I657	I658	I659	I660	I661	I662	I663	I664	I665	I666	I667	I668	I669	I670	I671	I672	I673	I674	I675	I676	I677	I678	I679	I680	I681	I682	I683	I684	I685	I686	I687	I688	I689	I690	I691	I692	I693	I694	I695	I696	I697	I698	I699	I700	I701	I702	I703	I704	I705	I706	I707	I708	I709	I710	I711	I712	I713	I714	I715	I716	I717	I718	I719	I720	I721	I722	I723	I724	I725	I726	I727	I728	I729	I730	I731	I732	I733	I734	I735	I736	I737	I738	I739	I740	I741	I742	I743	I744	I745	I746	I747	I748	I749	I750	I751	I752	I753	I754	I755	I756	I757	I758	I759	I760	I761	I762	I763	I764	I765	I766	I767	I768	I769	I770	I771	I772	I773	I774	I775	I776	I777	I778	I779	I780	I781	I782	I783	I784	I785	I786	I787	I788	I789	I790	I791	I792	I793	I794	I795	I796	I797	I798	I799	I800	I801	I802	I803	I804	I805	I806	I807	I808	I809	I810	I811	I812	I813	I814	I815	I816	I817	I818	I819	I820	I821	I822	I823	I824	I825	I826	I827	I828	I829	I830	I831	I832	I833	I834	I835	I836	I837	I838	I839	I840	I841	I842	I843	I844	I845	I846	I847	I848	I849	I850	I851	I852	I853	I854	I855	I856	I857	I858	I859	I860	I861	I862	I863	I864	I865	I866	I867	I868	I869	I870	I871	I872	I873	I874	I875	I876	I877	I878	I879	I880	I881	I882	I883	I884	I885	I886	I887	I888	I889	I890	I891	I892	I893	I894	I895	I896	I897	I898	I899	I900	I901	I902	I903	I904	I905	I906	I907	I908	I909	I910	I911	I912	I913	I914	I915	I916	I917	I918	I919	I920	I921	I922	I923	I924	I925	I926	I927	I928	I929	I930	I931	I932	I933	I934	I935	I936	I937	I938	I939	I940	I941	I942	I943	I944	I945	I946	I947	I948	I949	I950	I951	I952	I953	I954	I955	I956	I957	I958	I959	I960	I961	I962	I963	I964	I965	I966	I967	I968	I969	I970	I971	I972	I973	I974	I975	I976	I977	I978	I979	I980	I981	I982	I983	I984	I985	I986	I987	I988	I989	I990	I991	I992	I993	I994	I995	I996	I997	I998	I999	I1000	I1001	I1002	I1003	I1004	I1005	I1006	I1007	I1008	I1009	I1010	I1011	I1012	I1013	I1014	I1015	I1016	I1017	I1018	I1019	I1020	I1021	I1022	I1023	I1024	I1025	I1026	I1027	I1028	I1029	I1030	I1031	I1032	I1033	I1034	I1035	I1036	I1037	I1038	I1039	I1040	I1041	I1042	I1043	I1044	I1045	I1046	I1047	I1048	I1049	I1050	I1051	I1052	I1053	I1054	I1055	I1056	I1057	I1058	I1059	I1060	I1061	I1062	I1063	I1064	I1065	I1066	I1067	I1068	I1069	I1070	I1071	I1072	I1073	I1074	I1075	I1076	I1077	I1078	I1079	I1080	I1081	I1082	I1083	I1084	I1085	I1086	I1087	I1088	I1089	I1090	I1091	I1092	I1093	I1094	I1095	I1096	I1097	I1098	I1099	I1100	I1101	I1102	I1103	I1104	I1105	I1106	I1107	I1108	I1109	I1110	I1111	I1112	I1113	I1114	I1115	I1116	I1117	I1118	I1119	I1120	I1121	I1122	I1123	I1124	I1125	I1126	I1127	I1128	I1129	I1130	I1131	I1132	I1133	I1134	I1135	I1136	I1137	I1138	I1139	I1140	I1141	I1142	I1143	I1144	I1145	I1146	I1147	I1148	I1149	I1150	I1151	I1152	I1153	I1154	I1155	I1156	I1157	I1158	I1159	I1160	I1161	I1162	I1163	I1164	I1165	I1166	I1167	I1168	I1169	I1170	I1171	I1172	I1173	I1174	I1175	I1176	I1177	I1178	I1179	I1180	I1181	I1182	I1183	I1184	I1185	I1186	I1187	I1188	I1189	I1190	I1191	I1192	I1193	I1194	I1195	I1196	I1197	I1198	I1199	I1200	I1201	I1202	I1203	I1204	I1205	I1206	I1207	I1208	I1209	I1210	I1211	I1212	I1213	I1214	I1215	I1216	I1217	I1218	I1219	I1220	I1221	I1222	I1223	I1224	I1225	I1226	I1227	I1228	I1229	I1230	I1231	I1232	I1233	I1234	I1235	I1236	I1237	I1238	I1239	I1240	I1241	I1242	I1243	I1244	I1245	I1246	I1247	I1248	I1249	I1250	I1251	I1252	I1253	I1254	I1255	I1256	I1257	I1258	I1259	I1260	I1261	I1262	I1263	I1264	I1265	I1266	I1267	I1268	I1269	I1270	I1271	I1272	I1273	I1274	I1275	I1276	I1277	I1278	I1279	I1280	I1281	I1282	I1283	I1284	I1285	I1286	I1287	I1288	I1289	I1290	I1291	I1292	I1293	I1294	I1295	I1296	I1297	I1298	I1299	I1300	I1301	I1302	I1303	I1304	I1305	I1306	I1307	I1308	I1309	I1310	I1311	I1312	I1313	I1314	I1315	I1316	I1317	I1318	I1319	I1320	I1321	I1322	I1323	I1324	I1325	I1326	I1327	I1328	I1329	I1330	I1331	I1332	I1333	I1334	I1335	I1336	I1337	I1338	I1339	I1340	I1341	I1342	I1343	I1344	I1345	I1346	I1347	I1348	I1349	I1350	I1351	I1352	I1353	I1354	I1355	I1356	I1357	I1358	I1359	I1360	I1361	I1362	I1363	I1364	I1365	I1366	I1367	I1368	I1369	I1370	I1371	I1372	I1373	I1374	I1375	I1376	I1377	I1378	I1379	I1380	I1381	I1382	I1383	I1384	I1385	I1386	I1387	I1388	I1389	I1390	I1391	I1392	I1393	I1394	I1395	I1396	I1397	I1398	I1399	I1400	I1401	I1402	I1403	I1404	I1405	I1406	I1407	I1408	I1409	I1410	I1411	I1412	I1413	I1414	I1415	I1416	I1417	I1418	I1419	I1420	I1421	I1422	I1423	I1424	I1425	I1426	I1427	I1428	I1429	I1430	I1431	I1432	I1433	I1434	I1435	I1436	I1437	I1438	I1439	I1440	I1441	I1442	I1443	I1444	I1445	I1446	I1447	I1448	I1449	I1450	I1451	I1452	I1453	I1454	I1455	I1456	I1457	I1458	I1459	I1460	I1461	I1462	I1463	I1464	I1465	I1466	I1467	I1468	I1469	I1470	I1471	I1472	I1473	I1474	I1475	I1476	I1477	I1478	I1479	I1480	I1481	I1482	I1483	I1484	I1485	I1486	I1487	I1488	I1489	I1490	I1491	I1492	I1493	I1494	I1495	I1496	I1497	I1498
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Adr. **STRCMP RA,RB**

Speichere RA nach r14 (weil RA nicht inkrementiert werden kann)

420	X	X	X	ZA	ADD	RAMA	X	I R	14	M R	H	H	C	0	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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Speichere [r14] nach r15. Vergleiche [RB] mit r15.

421	DIS	X	X	X	ZB	ADD	NOP	X	X	14	M R	A B	H	C10	X	H	H	X	X	CONT	X	H	H	H	H	H	R
422	DIS	X	D	X	DZ	ADD	RAMA	X	X	15	M R	H	H	C10	X	H	H	X	X	CONT	X	H	H	H	H	H	R
423	DIS	X	X	X	ZB	ADD	NOP	X	X	X	I R	A B	H	C10	X	H	H	X	X	CONT	X	H	H	H	H	H	H
424	DIS	X	D	X	DA	SUBR	NOP	15	M R	X	X	H	H	C11	X	H	L	LOAD	X	CONT	X	H	H	H	H	H	R

Springe zu IFETCH, wenn [r14] ungleich [RB]

425	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X	H	H	MA-NoZ	C	GJP	IFETCH	H	H	H	H	H	R
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Springe zu IFETCH, wenn r15 gleich 0

426	DIS	X	X	X	ZA	ADD	NOP	15	M H	X	X	H	H	C/O	X	H	L	LOAD	X	CONT	X	H	H	H	H	H	R
427	DIS	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X	H	H	MA-Zero	C	CJP	IFETCH	H	H	H	H	H	R

Inkrementiere r14 und RB, weiter

428	EDIS	X	X	X	ZB	ADD	RAMA	X	X	14	M R	H	H	C11	X	H	H	X	X	CONT	X	H	H	H	H	H	R		
429	EDIS	X	X	X											X	H	H	X						H	H	H	H	H	R



# STRCMP RA, RB

[illegible]

Adr. **STRCMP RA,RB**

Speichere RA nach r14 (weil RA nicht inkrementiert werden kann)

420	SEX	X	X	X	ZA	ADD	RAMA	X	I	14	M	H	H	C	0	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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**Speichere [r14] nach r15. Vergleiche [RB] mit r15.**

421	E15	X	X	X	ZB	ADD	NOP	X	X	14	M R	A B	H	C10	X	H	H	X	X	CONT	X	H	H	H	H	H	R
422	E15	X	D	X	DZ	ADD	RAMA	X	X	15	M R	H	H	C10	X	H	H	X	X	CONT	X	H	H	H	H	H	R
423	E15	X	X	X	ZB	ADD	NOP	X	X	X	I R	A B	H	C10	X	H	H	X	X	CONT	X	H	H	H	H	H	R
424	E15	X	D	X	DA	SUBR	NOP	15	M R	X	X	H	H	C11	X	H	L	LOAD	X	CONT	X	H	H	H	H	H	R

Springe zu IFETCH, wenn [r14] ungleich [RB]

425		X	X	X	X	X	NOP	X	X	X	X	H	H	X	X	H	H	MA-NoZ	C	GJP	IFETCH	H	H	H	H	H	R
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Springe zu IFETCH, wenn r15 gleich 0

426	DIS	X	X	X	ZA	ADD	NOP	15	M H	X	X	H	H	C/O	X	H	L	LOAD	X	CONT	X	H	H	H	H	H	R
427	DIS	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X	H	H	MA-Zero	C	CJP	IFETCH	H	H	H	H	H	R

Inkrementiere r14 und RB, weiter

428	DIS	X	X	X	ZB	ADD	RAMA	X	X	14	M R	H	H	CI1	X	H	H	X	X	CONT	X	H	H	H	H	H	R	
429	DIS	X	X	X	ZB	ADD	RAMA	X	X	X	I R	H	H	CI1	X	H	H	X					H	H	H	H	H	R

# STRCMP RA, RB

76	75	74	73	72	71	70	69	68	67	66	65	64	63	62	61	60	59	58	57	56	55	54	53	52	51	50	49	48	47	46	45	44	43	42	41	40	39	38	37	36	35	34	33	32	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0																																																								
IE	I3	I2	I1	I0	K15 K14 K13 K12 K11 K10 K9 K8 K7 K6 K5 K4 K3 K2 K1 K0																G7 G6 G5 G4 G3 G2 G1 G0 A3 A2 A1 A0 ASEL																E3 E2 E1 E0 RSEL																Y- MUX CIN- MUX Schiebe- steuerung																CEMUJ* CEM* IS I4 I3 I2 I1 I0 CCEN*																AM2910 Befehle																Direktdaten																															
Interrupt					Konstante																																Src		Func		Dest		RA Addr		RB Addr		Y- MUX		CIN- MUX		Schiebe- steuerung				Statusregister Test				AM2910 Befehle		Direktdaten																																																																							

Adr. **STRCMP RA,RB**

Speichere RA nach r14 (weil RA nicht inkrementiert werden kann)

420	516	X	X	X	ZA	ADD	RAMA	X	I R	14	M R	H	H	CIO	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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**Speichere [r14] nach r15. Vergleiche [RB] mit r15.**

421	STC	X	X	X	ZB	ADD	NOP	X	X	14	M R	A B	H	C/O	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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422		X	D	X	DZ	ADD	RAMA	X	X	15	M R	H	H	C	0	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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423	DES	X	X	X	ZB	ADD	NOP	X	X	X	I R	A B	H	CIO	X	H	H	X	X	CONT	X	H	H	H	H	H	H
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424	DIS	X	D	X	DA	SUBR	NOP	15	M R	X	X	H	H	CH	X	H	L	LOAD	X	CONT	X	H	H	H	H	H	R
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Springe zu IFETCH, wenn [r14] ungleich [RB]

425	DIS	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X	H	H	MA-NoIZ	C	GJP	IFETCH	H	H	H	H	H	R
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Springe zu IFETCH, wenn r15 gleich 0

426	DIS	X	X	X	ZA	ADD	NOP	15	M R	X	X	H	H	C	O	X	H	L	LOAD	X	CONT	X	H	H	H	H	R
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427	MS	X	X	X	X	X	NOP	X	X	X	X	H	H	X	X	H	H	MA-Zero	C	CJP	IFETCH	H	H	H	H	H	R
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Inkrementiere r14 und RB, weiter

428	X	X	X	ZB	ADD	RAMA	X	X	14	M R	H	H	CH	X	H	H	X	X	CONT	X	H	H	H	H	H	R
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429	X	X	X	ZB	ADD	RAMA	X	X	X	I R	H	H	CII	X	H	H	X	P S	CJP	421	H	H	H	H	H	R
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