

Introduction to ChimeraTK.

Part 1: The DeviceAccess library and the ControlSystemAdapter



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ChimeraTK

Control system and Hardware Interface with Mapped and Extensible Register-based device Abstraction Tool Kit



1 DeviceAccess

Register based access to (hardware) devices

2 ControlSystemAdapter

Making application implementations independent from the middleware

3 ApplicationCore (talk by Martin Hierholzer)

Improving abstraction of DeviceAccess and the ControlSystemAdapter, and allow more functionality

ChimeraTK

Control system and Hardware Interface with Mapped and Extensible Register-based device Abstraction Tool Kit



1 DeviceAccess

Register based access to (hardware) devices

2 ControlSystemAdapter

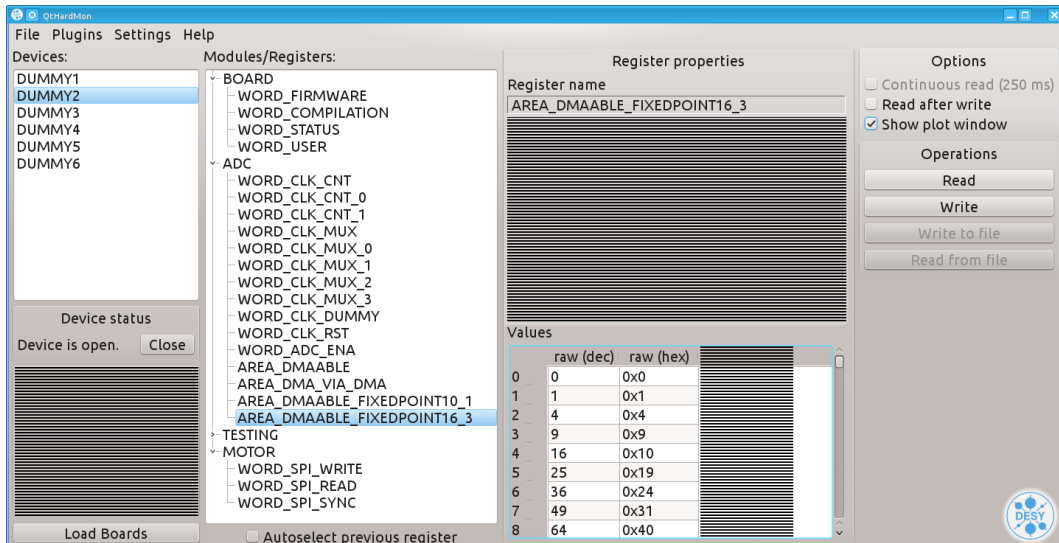
Making application implementations independent from the middleware

3 ApplicationCore (talk by Martin Hierholzer)

Improving abstraction of DeviceAccess and the ControlSystemAdapter, and allow more functionality

A register

- contains **data** (numerical or a string)
- is identified by a **name**
- lives on a **device**
- has a **length** ($1 \hat{=}$ scalar, $> 1 \hat{=}$ array)



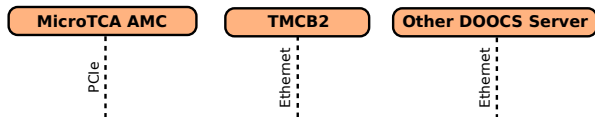
The screenshot shows the Qt Hardware Monitor (QtHardMon) application window. The interface is divided into several sections:

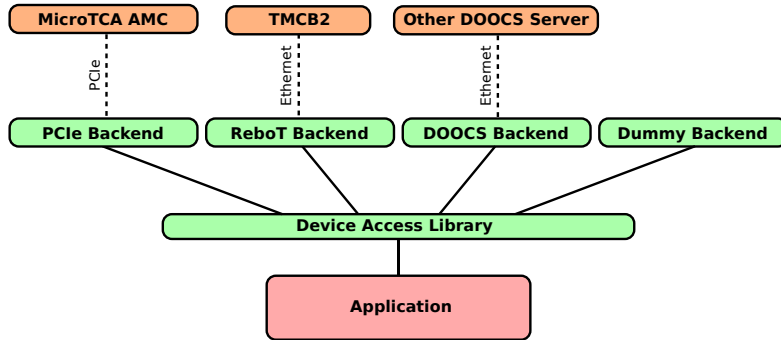
- File Plugins Settings Help**: The top menu bar.
- Devices:** A list of devices including DUMMY1 through DUMMY6. Below this is a "Device status" section showing "Device is open." and a "Close" button.
- Modules/Registers:** A tree view showing the hierarchy of modules and registers. The "AREA_DMAABLE_FIXEDPOINT16_3" register is selected.
- Register properties:** A section showing the selected register name and a large area for register values.
- Options:** A section with checkboxes for "Continuous read (250 ms)", "Read after write", and "Show plot window".
- Operations:** A section with buttons for "Read", "Write", "Write to file", and "Read from file".
- Values:** A table showing the raw (dec) and raw (hex) values for the selected register.

The "Values" table contains the following data:

	raw (dec)	raw (hex)
0	0	0x0
1	1	0x1
2	4	0x4
3	9	0x9
4	16	0x10
5	25	0x19
6	36	0x24
7	49	0x31
8	64	0x40

Live Demo (1)





- DeviceAccess identifies registers by name
- PCI Express identifies registers by address in a "Base Address Range" (BAR)

⇒ We need a mapping

Example map file

#name	n_words	address	n_bytes	BAR
heater.heatingCurrent	1	1024	4	2
heater.temperatureReadback	1	1028	4	2
heater.supplyVoltages	4	1032	16	2

- Map files are automatically created by the DESY (MSK) firmware framework
- Can easily be written manually


```
#include <mtca4u/Device.h>
#include <iostream>

int main(){

    mtca4u::Device d;
    d.open("sdm:///./pci:pciedevs6=oven.map");

}
```

Note: ChimeraTK was previously called MicroTCA.4 User Tool Kit (MTCA4U)

```
#include <mtca4u/Device.h>
#include <iostream>

int main(){

    mtca4u::Device d;
    d.open("sdm:///./pci:pciedevs6=oven.map");

    auto heatingCurrent
        = d.getScalarRegisterAccessor<int>("heater/heatingCurrent");

}
```

Note: ChimeraTK was previously called MicroTCA.4 User Tool Kit (MTCA4U)

```
#include <mtca4u/Device.h>
#include <iostream>

int main(){

    mtca4u::Device d;
    d.open("sdm:///./pci:pciedevs6=oven.map");

    auto heatingCurrent
        = d.getScalarRegisterAccessor<int>("heater/heatingCurrent");

    heatingCurrent.read();
    std::cout << "Heating current is " << heatingCurrent << std::endl;

}
```

Note: ChimeraTK was previously called MicroTCA.4 User Tool Kit (MTCA4U)

```
#include <mtca4u/Device.h>
#include <iostream>

int main(){

    mtca4u::Device d;
    d.open("sdm:///pci:pcidevs6=oven.map");

    auto heatingCurrent
        = d.getScalarRegisterAccessor<int>("heater/heatingCurrent");

    heatingCurrent.read();
    std::cout << "Heating current is " << heatingCurrent << std::endl;

    heatingCurrent += 3;
    heatingCurrent.write();

}
```

Note: ChimeraTK was previously called MicroTCA.4 User Tool Kit (MTCA4U)

```
#include <mtca4u/Device.h>
#include <iostream>

int main(){

    mtca4u::Device d;
    d.open("sdm:///./pci:pciedevs6=oven.map");

    auto heatingCurrent
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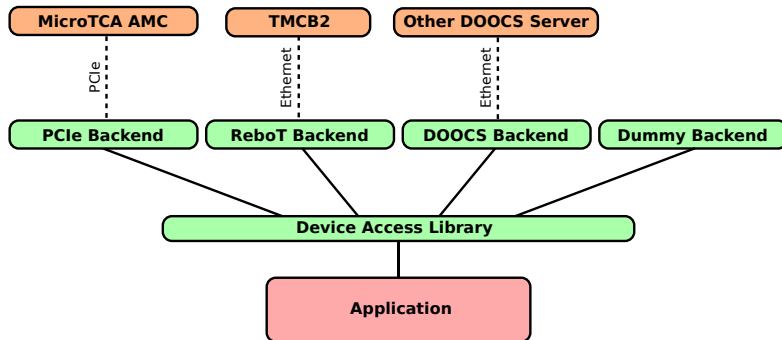
    heatingCurrent.read();
    std::cout << "Heating current is " << heatingCurrent << std::endl;

    heatingCurrent += 3;
    heatingCurrent.write();

}
```

Live Demo (2)

Note: ChimeraTK was previously called MicroTCA.4 User Tool Kit (MTCA4U)



More abstraction: Identify devices by an alias name

Example device map file

#alias_name	URI	map_file
oven	sdm:///pci:pciedevs6	oven.map
#oven	sdm:///dummy=oven.map	oven.map

- Client code identifies devices by functional name
- Actual implementation can be changed at run time

```
#include <mtca4u/Device.h>
#include <mtca4u/Utilities.h>
#include <iostream>

int main(){

    mtca4u::setDMapFilePath("devices.dmap");
    mtca4u::Device d;
    d.open("oven");

    auto heatingCurrent
        = d.getScalarRegisterAccessor<int>("heater/heatingCurrent");

    heatingCurrent.read();
    std::cout << "Heating current is " << heatingCurrent << std::endl;

    heatingCurrent += 3;
    heatingCurrent.write();

}
```

Live Demo (3)

- Firmware often uses fixed-point arithmetic
 - CPU uses floating point
 - Transport layer (PCI Express) uses 32 bit words
- ⇒ Extend the mapping with conversion information^{*}

Example map file

#name	n_words	address	n_bytes	BAR	n_bits	n_fractionalBits	signed
heater.heatingCurrent	1	102	4	2	32	0	0
heater.temperatureReadback	1	102	4	2	16	3	1
heater.supplyVoltages	4	103	16	2	32	0	0

^{*} Optional, default conversion is 32 bit signed integer, no fractional bits

```
#include <mtca4u/Device.h>
#include <mtca4u/Utilities.h>
#include <iostream>

int main(){

    mtca4u::setDMapFilePath("devices.dmap");
    mtca4u::Device d;
    d.open("oven");

    auto temperature
        = d.getScalarRegisterAccessor<float>("heater/temperatureReadback");

    temperature.read();
    std::cout << "Readback temperature is " << temperature << std::endl;

}
```

```
#include <mtca4u/Device.h>
#include <mtca4u/Utilities.h>
#include <iostream>

int main(){

    mtca4u::setDMapFilePath("devices.dmap");
    mtca4u::Device d;
    d.open("oven");

    auto supplyVoltages
        = d.getOneDRegisterAccessor<int>("heater/supplyVoltages");

    supplyVoltages.read();

    std::cout << "Supply voltages are ";
    for (size_t i = 0; i < supplyVoltages.getNElements(); ++i){
        std::cout << supplyVoltages[i] << " ";
    }
    std::cout << std::endl;
}
```

```
#include <mtca4u/Device.h>
#include <mtca4u/Utilities.h>
#include <iostream>

int main(){

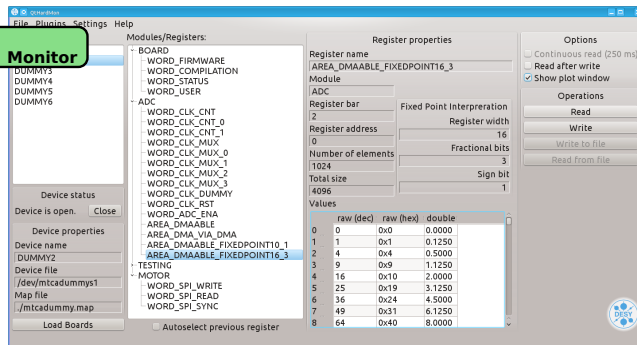
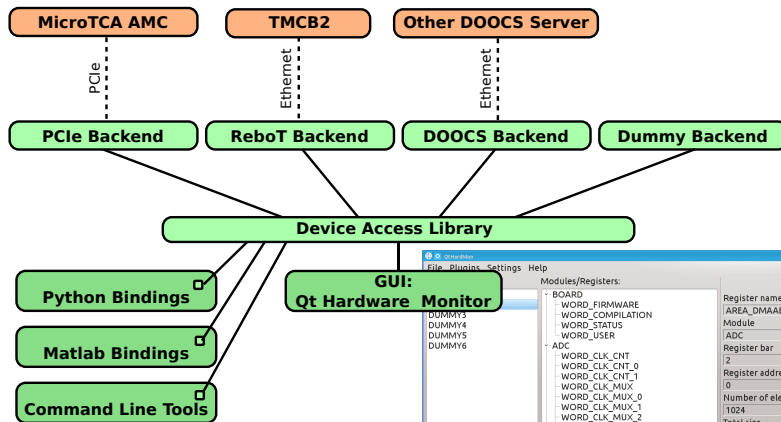
    mtca4u::setDMapFilePath("devices.dmap");
    mtca4u::Device d;
    d.open("oven");

    auto supplyVoltages
        = d.getOneDRegisterAccessor<int>("heater/supplyVoltages");

    supplyVoltages.read();

    std::cout << "Supply voltages are ";
    for (auto voltage : supplyVoltages){
        std::cout << voltage << " ";
    }
    std::cout << std::endl;
}
```

Live Demo (5)



C++

```
#include <mtca4u/Device.h>
#include <mtca4u/Utilities.h>

int main(){
    mtca4u::setDMapFilePath("devices.dmap");
    mtca4u::Device d;
    d.open("oven");

    // "inefficient" shortcut to read a variable
    int temperature = d.read<float>("heater/temperatureReadback");
}
```

Python

```
import mtca4u

mtca4u.set_dmap_location('devices.dmap')
d = mtca4u.Device('oven')

temperature = d.read('heater', 'temperatureReadback')
```

Matlab

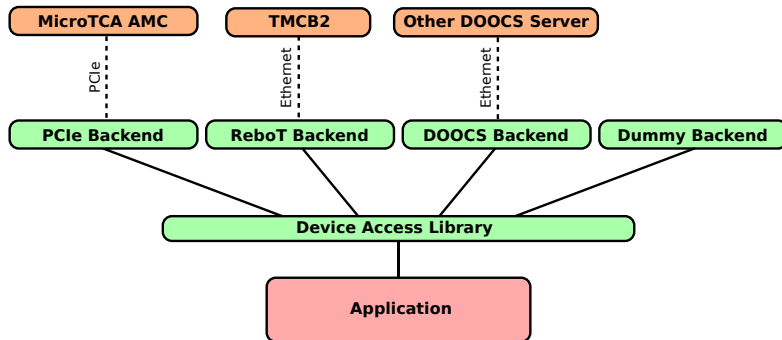
```
mtca4u.setDMapFilePath('devices.dmap')  
d = mtca4u('oven')  
  
temperature = d.read('heater','temperatureReadback')
```

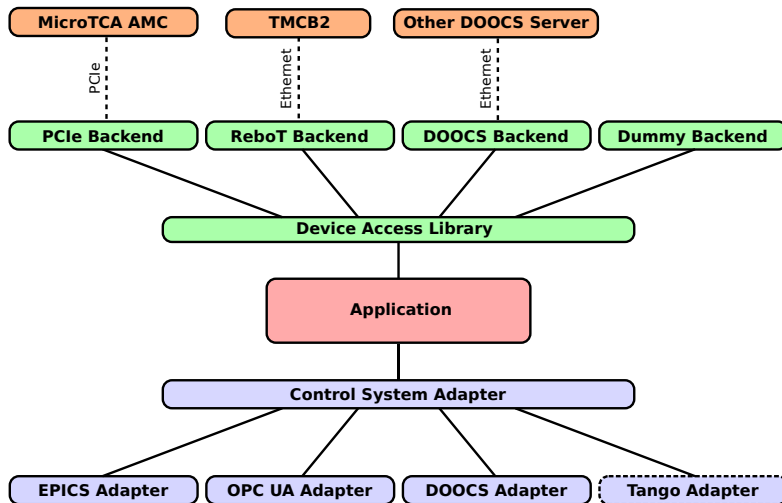
Command line

```
$ mtca4u read oven heater temperatureReadback
```

- All needed arguments in one call
- Takes the first dmap-file it finds :-O

Live Demo (6)





Typical Scenario: Integrating a **small** device

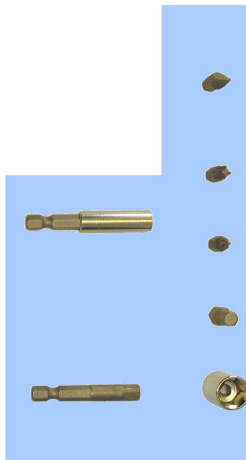
Integrate the device into your **EPICS** environment

- Just a few Process Variables
- ⇒ Write a new EPICS IOC, not too much work...

Integrate the same device into a **DOOCS** environment

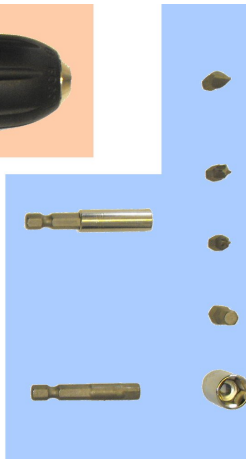
- Just a few Process Variables
- DOOCS and EPICS are very different, not much code to reuse:
Better start from scratch
- ⇒ Write a new DOOCS device server, not too much work...







Device



Adapter



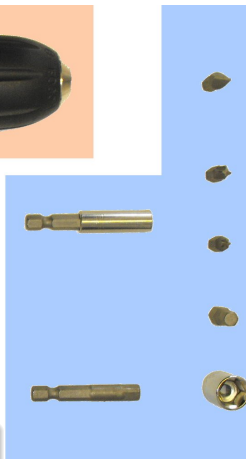
Control
System





EXAMPLE: LLRF Server

- $\mathcal{O}(400)$ process variables
- iterative learning algorithm
- feed forward table calculation



Adapter



Control System

EXAMPLE: Target Control Systems

- DOOCS at FLASH, XFEL/DESY
- EPICS 3 at FLUTE/KIT
- WinCC/OPC UA at ELBE/HZDR
- EPICS 4 at TARLA

Task

Complex control algorithms should be used with different control systems.

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Complex control algorithms should be used with different control systems.

Requirements For Abstraction

- Keep application code control system independent
- The algorithm must interact with the control system
- Use functionality provided by the control system
- No device-dependent code on the control system side

Additional Requirements:

- Thread-safe
- Lock-free
- Must not copy large data objects (arrays)

Task

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First Implementation

- Uni-directional process variables to transfer data to/from the control system

Slide by Sebastian Marsching on the 2015 Matter and Technology meeting

Comparison of Control Systems

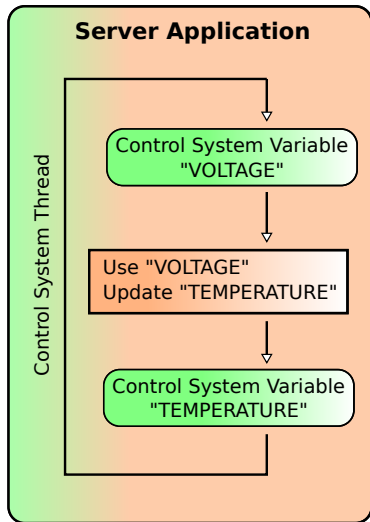
Control System	Device Description	Device Model	Mutex
DOOCS	code based	object oriented	per group
EPICS	configuration based	channel based	per PV
TANGO	code based	object oriented	?
WinCC OA	configuration based	channel based	no (single threaded)

Plus different handling of

- ▶ limits
- ▶ alarms
- ▶ engineering units
- ▶ etc.

Completely different locking schemes

- Locking cannot work
- ⇒ We need a lock-free implementation!



- Control system data types used inside the algorithm
- Control system variables can be locking/blocking
- Control system variables might not be thread safe
- Threading often handled by control system

Required abstraction for the ControlSystemAdapter:
Separate device logic and control system integration

Application code

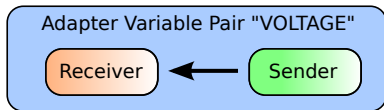
- Define process variables
- Implement algorithms
- Talk to hardware

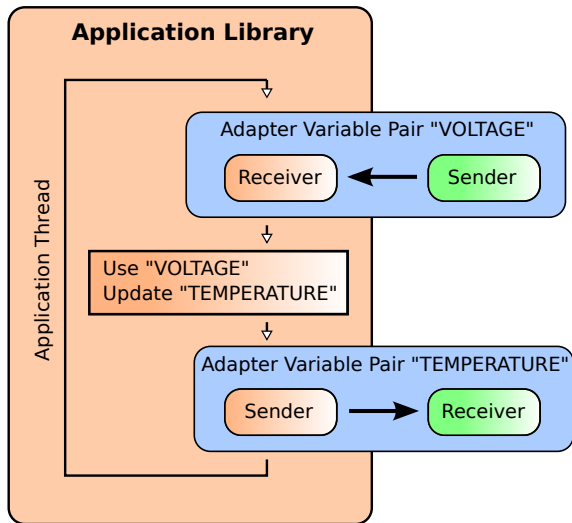
Control system “code”

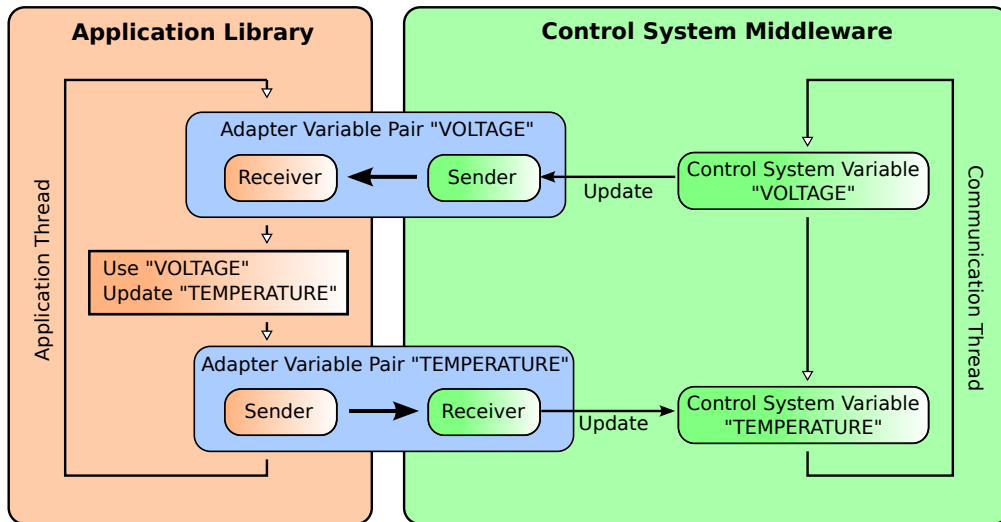
- Publish process variables via middleware
- Define variable name visible in control system
- Define middleware dependent features/data types
 - Histories
 - Display properties
- Application independent, configured via config file

Application and control system dependent code

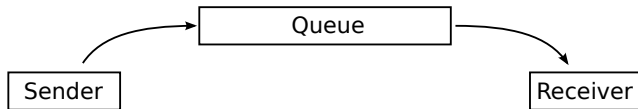
- **Avoid it!**





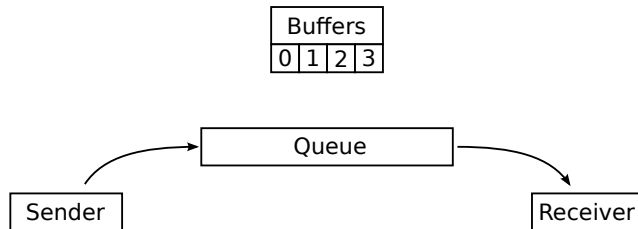


Use a queue: Allows processing a sequence of data and update notifications



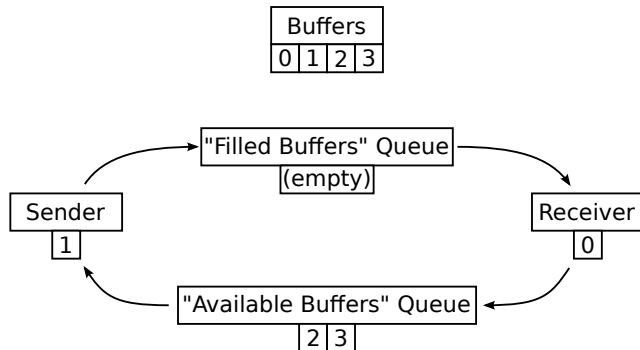
- Lock-free queue

Use a queue: Allows processing a sequence of data and update notifications



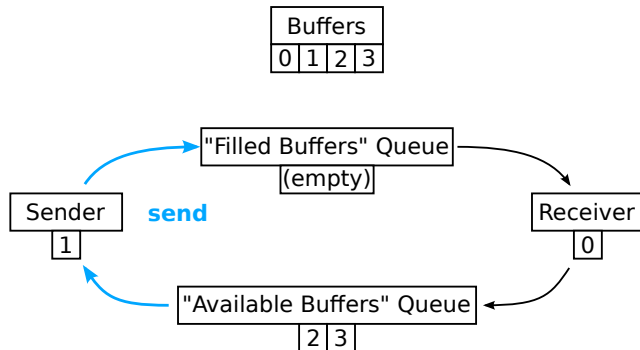
- Lock-free queue
- Pre-allocated buffers for arrays

Use a queue: Allows processing a sequence of data and update notifications



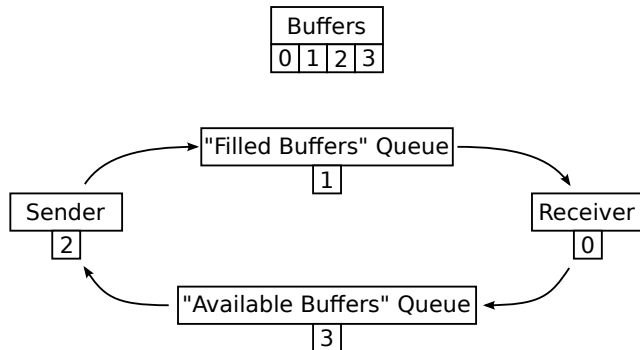
- Lock-free queues
- Pre-allocated buffers for arrays
- Copy references, not buffers

Use a queue: Allows processing a sequence of data and update notifications



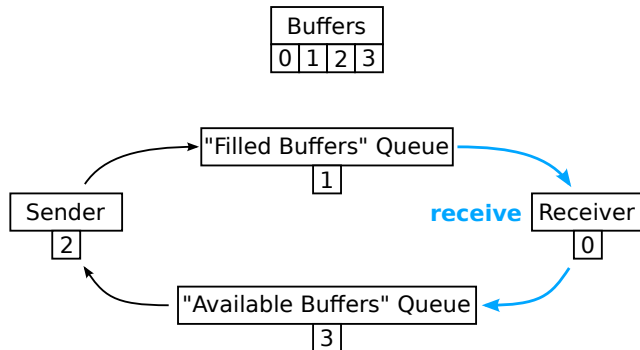
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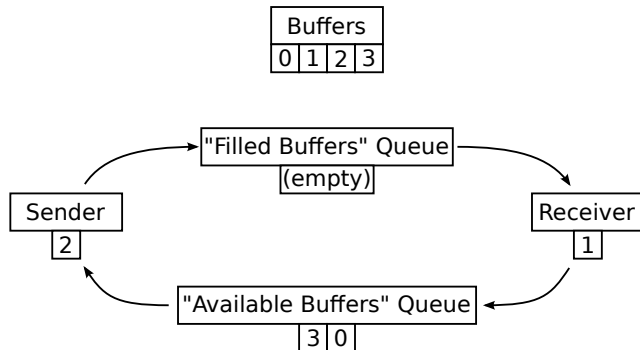
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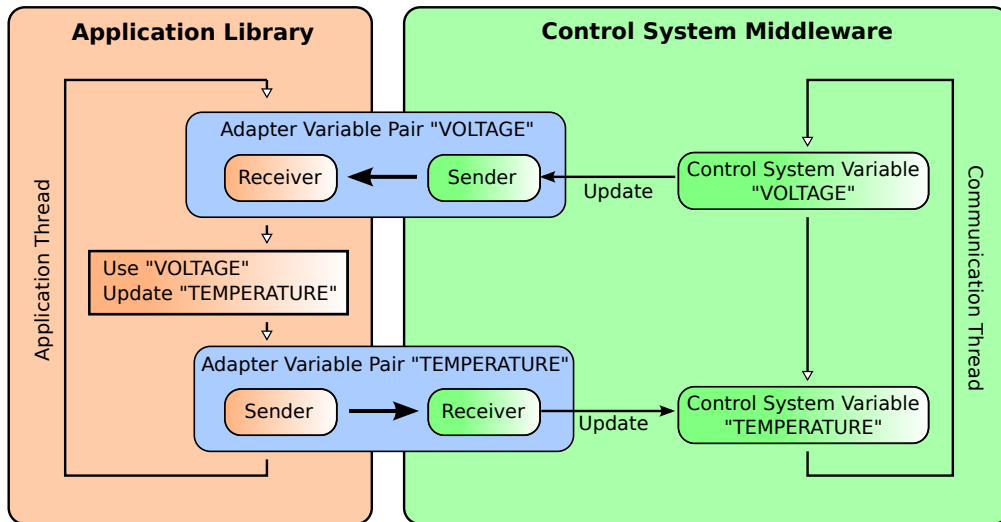


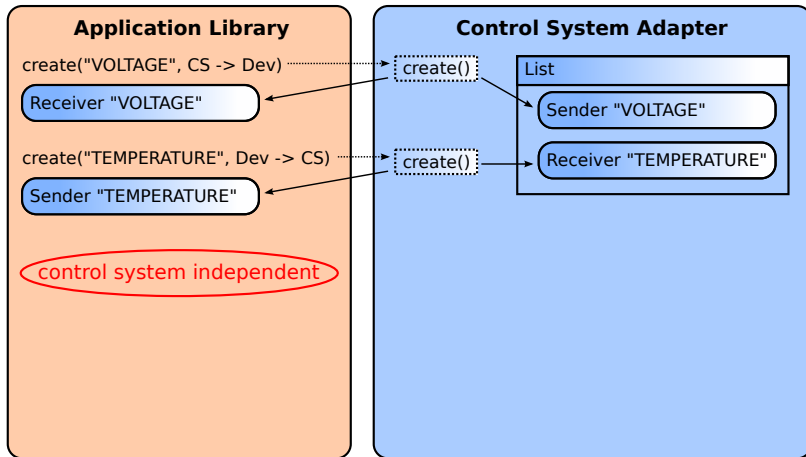
- Lock-free queues
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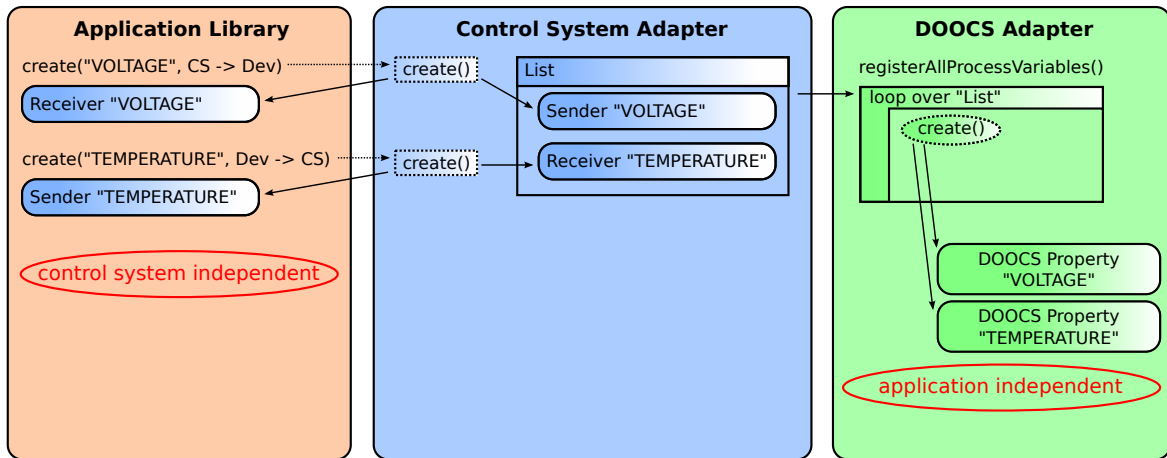
Use a queue: Allows processing a sequence of data and update notifications



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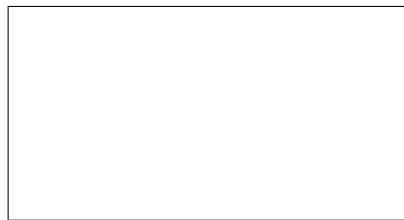




How many lines of C++ code do I need to integrate an existing application into my control system (e.g. DOOCS)?

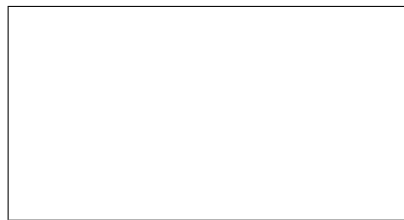
How many lines of C++ code do I need to integrate an existing application into my control system (e.g. DOOCS)?

Real code from the LLRF server



How many lines of C++ code do I need to integrate an existing application into my control system (e.g. DOOCS)?

Real code from the LLRF server



0 lines of code are needed. Just link it!

```
$ ld myApp.o -l ChimeraTK-ControlSystemAdapter-DoocsAdapter -o myAppDoocsServer
```

(You might need config files, or at least they improve the system integration.)

Now it's time to write an application!

Input tree

```
|-- oven1
|   |-- controller
|   |   |-- temperatureSetpoint
|   |   |-- temperatureReadback
|   |
|   |-- supplyVoltages
|
|-- oven2
|   |-- controller
|   |   |-- temperatureSetpoint
|   |   |-- temperatureReadback
|   |
|   |-- supplyVoltages
```

System Integration

Input tree

```
|-- oven1
|   |-- controller
|   |   |-- temperatureSetpoint
|   |   |-- temperatureReadback
|   |
|   |-- supplyVoltages
|
|-- oven2
|   |-- controller
|   |   |-- temperatureSetpoint
|   |   |-- temperatureReadback
|   |
|   |-- supplyVoltages
```

Output tree

```
|-- breadOven
|   |-- temperature
|
|-- cookieOven
|   |-- temperature
```

System Integration

- Names need to be adapted for the facility (manufacturer does not know if oven is used for bread or cookies)
- ⇒ Do it in system integration

Input tree

```
|-- oven1
|   |-- controller
|   |   |-- temperatureSetpoint
|   |   |-- temperatureReadback
|   |
|   |-- supplyVoltages
|
|-- oven2
|   |-- controller
|   |   |-- temperatureSetpoint
|   |   |-- temperatureReadback
|   |
|   |-- supplyVoltages
```

Output tree

```
|-- breadOven
|   |-- temperature
|
|-- cookieOven
|   |-- temperature
|
|-- expert
|   |-- temperatureSetpoints
|       |-- breadOven
|       |-- cookieOven
```

System Integration

- Names need to be adapted for the facility (manufacturer does not know if oven is used for bread or cookies)
⇒ Do it in system integration
- Naming depends on the middleware (e.g. DOOCS only has two hierarchy levels per server)
⇒ Has to be in the middleware-specific part of the adapter

Input tree

```
|-- oven1
|   |-- controller
|   |   |-- temperatureSetpoint
|   |   |-- temperatureReadback
|   |
|   |-- supplyVoltages
|
|-- oven2
|   |-- controller
|   |   |-- temperatureSetpoint
|   |   |-- temperatureReadback
|   |
|   |-- supplyVoltages
```

Output tree

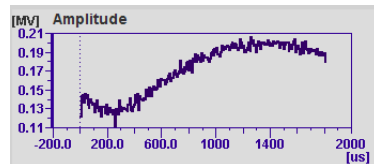
```
|-- breadOven
|   |-- temperature
|
|-- cookieOven
|   |-- temperature
|
|-- expert
|   |-- temperatureSetpoints
|   |   |-- breadOven
|   |   |-- cookieOven
|   |
|
|-- powerSupply
|   |-- fuse1
|   |   |-- breadOvenVoltages
|   |
|   |-- fuse2
|       |-- cookieOvenVoltages
```

System Integration

- Names need to be adapted for the facility (manufacturer does not know if oven is used for bread or cookies)
⇒ Do it in system integration
- Naming depends on the middleware (e.g. DOOCS only has two hierarchy levels per server)
⇒ Has to be in the middleware-specific part of the adapter

D_spectrum: Aggregated DOOCS data type for plotting

- Main data: 1D array
 - Meta data:
 - Engineering units
 - X-Axis scaling
 - ...
 - D_spectrum only known in DOOCS
- ⇒ Can only be configured during system integration
- Application is publishing main data and meta data^(*) (example):
 - Amplitude with EGUs
 - X-axis start with EGUs
 - X-axis step width



XML code

```
<D_spectrum source="Amplitude">  
  <startSource="Ampl_x_offset"/>  
  <incrementSource="Ampl_x_step"/>  
</D_spectrum>
```

Note: With the OPC UA adapter the published meta data is used in the panel to create the plot.

(*) Meta data can also be hard-coded in the XML config

Adapter for Process Variables

Decouple application logic and control system

- Generic part
- Control system specific part
 - Implementations for DOOCS and OPC UA
 - EPICS 3 adapter currently being updated

Design Goals

- Control system independent process variables ✓
- Thread safe ✓
- Lock free ✓
- Minimise copying ✓
- No device-dependent code on control system side ✓

Tools for System Integration

- Name mapping for Process Variables ✓
(should be available in every adapter impl.)
- Save and restore settings ✓
(default implementation in ControlSystemAdapter)
- Access to control system features:
Availability depends on the middleware
 - Display limits
 - Engineering units ✓
 - History
 - Handle alarms

Tools for writing virtual devices, functional mocks and plant models

Virtual Timing

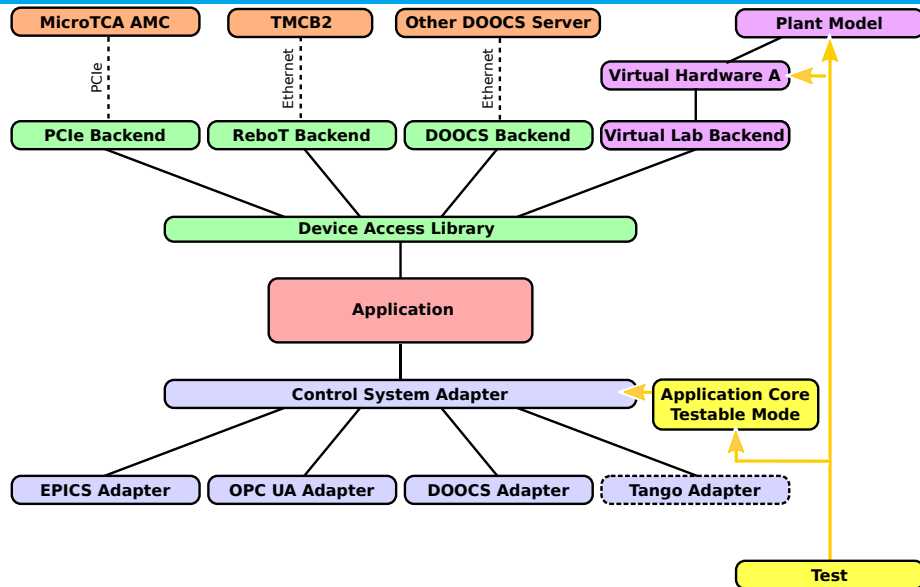
- Run test faster than the real time
- Simulation takes longer than real time → run application synchronously
- Test race conditions, check error handling

State Machine

- React on read/write to the device
- Easy implementation of firmware functionality

Signal Sinks/Sources

- Connect devices and plant models
- Modular plant models
- Planned: Share the same plant model across different applications





ControlSystemAdapter

- Use device logic with different control systems
- Implementations for DOOCS and OPC UA
- Epics 3 adapter is currently being updated

DeviceAccess Library

- Abstracted, register based hardware access
- Use real and virtual hardware, device servers
- Scripting tools and GUI

ApplicationCore Library

- Unifies DeviceAccess and ControlSystemAdapter
- Application modules
 - Input/output variables
- Hierarchical data model
 - Module and variable groups
 - Tags
- High abstraction level
 - Improves readability and reliability
 - Good maintainability

Software Repositories

All software is published under the GNU GPL or the GNU LGPL.

- ChimeraTK: <https://github.com/ChimeraTK>
- EPICS 3 Adapter: <http://oss.aquenos.com/svnroot/epics-mtca4u/>

Source code for the live demos: https://github.com/killenb/DeviceAccess_live_demo