



	1	2	3	4	5	6
	PART B					
A	pin gv1103g					A
	VCCTOA	SDMMC/UART/JTAG/PWM/I2C(3.3V only)				
	VCC104		8	GPIO3 Al u //SDAMC DET	UART	
		PWM8 'M0'		O GPIO3 A3 u SDMMC_D0	SDMMC D0	
		PWM9 ⁻ M0 JTAG_HPMCU_TCK_MI JTAG_CPU_TCK JTAG_HPMCU_TMS_MI JTAG_CPU_TMS	M0 UART5_RX_M0 SDMMC_D2 GPIO3_A7_u	4 GPIO3 A7 u SDMMC_D1	SDMMC_DI (some of	
		12C0_SCL_M2	TIARTS RTS MO STIMMO CIK GPIO3 A4 A	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	CLK	
			7	7		
			VCCIO4_VCC3V3	- E32		
		NSM / (13 PM / 17 O / ORTO / ORT / AUD. DOW		20nF 250V		
	VCCIO6	PWM/UART/I2C/SDIO/SPI/AUD_DSM	6	, <u> </u>		
В		SDIO_DI_ SDIO_DO_I 12C4_SCL_MI SDIO_CLK_I	MI SPI0_CS0_M0 PWM2_M2 GPIOI_C0_d = 0.0 SPI0_CCK_M0 PWM4_M2 GPIOI_C1_d = 0.0 SPI0_CCK_M0 PWM4_M2 GPIOI_C1_CCK_M0 PWM4_M2 GPIOI_C1_C1_CCK_M0 PWM4_M2 GPIOI_C1_C1_C1_C1_C1_C1_C1_C1_C1_C1_C1_C1_C1_	GPIOI_CI_d		В
		I2C4_SDA_MI SDIO_CMD_1	Al SPIO MISO MO PWM6 M2 GPIO1 C3 d 6	GPIOL C4 d		
		SDIO_D2_M	UARI4 TX MI PWM9 MI GPIOL C5 d	GPIO1_C5_d		
			UART4_CTS_MT_PWM11_IR_M1GPIO1_C7_d			
		UAR13 RX M	UART5_CTS_MI PWMI0_M2 GPI01_D1_d 5. UART5_RX_MI PWM0_M1 GPI01_D2_d 5. UART5_TX_MI PWM11_IR_M2 GPI01_D3_d 5.	// GLIOL_D2_d		
		12C3_SCL_M1 AUD_DSM_N	UARIS IX MI PWMII IR M2 GPIOI D3 d	GPIO1_D3_d		
			vccio6_vcc	- c34 2 0 VCC_	3V3	
				ysn sev VCC	1V8	
		MIPI_CSI/LVDS_RX/UART/I2C only,some for input only)		2		
C			LVDS RX D3N	MIPILY MIPILY	/DS_D3B	C
		LV	DS RX CLKIN MIPI CSI RX CLKIN GPI3 B2 d	// GI 15_B5_d	- VCC 1V8	
			LVDS RX D2N MIPI CSI RX D2N GPI3 B4 d 8 LVDS RX D2P MIPI CSI RX D2P GPI3 B5 d 8 LVDS RX D1N MIPI CSI RX D1N GPI3 B6 d 8 LVDS RX D1N MIPI CSI RX D1N GPI3 B6 d 8 RX D1	MIPI/LY	/DS_D2N /DS_D2P —	-
			FADS KY DIL WILL CSL BA CL NOVI CDIA CO. 11 8	MIP/LV	/DS_DIN	223 4-7≅
		Г	/DS RX CLK0P MIPI CSI RX CLK0P GPI3 C1 d1 8	MIPI/LV	/DS_CLKP /DS_D0N	2
			LVDS_RX_DOP MIPI_CSI_RX_DOP GPI3_C3_d MIPI_CLK0_OUT GPI03_C4_d	MIPI/LY MIPI/LY MIPI/LY	/DS_D0P /DS_MCLK0	MIPI_12C_SCL MIPI_12C_SDA
			PWM7 IR M2 MIPI CLKI OUT GPIO3 C6 d 3 12C4 SCL M2 UART5 TX M2 GPIO3 C7 d 5	GPIO3 C6 d → GPIO3_C6_d → MIPI_I2		MIPI_I2C_SDA
			12C4_SCL_M2 UARTS_TX_M2 GPIO3_C/_d	MIPI_I		
			MIPI_AVDD1V8 VCCIO7_VCC1V8	8 MIDI COL AMOD 1V8 P25 1 21 25 2	─○ VCC_1V8	
D				200nF 25R 26V		D
				-		
	1	2	3	4	5	6

