Practice_2

Combinational Logic Gates Design

Complete task1 and task2

1. Design a comparator to compare two bits, the output will be 1 if two bits are equal

2. Design a two - out - of - three voter logic circuit using logic gates only. The circuit has 3 inputs (A, B and C) and 1 output. The output is 1 if two or more of the inputs are 1

a) Draw a truth table with 3 input columns and 1 output column

b) Minimized Boolean expression

c) Implement your minimized expression using logic gates only (draw the circuit)