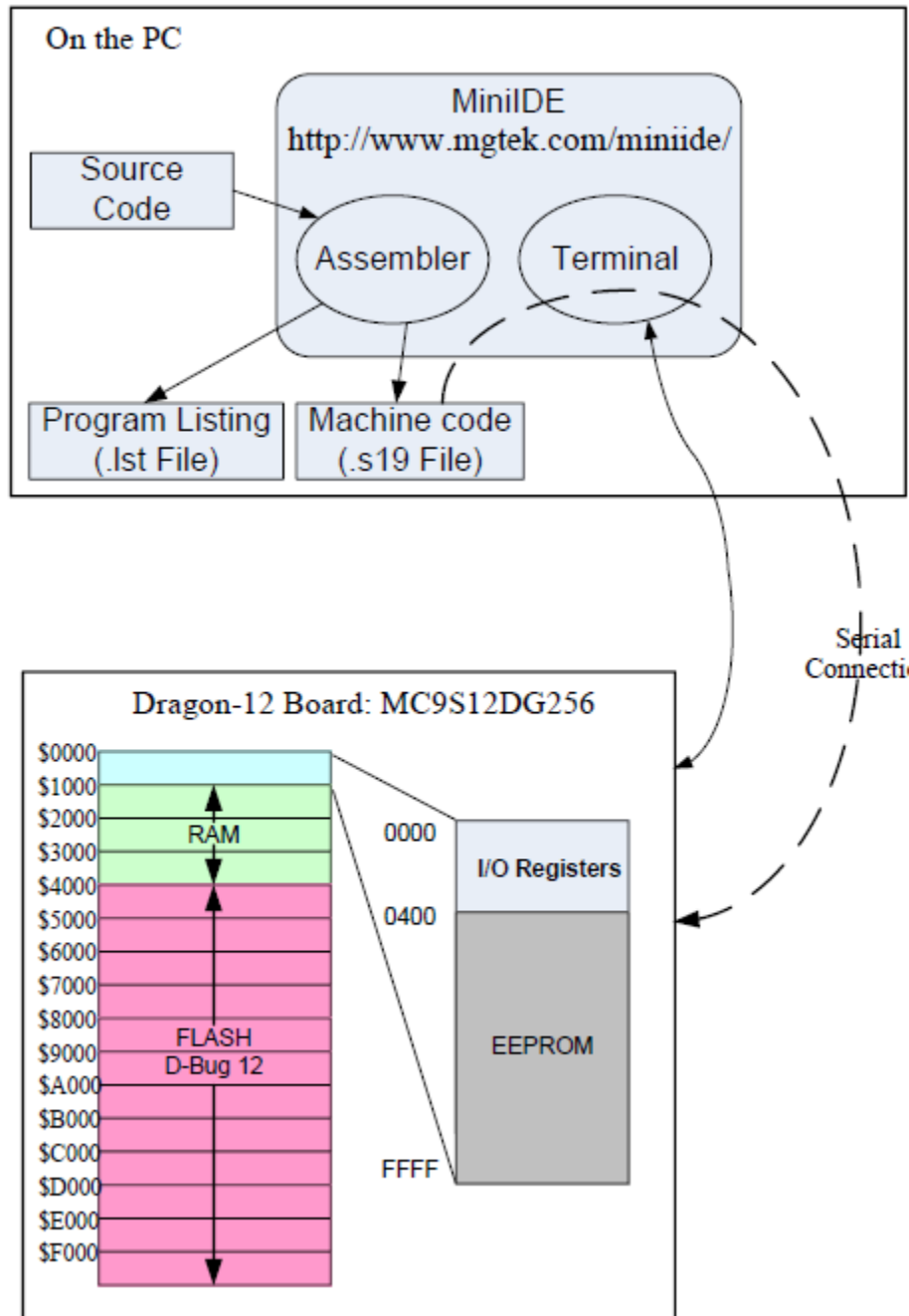
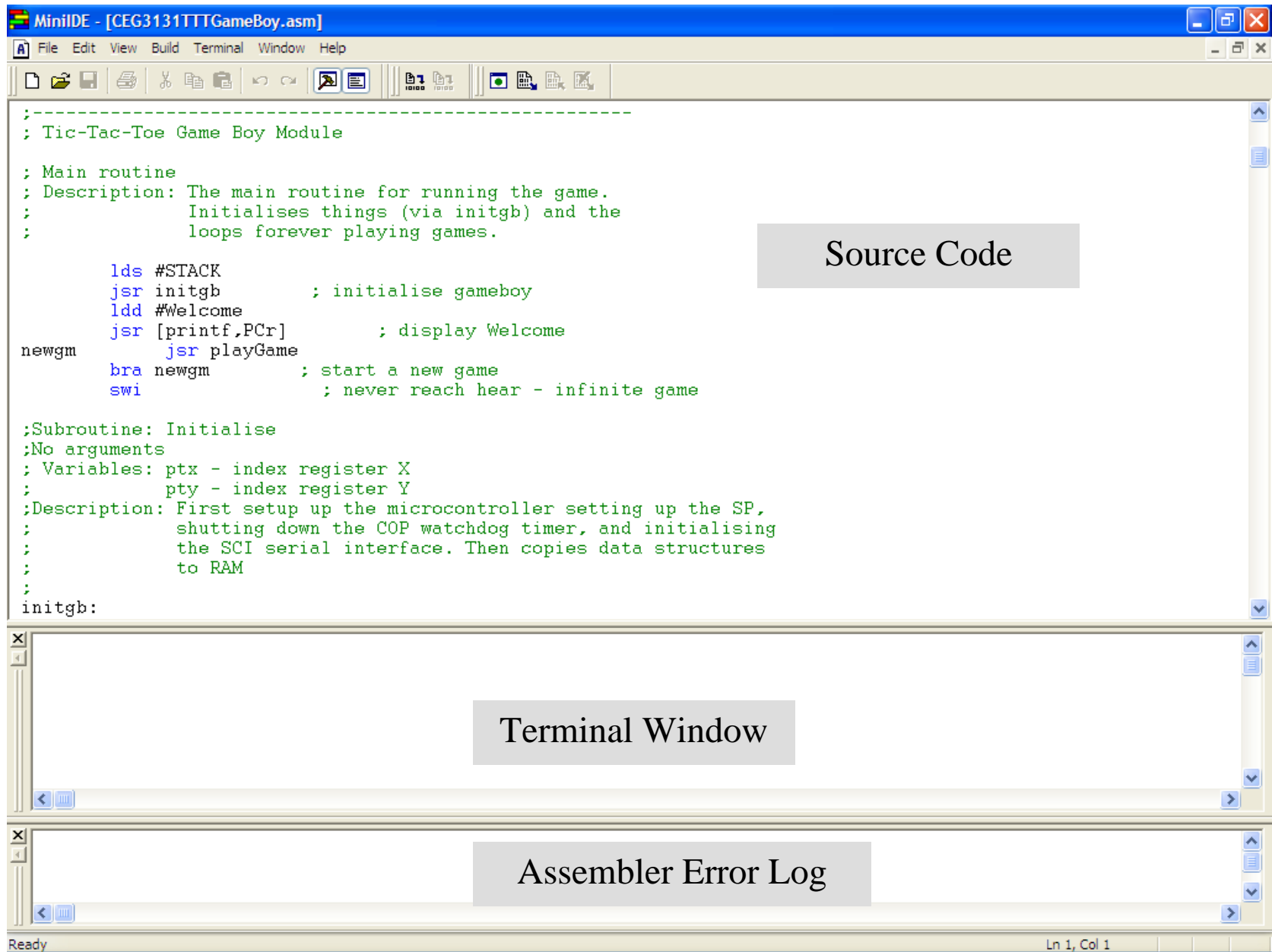
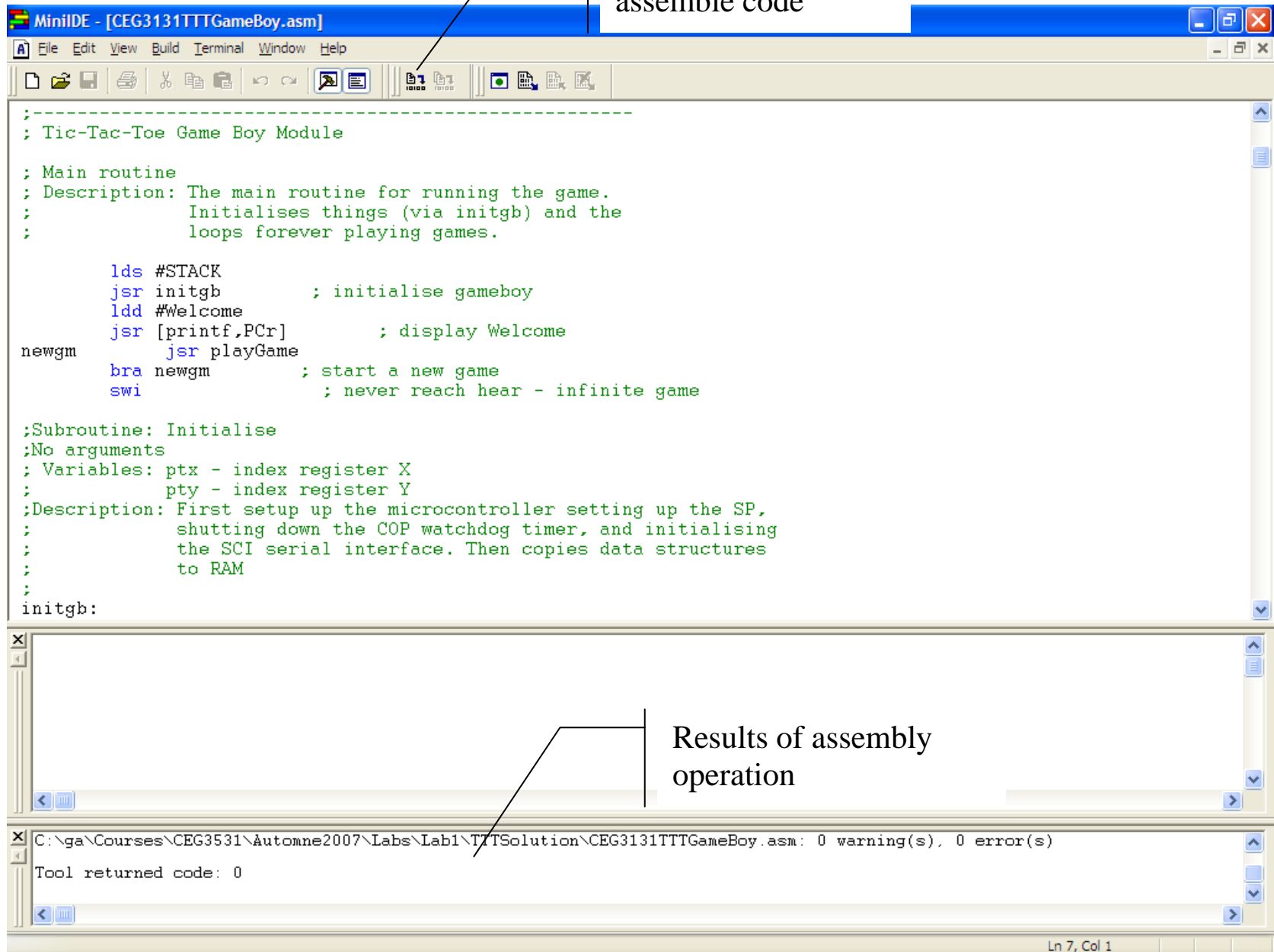


CEG 3136 – Computer Architecture II
Tutorial 1 - Introduction to Assembler
Programming Fall 2019







Contents of the S19 File

S0030000FC

S113**0400**CF2000160413CC068115FBEA7B16045397

S113**0410**20FB3F1410CE00000DE039800CE03A4080

S113**0420**86056AE03486016AE0350FE03708FB0C84

.

.

S113**0560**323DCE06BA0710B7101806180636CE0666

S113**0570**CF07043218063D34EC8015FBE90A15FB5D

S113**0580**E9023715FBE8FFCC071C15FBE8FA33C179

S113**0590**302D04C1332D09CC06A415FBE8EA20D87C

.

.

S112**0710**776F6E0A0D004472617721210A0D0084

S113**2500**202020202020202020000053434F52452B

S113**2510**3A20506C6179657220583A20303020504E

S113**2520**6C61796572204F3A2030300A0D00506C8E

S113**2530**617965722058206D6F76650A0D00203030

S113**2540**20202031202020320A0D2020207C202031

S113**2550**207C20202020300A0D2D2D2D2B2D2D2DDB

S113**2560**2B2D2D2D0A0D2020207C2020207C2020A6

S113**2570**2020310A0D2D2D2D2B2D2D2D2B2D2D2DE4

S113**2580**0A0D2020207C2020207C20202020320ABC

S105**2590**0D0038

S9030000FC

Contents of the S29 File

S2240F**0400**CF2000160413CC068115FBEA7B16045320FB3F1410CE00000DE039800CE03A401F
S2240F**0420**86056AE03486016AE0350FE03708FB0CE03980CC009C5CC8860C5ACBCE05F0CDF8
S2240F**0440**250B180A3070A60081FF26F679250979250A3DCE250034CC06E715FBEA2A1604AA

.

.

S2240F**0560**323DCE06BA0710B7101806180636CE06CF07043218063D34EC8015FBE90A15FB2C
S2240F**0580**E9023715FBE8FFCC071C15FBE8FA33C1302D04C1332D09CC06A415FBE8EA20D87E
S2240F**05A0**C030303DCE0678E63026040732200E180FC40F444444440705812027EA3D34CED0 .

.

.

S2240F**0700**6572650A0D005820776F6E0A0D004F20776F6E0A0D004472617721210A0D00FFCF
S2240F**2500**202020202020202020000053434F52453A20506C6179657220583A2030302050A2
S2240F**2520**6C61796572204F3A2030300A0D00506C617965722058206D6F76650A0D00203007
S2240F**2540**20202031202020320A0D2020207C2020207C20202020300A0D2D2D2D2B2D2D2D75
S2240F**2560**2B2D2D2D0A0D2020207C2020207C20202020310A0D2D2D2D2B2D2D2D2B2D2D2D13
S2240F**2580**0A0D2020207C2020207C20202020320A0D00FFFFFFFFFFFFFFFFFFFFFFFFFFFFFFFF9D
S9030000FC

Select « Options » in Terminal menu to get the options dialog

The screenshot shows the MiniIDE interface with the assembly code for a Tic-Tac-Toe Game Boy Module. The code includes a main routine and an initialization subroutine. The Options dialog box is open, showing the Terminal tab with COM settings and ASCII setup options.

Assembly Code:

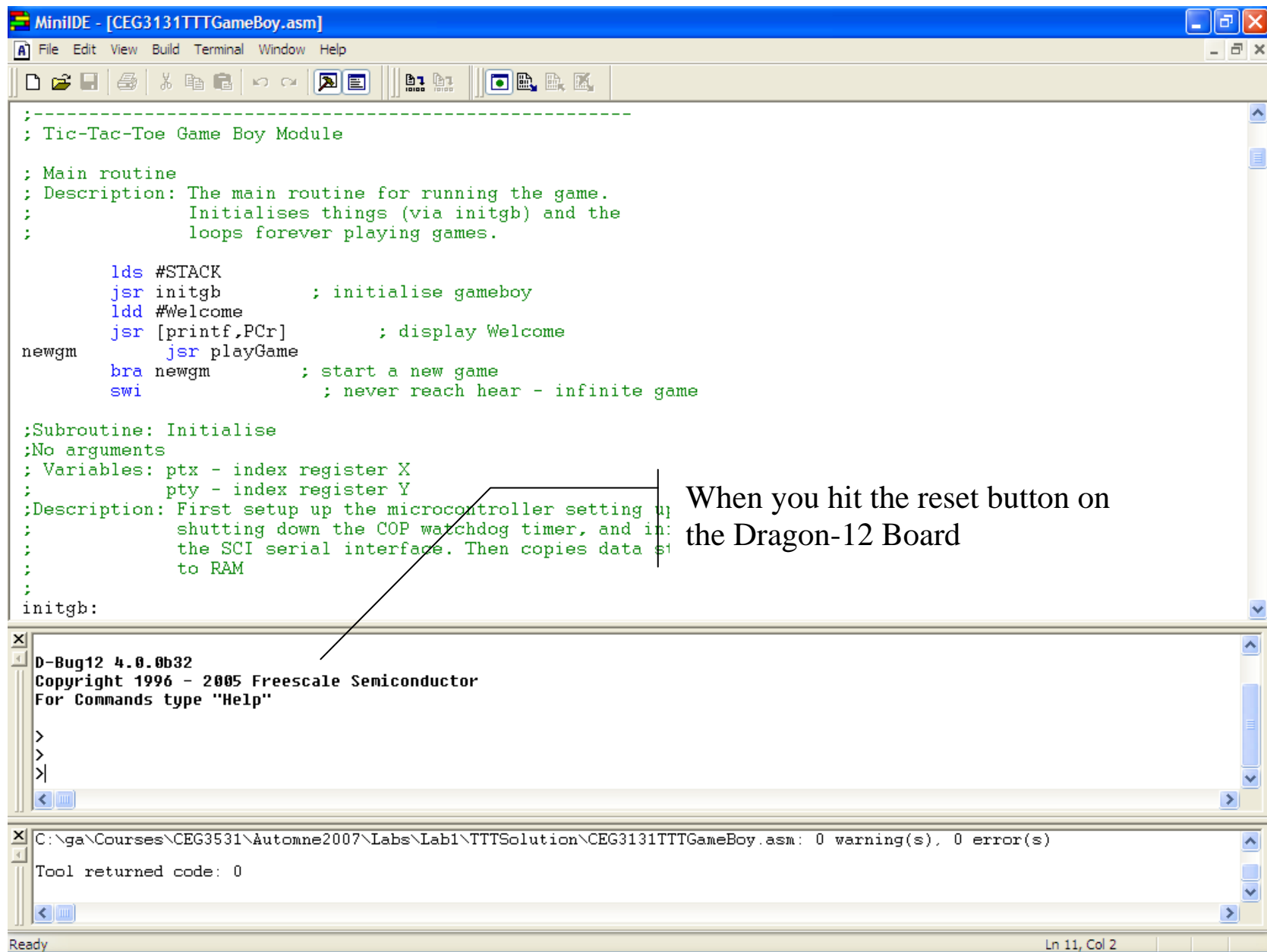
```
;-----  
; Tic-Tac-Toe Game Boy Module  
  
; Main routine  
; Description: The main routine for running the game  
; Initialises things (via initgb) and then  
; loops forever playing game  
  
    lds #STACK  
    jsr initgb      ; initialise game board  
    ldd #Welcome  
    jsr [printf,PCr] ; display welcome message  
newgm    jsr playGame  
    bra newgm      ; start a new game  
swi      ; never reach here  
  
;Subroutine: Initialise  
;No arguments  
; Variables: ptx - index register X  
;            pty - index register Y  
;Description: First setup up the microcontroller  
;             shutting down the COP watch  
;             the SCI serial interface.  
;             to RAM  
  
initgb:
```

Options Dialog Box:

- General Tab:** COM Settings. Specify the port and baudrate of the target system. Default values are COM2, 9600, 8, 1, n.
 - Port: COM4
 - Baud Rate: 9600
 - Data Bits: 8
 - Stop Bits: 1
 - Parity: None
 - Flow Control: ☐ DTR/DSR, ☐ RTS/CTS, ☒ XON/XOFF
- ASCII Setup Tab:**
 - ☐ Send line ends with carriage returns
 - Char delay: 0 milliseconds
 - Line delay: 0 milliseconds

Terminal Output:

```
C:\ga\Courses\CEG3531\Automne2007\Labs\Lab1\TTTSolution\CEG3131TTTGameBoy.asm: 0 warning(s), 0 error(s)  
Tool returned code: 0
```



2. Click on the download icon to open the "Download S-Record" (Can also select *Download File...* in the Terminal Menu; or hit the *F8* key)

3. Select the S19 file when downloading into RAM.

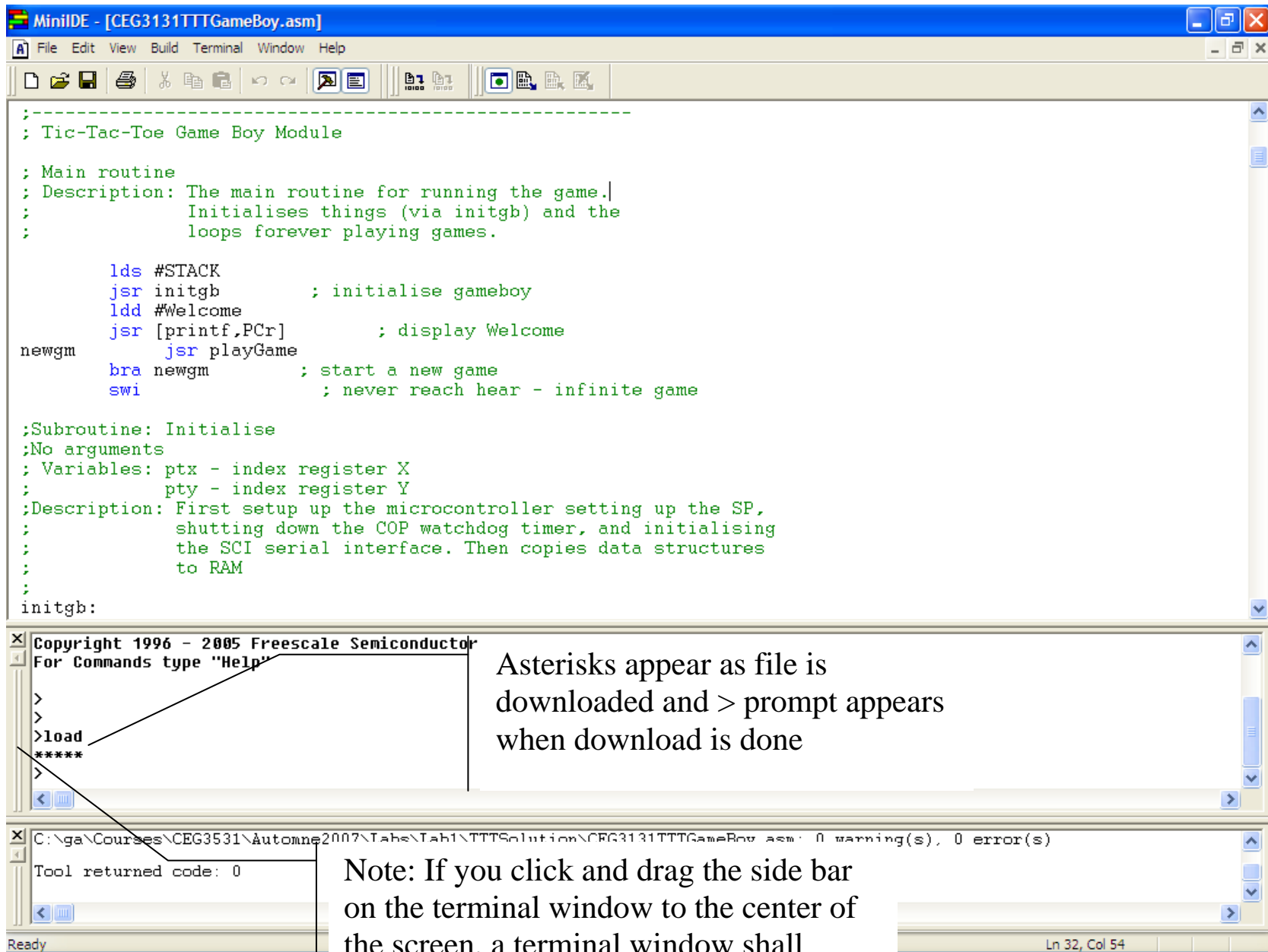
4. Click open to download file.

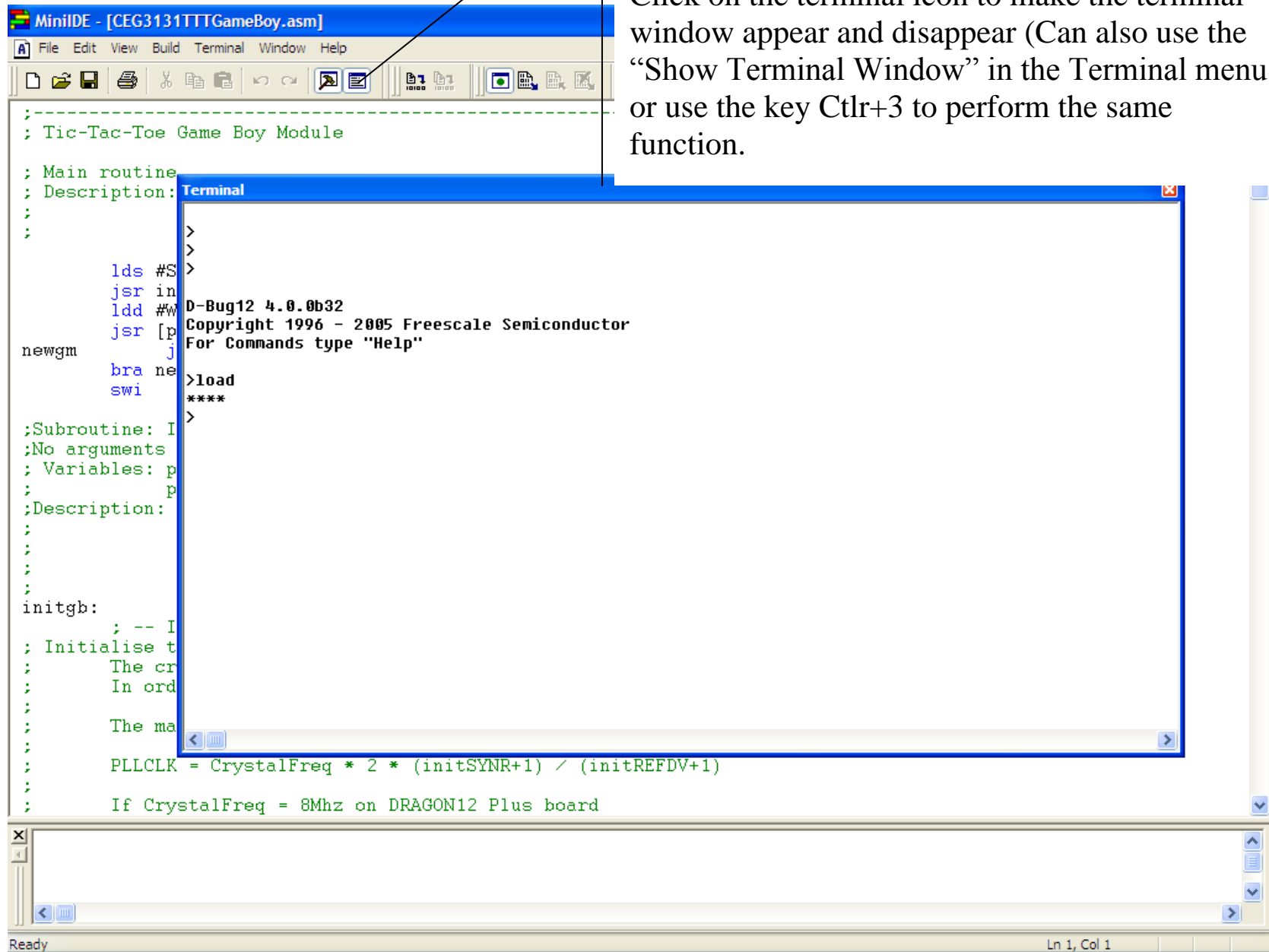
1. Type in the D-Bug12 "load" Command

The screenshot shows the MiniIDE interface with the following components:

- Assembly Code Editor:** Displays assembly code for a Tic-Tac-Toe Game Boy Module. The code includes comments and instructions like `lds #STACK`, `jsr initgb`, `ldd #Welcome`, `jsr [printf,PCr]`, `jsr playGame`, `bra newgm`, `swi`, `;Subroutine: Initialise`, `;No arguments`, `; Variables: ptx - index register X`, `;pty - index register Y`, `;Description: First setup up the mi`, `;shutting down the COP`, `;the SCI serial interf`, `to RAM`, and `initgb:`.
- Terminal:** Shows the command `>load` entered. Below it, a status message reads: `C:\ga\Courses\CEG3531\Automne2007\Labs\Lab1\TTTSolution\CEG3131TTTGameBoy.asm: 0 warning(s), 0 error(s)` and `Tool returned code: 0`.
- Download S-Record Dialog:** A file selection window titled "Download S-Record" is open. The "Look in:" field shows "TTTSolution". The file list includes `.CEG3131TTTGameBoy.s19.swp`, `.CEG3131TTTGameBoy.s29.swp`, `CEG3131TTTGameBoy.asm`, `CEG3131TTTGameBoy.lst`, `CEG3131TTTGameBoy.s19`, `CEG3131TTTGameBoy.s29`, `Make_s2_TTTGameBoy.bat`, `Reg9s12.inc`, `SRecCvt.exe`, and `TTTGameBoyFlowCharts.vsd`. The "File name:" field contains `Lab3Prog2.s19` and the "Files of type:" field is set to `All Files (*.*)`. The "Open" button is highlighted.

Ln 32, Col 54





Another placement of the terminal window. Drag the terminal window to the right edge of the IDE.

In the source window open the .LST file. This allows you to debug the code.

Set the PC to the starting address of the program

By default, D-Bug12 uses hardware breakpoints required to debug code in EEPROM, only 2 allowed.

When working in RAM, can turn off hardware breakpoints and thus use up to have up to 10 breakpoints

Trace (t) first instruction
Following in the LST file

Set a breakpoint (br) to stop
after call to subroutine

Use g to start execution until
breakpoint is met

The screenshot shows the MinIDE IDE interface. The main window displays assembly code with addresses and hex values on the left, and a list of instructions on the right. A terminal window is docked on the right side. The debugger console shows the current state of the program, including registers, memory, and breakpoints.

Assembly code (left):

```
0400 CF 2000
0403 16 0413
0406 CC 0681
0409 15 FB EA7B
040D 16 0453
0410 20 FB
0412 3F
```

Instructions (right):

```
ldc #STACK
jsr initgb ; ini
ldd #welcome
jsr [printf,PCr]
jsr playGame
bra newgm ; star
swi ; ne
```

Debugger console (right):

```
D-Bug12 4.0.0b32
Copyright 1996 - 2005 Freescale Semiconductor
For Commands type "Help"

>pc 0400

PP PC SP X Y D = A:B CCR
38 0400 3C00 0000 0000 00:00
xx:0400 CF2000 LDS #$2000

>t

PP PC SP X Y D = A:B CCR = SXHI NZUC
38 0403 2000 0000 0000 00:00 1001 0000
xx:0403 160413 JSR $0413

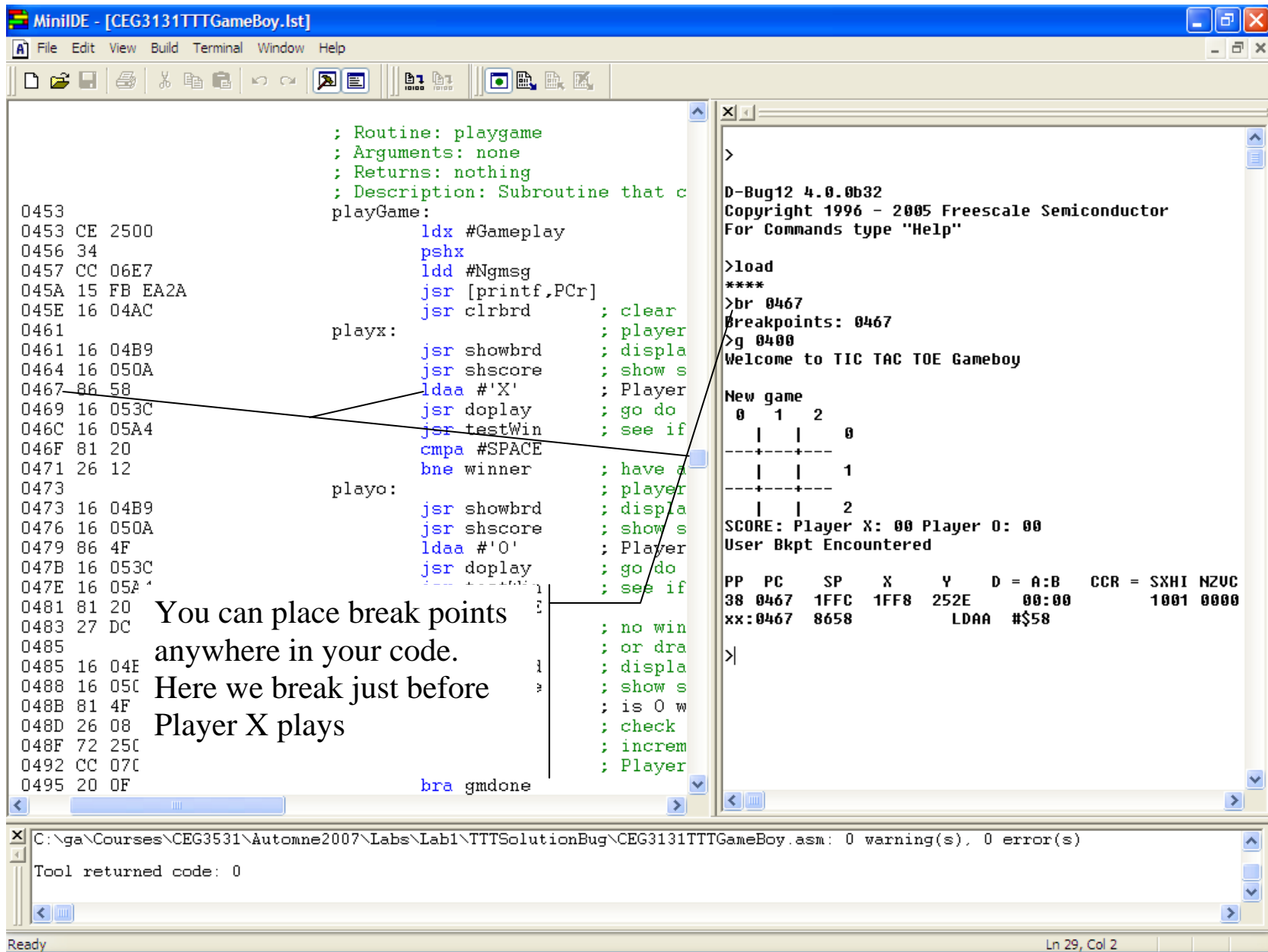
>br 0406
Breakpoints: 0406
>g
User Bkpt Encountered

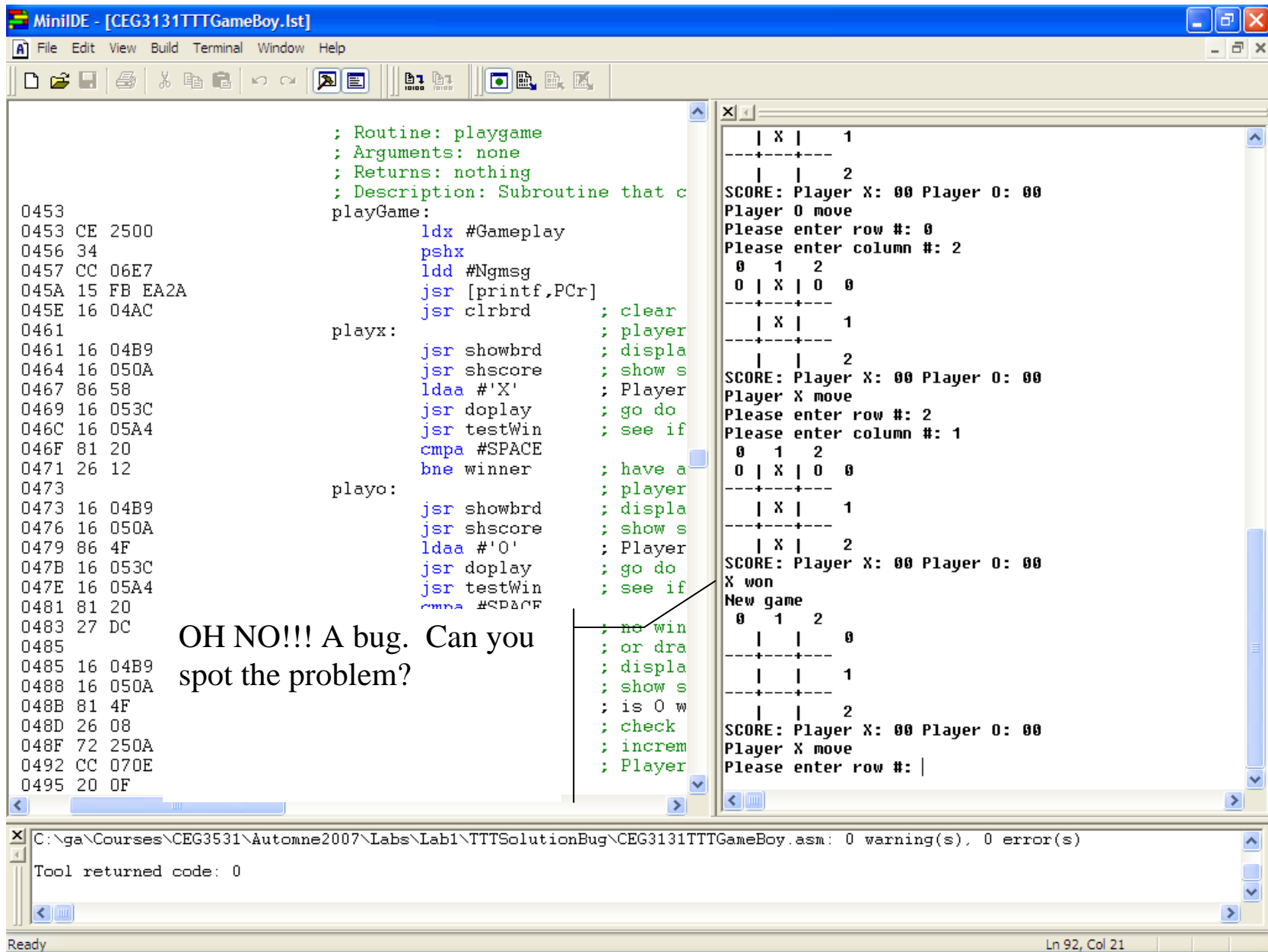
PP PC SP X Y D = A:
38 0406 2000 0677 2592 FF:
xx:0406 CC0681 LDD #$06

>br 0400
Breakpoints: 0406 0400
>br 0410
Breakpoints: 0406 0400
Breakpoint Table Full
>usehbr off
Using Software Breakpoints
>br 0410
Breakpoints: 0410
>
```

Terminal window (bottom right):

```
Ready Ln 106, Col 2
```





This subroutine contains a bug

```
; Subroutine - shscore
; Arguments: none
; Returns: nothing
; Global Variables: Xscr - X score number
;                   Xscr_a - X score ASCII
;                   Oscr - O score number
;                   Oscr_a - O score ASCII
; Description: Outputs current score, but first
; translates the hexadecimal value to a 2 byte
; ASCII value. Value is passed in Acc D to scr2asc
; for translation. Register X contains address
; where converted bytes are to be stored.
; After scores are converted, score string is displayed.
```

```
shscore      pshd
             pshx    ; preserve registers
             ldab Xscr
             clra
             ldx #Xscr_a
             bsr scr2asc
             ldab Oscr
             clra
             ldx #Oscr_a
             bsr scr2asc
             ldd #Score
             jsr [printf,PCr] ; display current Score
             pulx    ; restore registers
             puld
             rts

; Subroutine; scr2asc (num,addr)
; Parameters: num - in accumulator D (need in D for divide)
;             addr - in Y register
; Local variables: rem - in accumulator D
;                 qu - in X register
; Converts number to ascii decimal equivalent
```

```
scr2asc
             pshx
             pshd    ; preserve
             ldx #10
             idiv    ; remainder in D, i.e. B
             addb #ASCCONVNUM ; converts digit to ASCII
             stab 1,y ; save first digit
             tfr x,d ; move quotient back to d
             addb #ASCCONVNUM ; convert digit to ASCII
             stab 0,y ; save second digit
             puld    ; restore
             pulx
             rts
```

Basic Programming

Questions:

1. What is the meaning of the sign bit = 1 when unsigned binary coded numbers are added?
2. What is the meaning of the carry bit = 1 when two unsigned binary coded numbers are subtracted? When two's complement binary coded numbers are subtracted?
3. What addressing mode is best to use when you want to access several sequential elements in a data array – immediate, direct, indexed?
4. Pointer addressing with auto-increment and auto-decrement is referred to as what type of addressing for the MC68HC12?

Exercise 1

Give the values of the starting address, offset (indicate the size of the offset) and calculate the effective address of each of the following examples. Illustrate on the programmer's model the effect of the instructions to the CPU registers and memory.

- a. X = \$3000 A=\$E5
LDY A,X EA =
- b. Y = \$6F40
STD 4,-Y EA =
- c. SP = \$0BF3
ADDA 3,SP EA =

Exercise 2:

An array of bytes contains a set of non-zero unsigned values. The last value in the array contains the value 0 to indicate the end of the array. Write a piece of code that contains a loop to read the contents of each byte into accumulator A. The array starts at address ARRAY. Write a first version that uses indexed addressing with accumulator B as the offset. Write a second version that auto-increments the index register. (Use the programming model to follow the steps used in running the program for this first version and if time permits the second version).

Exercise 3

Consider two three byte numbers stored at addresses NUM1 and NUM2 as defined by the following assembler pseudo-operations:

NUM1	EQU	\$0850
NUM2	EQU	\$0853
DIFF	EQU	\$0856

It is possible to load into the accumulators the bytes (or store into memory) using the labels as follows:

LDAA	NUM1	; Loads most significant byte into accumulator A
STAB	DIFF+2	; Store the contents of accumulator B into the least ; Significant byte of the SUM number

The assembler will translate the labels to the appropriate addresses as shown below:

LDAA	\$0850	; NUM1
STAB	\$0858	; DIFF+2

The objective of this exercise is to write assembler source code to compute the difference between the two three-byte numbers (NUM1-NUM2) and store the result at address DIFF.

You will need the following 68HC12 instructions:

LDAA: Load accumulator A

SUBA: Subtract memory from A: $A \leftarrow A - (M)$

SBCA: Subtract memory and carry from A: $A \leftarrow A - (M) - C$

STAA: Store accumulator A

Use the programming model to follow the steps used in running the program.

