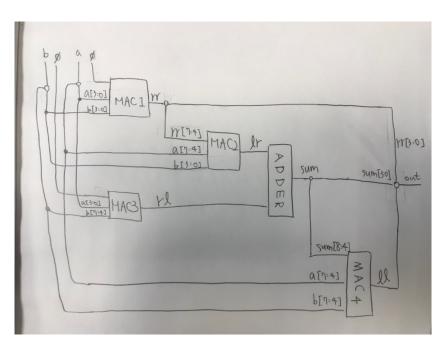
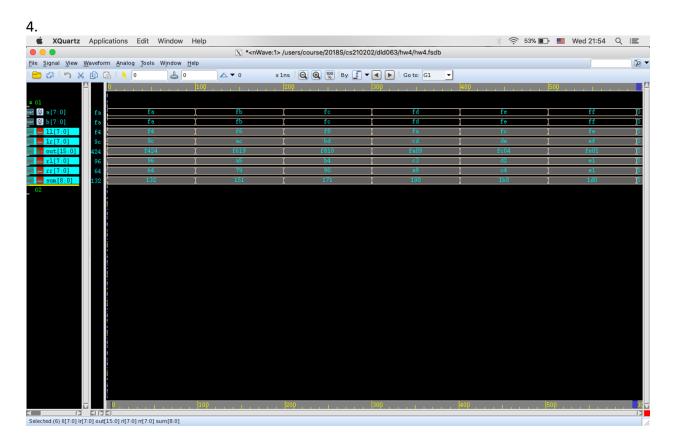
hw04_report_106062202

1. I verified the design by writing a simple test bench:

2. 16 bits. Yes, an adder will help. I designed an adder which adds two 8-bit number. I used one adder to minimize the number of MACs used.







The six test patterns I tested are numbers starting from 250 * 250 to 255 * 255. Inputs are 'a' and 'b'. 'll', 'lr', 'rl', and 'rr' are the partial products. The wire 'sum' holds an intermediate sum of 'rl' and 'rl'. The output is stored in 'out'.

5.

```
`timescale 1ns / 100ps
      2 module test;
   4 parameter pattern = (1 << 16); // 2^16
5 reg [7:0] a, b;
6 integer i, error;</pre>
                   wire [15:0] out;
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                   multiplier multi(.out(out), .a(a), .b(b));
                     integer golden, count;
                    initial begin
   $fsdbDumpfile("hw4.fsdb");
   $fsdbDumpvars;
                     end
                      initial begin
                                    a = 0;
b = 0;
                                    error = 0;
                                  count = 0;
count = 0;
for (i = 0; i < pattern; i = i + 1) begin
    a = i[15:8];
    b = i[7:0];
    golden = a * b;</pre>
                                                                                                                                                                                                                                                                                                                                                                                                                                                                   3,1-4
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                                                          d(x_b) = a_0 + b_0 + b
                                                           if (golden != out) begin
                                                                              count = count + 1;
                                                                                $display("Mismatched!");
                                                           end
                                    end
                                   if (count == 0) $display("\n<<< PERFECT!! >>>\n");
else $display("%d ERRORS\n", count);
                                    #100 $finish;
                     end
                endmodule
```