**IO Map**

Project: None

Device: -

|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
| **Pin** | **Signal** | **Mode** | **Type** | **Speed** | **Default** | **Pull** | **Alternate** |
| PA0 | MEAS12V | Analog | Push pull | Low | Low | Floating | AF 0 - SYS |
| PA1 | SPI\_CS\_PS2\_N | Output | Push pull | Medium | High | Floating | AF 0 - SYS |
| PA2 | USART2\_TX | Alternate | Push pull | Medium | Low | Pull down | AF 7 - USART1-3 |
| PA6 | SERVO1 | Alternate | Push pull | Low | Low | Floating | AF 2 - TIM3-5 |
| PA7 | SERVO2 | Alternate | Push pull | Low | Low | Floating | AF 2 - TIM3-5 |
| PA9 | AUXLINK\_TX | Alternate | Push pull | Medium | High | Floating | AF 7 - USART1-3 |
| PA10 | AUXLINK\_RX | Alternate | Push pull | Medium | High | Pull up | AF 7 - USART1-3 |
| PA11 | USB\_DM | Alternate | Push pull | High | High | Floating | AF 10 - OTG USB |
| PA12 | USB\_DP | Alternate | Push pull | High | High | Floating | AF 10 - OTG USB |
| PB0 | SERVO3 | Alternate | Push pull | Low | Low | Floating | AF 2 - TIM3-5 |
| PB3 | SPI1\_CLK | Alternate | Push pull | High | High | Pull up | AF 5 - SPI1-2 |
| PB4 | SPI1\_MISO | Alternate | Push pull | High | High | Pull up | AF 5 - SPI1-2 |
| PB5 | SPI1\_MOSI | Alternate | Push pull | High | High | Pull up | AF 5 - SPI1-2 |
| PB6 | I2C\_SCL | Alternate | Open drain | High | High | Pull up | AF 4 - I2C1-3 |
| PB7 | I2C\_SDA | Alternate | Push pull | High | High | Pull up | AF 4 - I2C1-3 |
| PB8 | PULSE\_4 | Alternate | Push pull | Low | Low | Floating | AF 3 - TIM8-11 |
| PB9 | PULSE\_5 | Alternate | Push pull | Low | Low | Floating | AF 3 - TIM8-11 |
| PB10 | OUT\_3 | Output | Push pull | Low | Low | Floating | AF 0 - SYS |
| PB11 | OUT\_4 | Output | Push pull | Low | Low | Floating | AF 0 - SYS |
| PB15 | USBRENUM | Output | Open drain | Low | High | Floating | AF 0 - SYS |
| PC0 | OUT\_1 | Output | Push pull | Low | Low | Floating | AF 0 - SYS |
| PC1 | OUT\_2 | Output | Push pull | Low | Low | Floating | AF 0 - SYS |
| PC2 | DEBUG\_LED | Output | Push pull | Low | Low | Floating | AF 0 - SYS |
| PC3 | RUN\_LED | Output | Push pull | Low | Low | Floating | AF 0 - SYS |
| PC4 | ERR\_LED | Output | Push pull | Low | High | Floating | AF 0 - SYS |
| PC6 | PULSE\_2 | Alternate | Push pull | Low | Low | Floating | AF 3 - TIM8-11 |
| PC8 | INPUT\_0 | Input | Push pull | Low | High | Pull down | AF 0 - SYS |
| PC9 | INPUT\_1 | Input | Push pull | Low | High | Pull down | AF 0 - SYS |
| PC10 | INPUT\_2 | Input | Push pull | Low | High | Pull down | AF 0 - SYS |
| PC11 | INPUT\_3 | Input | Push pull | Low | High | Pull down | AF 0 - SYS |
| PC12 | INPUT\_4 | Input | Push pull | Low | High | Pull down | AF 0 - SYS |
| PC13 | INPUT\_5 | Input | Push pull | Low | High | Pull down | AF 0 - SYS |
| PC14 | INPUT\_6 | Input | Push pull | Low | High | Pull down | AF 0 - SYS |
| PC15 | INPUT\_7 | Input | Push pull | Low | High | Pull down | AF 0 - SYS |
| PD0 | INPUT\_8 | Input | Push pull | Low | High | Pull down | AF 0 - SYS |
| PD1 | INPUT\_9 | Input | Push pull | Low | High | Pull down | AF 0 - SYS |
| PD2 | INPUT\_10 | Input | Push pull | Low | High | Pull down | AF 0 - SYS |
| PD3 | INPUT\_11 | Input | Push pull | Low | High | Pull down | AF 0 - SYS |
| PD4 | INPUT\_12 | Input | Push pull | Low | High | Pull down | AF 0 - SYS |
| PD5 | INPUT\_13 | Input | Push pull | Low | High | Pull down | AF 0 - SYS |
| PD6 | INPUT\_14 | Input | Push pull | Low | High | Pull down | AF 0 - SYS |
| PD7 | INPUT\_15 | Input | Push pull | Low | High | Pull down | AF 0 - SYS |
| PD8 | USART\_TX | Alternate | Push pull | High | High | Pull up | AF 7 - USART1-3 |
| PD9 | USART\_RX | Alternate | Push pull | Low | High | Pull up | AF 7 - USART1-3 |
| PD12 | PULSE\_0 | Alternate | Push pull | Low | Low | Floating | AF 2 - TIM3-5 |
| PE5 | PULSE\_3 | Alternate | Push pull | High | Low | Floating | AF 3 - TIM8-11 |
| PE8 | DIR\_0 | Output | Push pull | Low | Low | Floating | AF 0 - SYS |
| PE9 | DIR\_1 | Output | Push pull | Low | Low | Floating | AF 0 - SYS |
| PE10 | DIR\_2 | Output | Push pull | Low | Low | Floating | AF 0 - SYS |
| PE11 | DIR\_3 | Output | Push pull | Low | Low | Floating | AF 0 - SYS |
| PE12 | DIR\_4 | Output | Push pull | Low | Low | Floating | AF 0 - SYS |
| PE13 | DIR\_5 | Output | Push pull | Low | Low | Floating | AF 0 - SYS |
| PE14 | ENABLE\_N | Output | Push pull | Low | Low | Floating | AF 0 - SYS |
| PG0 | ENA\_0 | Output | Push pull | Low | Low | Floating | AF 0 - SYS |
| PG1 | ENA\_1 | Output | Push pull | Low | Low | Floating | AF 0 - SYS |
| PG2 | ENA\_2 | Output | Push pull | Low | Low | Floating | AF 0 - SYS |
| PG3 | ENA\_3 | Output | Push pull | Low | Low | Floating | AF 0 - SYS |
| PG4 | ENA\_4 | Output | Push pull | Low | Low | Floating | AF 0 - SYS |
| PG5 | ENA\_5 | Output | Push pull | Low | Low | Floating | AF 0 - SYS |
| PG6 | SPARE\_1 | Output | Push pull | Low | Low | Floating | AF 0 - SYS |
| PG7 | SPARE\_2 | Output | Push pull | Low | Low | Floating | AF 0 - SYS |
| PG9 | ENABLE\_PWM\_N | Output | Push pull | Low | High | Floating | AF 0 - SYS |