

UNIT - 1

- OS is an interface b/w hardware and user
- It is a system program.
- Manages system resources
- Provides platform for other applications
- Core part of OS is Kernel

Primary Goal : To make ~~user~~ sys. friendly

Secondary Goal : Make sys. efficient

Functions :

- Process Management
  - Scheduling Processes & threads on CPU
  - Creating & deleting both user & sys. process
  - Process Sync.
  - Process Communication
- Memory Management
  - Keeping track of memory usage
  - Deciding which process & data move in/out of memory
  - Allocating / De-allocating memory space
- Disk Management
  - Free space mgmt. in disk
  - Disk scheduling
  - Storage alloc.

→ File Mgmt.

- Create/Delete files
- Backing up files on stable storage media
- Create/Delete Directories

→ I/O Device Mgmt.

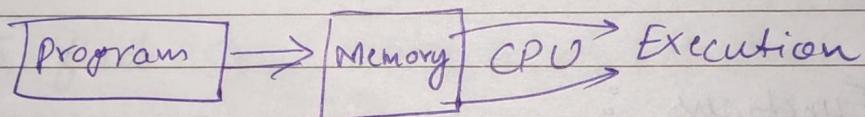
→ Network Mgmt.

→ Security and mgmt.

Services :

→ User Interface

→ Program Execution



→ Access I/O Devices

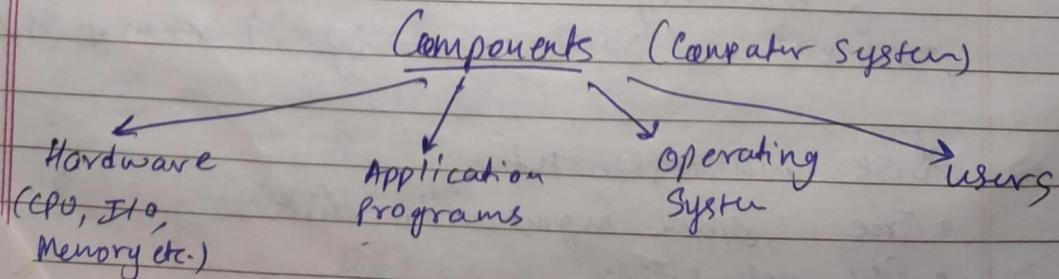
→ File System access

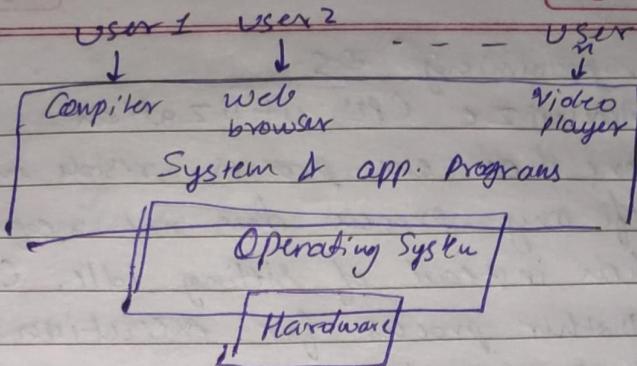
- Read/Write files & directories
- Search for a file in II
- Access and deny to files

→ Error Detection & Response

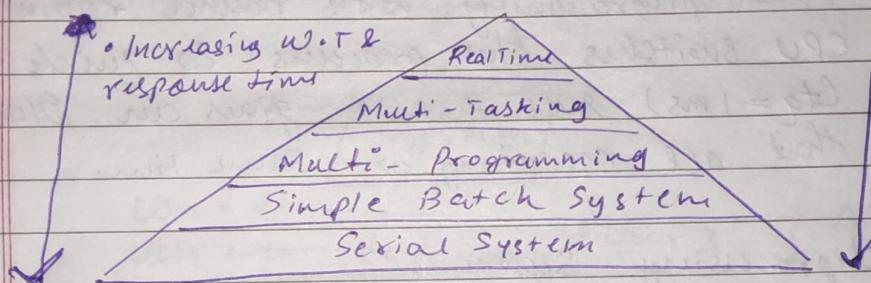
- Troubleshooting
- Error codes

→ Communication





## Classification of OS :



### ① Serial System

- NO O.S needed

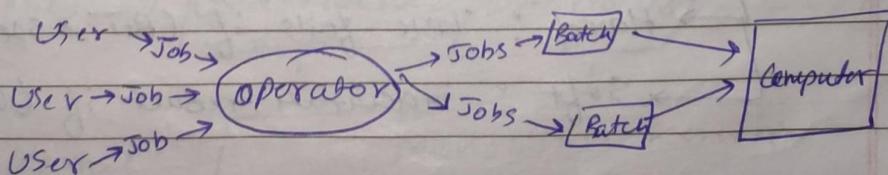
- Direct interaction of programs with hardware

### ② Batch System

- Set of JOBS with similar need.

- Program
- Input Data
- Central Instructions

- Similar types of jobs batch together



### ③ Multiprogramming OS

- Maximize CPU utilization
- More than one process reside in MIM
- If any process does not require CPU
  - { then instead of sitting idle, CPU picks another process for execution.
- Context switching

### ④ Time sharing / Multi - Tasking

- Multi-programming with round robin system
- CPU switches b/w processes so quickly ( $T_q = 1 \text{ ms}$ ) such that it gives an illusion that all executing at same time.

### ⑤ Multiprocessing System

- Two or more CPU within a single comp. share system bus and I/O devices.
- True parallel execution of processes.
- Two types
  - Asymmetric : Master - Slave
  - Symmetric : No master - slave

### ⑥ Real Time System

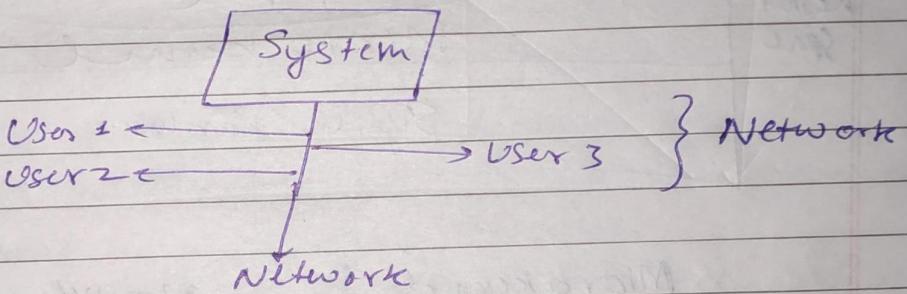
- Well-defined fixed time constraints.
- Used when we require quick response against input.
- Two Types
  - Hard : Task fails if  $(R.T > \text{specified time})$
  - Soft : inaccurate result if  $(R.T > \text{specified time})$

## (7) Interactive System

- Direct comm. b/w user & system.
- user gives ins. to OS or program (Input)
- Immediate results (output)

## (8) Multi-user OS

- Multiple users on different computers to access a single system with one OS on it.
- Access through a network

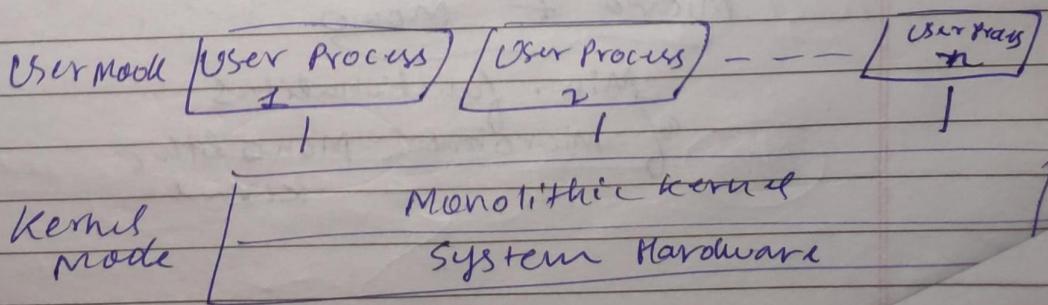


Kernels : Set of necessary software / programs modules which drives the hardware.

Types →

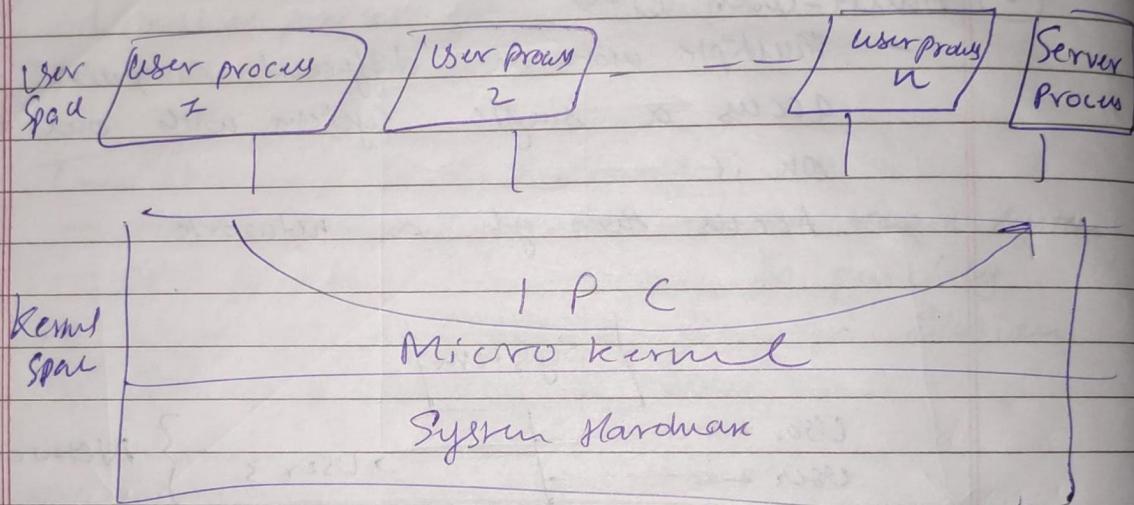
### (i) Monolithic

- Literally means all in one place.
- User and kernel services under same address space.



### ② Micro kernel

- Different address space for user and kernel services implementation
- only most imp. services present in kernel



### Microkernel

Size	Small
Speed	Faster
Extensibility	Easy
Error prone	Less

### Monolithic kernel

Large
Slower
Hard
More

### ③ Hybrid kernel

#### Micro + Mono

of Min. functionalities  
microkernel      Mono-lithic  
kernel

#### ④ Reentrant kernel

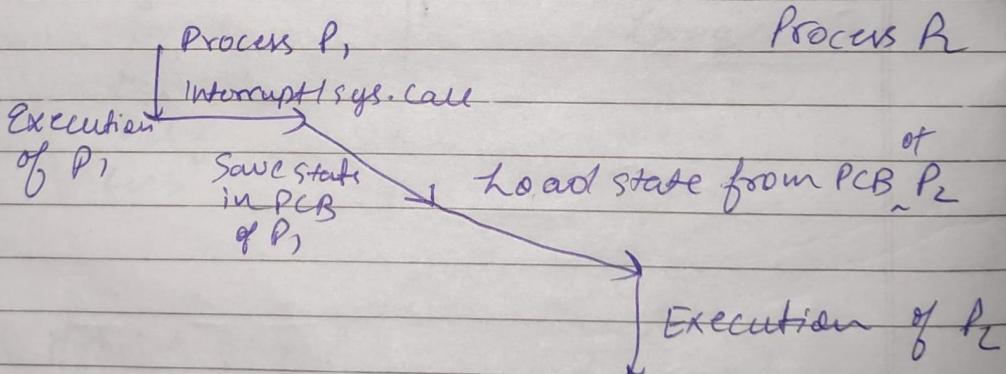
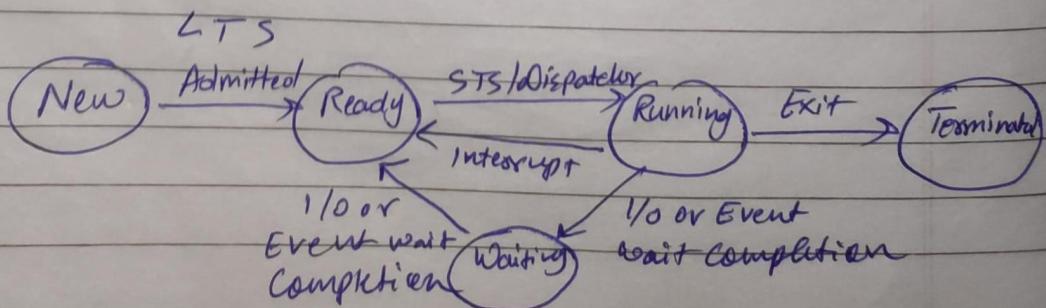
- Executable code stored in memory such that several processes can use this code at same time ~~be~~ without hindering the working of other processes.
- Multiple processes executed in kernel mode at same time.
- Example : UNIX.

<u>Program</u>	<u>Process</u>
- Set of instructions to do specific task	- Instance of an executing program
- Passive entity	- Active entity
- Resides in secondary memory	- Resides in primary <del>memory</del>
- Continues to exist until deleted	- Limited span of time until completion of time.

UNIT - 3Process Control Block :

- Holds all info necessary to keep track of process
- OS creates PCB for every process

Pointer	→ Address of parent process
Process ID	→ Unique ID of process
Program Counter	→ Add. of next ins. to be executed
CPU	→ High priority of process
Scheduling	
Accounting info	→ Amnt. of CPU used.
I/O Status info	→ Info. of I/O devices alloc. to processes

Process State Switching :Life Cycle of Process :

- New state : Process is created (Job queue)
- Ready state : Resides in Ready queue (Primary memory)
- Running state : Assigned CPU is assigned to a process
- Terminated state : Process has completed its execution successfully
- Waiting state : Resides in Waiting queue (Primary memory) → I/O burst.

### Schedulers :

- LTS
  - Selects process from Job Queue → Ready Queue
  - New state → Ready State
- STS
  - Selects process Ready Queue → CPU
  - Ready State → Running State
  - Dispatcher saves content of PCB and loads content of another process. (Content Switching)
- MTS
  - Removes processes from MM
  - ★ Reduces degree of multiprogramming

### Multilevel Queue Scheduling :

- Partitions ready queue into separate queues
- Each queue has its own scheduling algorithm.

## \* Aging :

Gradually increasing the priority of the process that wait in the system for long time

## Multilevel Feedback Queue Scheduling :

- Allows a process to move between queues.
- If process uses too much CPU time it will be moved towards lower priority queue
- If it stayed too long in lower priority queue, it may be moved to higher priority queue.

## Fork() System Call :

- used to create new process (child process)
- If we call fork() n-times,  $2^{(n-1)}$  child process will be generated.

## Deadlock :

Def → Dead Situation where two or more than 2 processes are waiting for each other to complete the execution and release the resource so that requesting processes will get resources for successful execution.

But, none of processes got executed.

- Mutual Exclusion
- Hold & Wait
- No Preemption
- Circular Wait

### Deadlock Recovery :

- Abort all processes involved in deadlock
- Abort processes one by one to make system deadlock free.

Q.

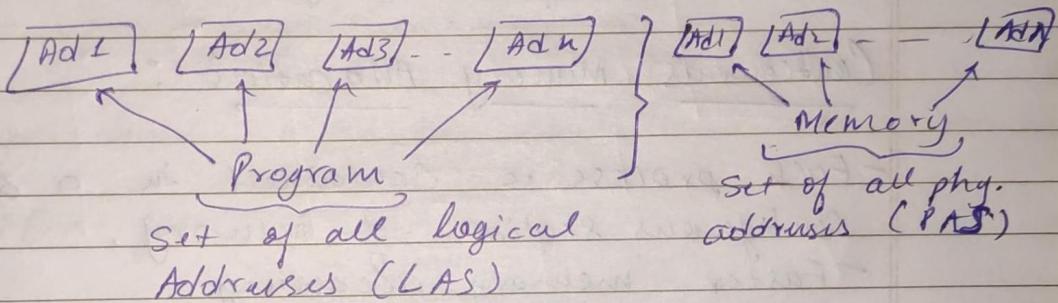
Process	Allocated			MAX	AVAILABLE			NEED		
	R <sub>1</sub>	R <sub>2</sub>	R <sub>3</sub>		R <sub>1</sub>	R <sub>2</sub>	R <sub>3</sub>	R <sub>1</sub>	R <sub>2</sub>	R <sub>3</sub>
P <sub>1</sub>	2	2	3	3 6 8	7	7	10	1	4	5
P <sub>2</sub>	2	0	3	4 3 3	9	9	13	2	3	0
P <sub>3</sub>	1	2	4	3 4 4	11	9	16	2	2	0
					12	11	20			

$[P_1 \rightarrow P_2 \rightarrow P_3]$

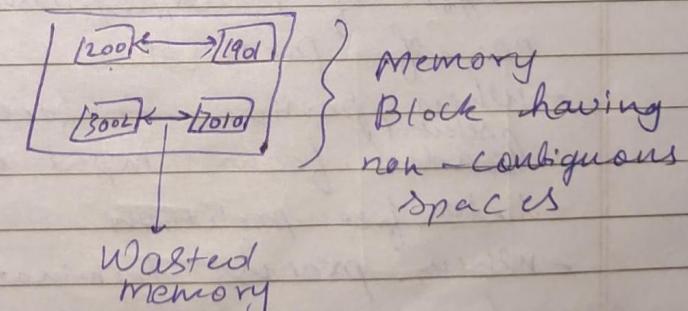
SAFE SEQUENCE  $\checkmark$  Safe State

UNIT - 4LAS Vs PAS :

- Logical Address is generated by CPU
- Physical Address is generated by Memory Unit.

Fragmentation :(1) External

- Enough total memory to satisfy a req. but available spaces are not contiguous.

(2) Internal

- Memory allocated to a process may be larger than req. memory.
- Difference b/w these 2 nos. is Internal frag.

# Compaction →

Shuffle all memory contents to place all free memory together in one large block.

→ To solve external fragmentation  
 ↳ we can move all holes to one side of memory.

Contiguous Memory Allocation :

- Each process is contained in a single contiguous section of memory.
- Faster memory accessing
- Two Categories :
  - ① Multiprogramming with fixed no. of tasks
    - Fixed sized partitions
    - Each partition may contain one process
    - Degree of multiprogramming is bound by no. of partitions
    - When partition is free, a process is <sup>selected</sup> loaded from input queue and loaded into free partition.
    - When process terminates, partition becomes free.
    - prone to Internal, External fragmentation.

## (2) Multiprogramming with variable no. of tasks

- OS keeps a table indicating which parts of memory are available / occupied.
- Initially all memory is one large hole
- As process enter, put into input queue and OS checks memory requirements of each process and allocates memory.
- Ext. fragmentation occur.

# Algorithms used in contiguous memory --

### (1) First fit

Allocate the first hole that is big enough

### (2) Best fit

Allocate the ~~second~~ smallest hole big enough

### (3) Worst fit

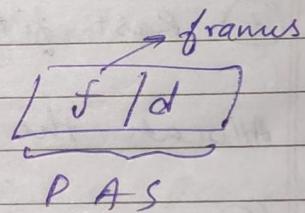
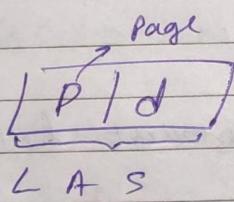
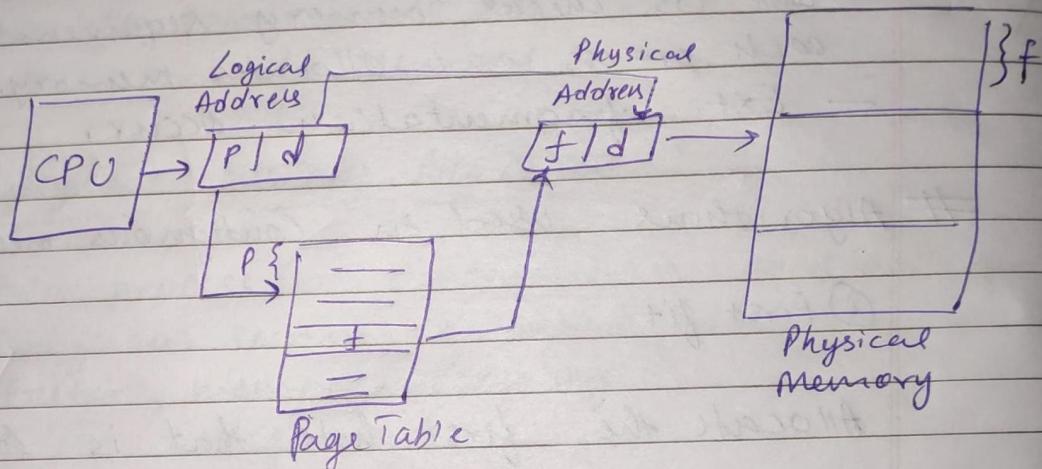
Allocate the largest hole.

### Memory Protection:

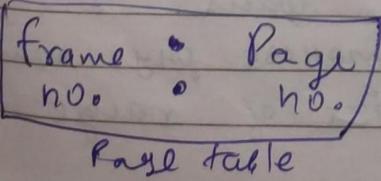
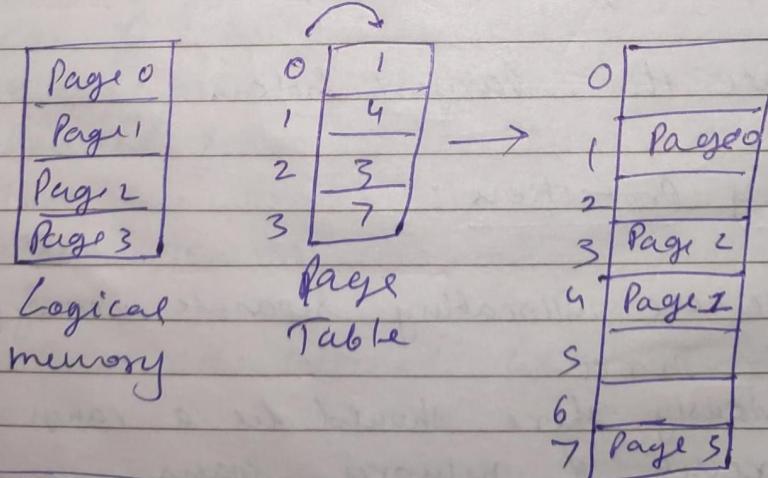
- We are allocating separate memory spaces to each process
- Obviously there should be a range for these addresses of memory spaces
- Base register  $\rightarrow$  smallest phy. memory address  
Limit register  $\rightarrow$  size of range

## Paging :

- a scheme that permits phy. address space a process to be non-contiguous.
- prone for internal fragmentation.



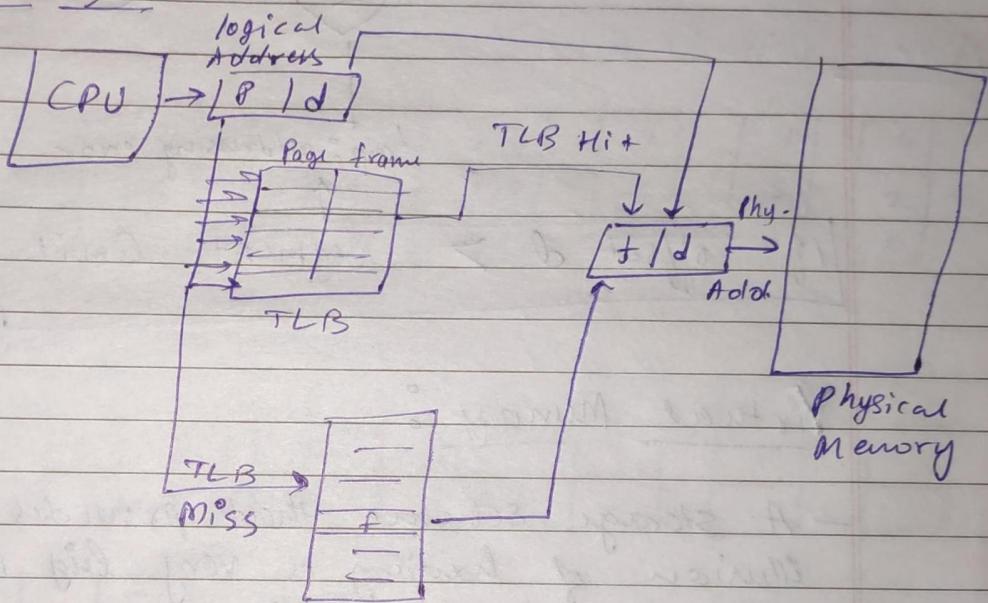
Example:



Physical  
memory

## Paging with TLB :

- To increase speed of memory access, TLB is used with page table.
- Small, fast
- Fixed sized blocks

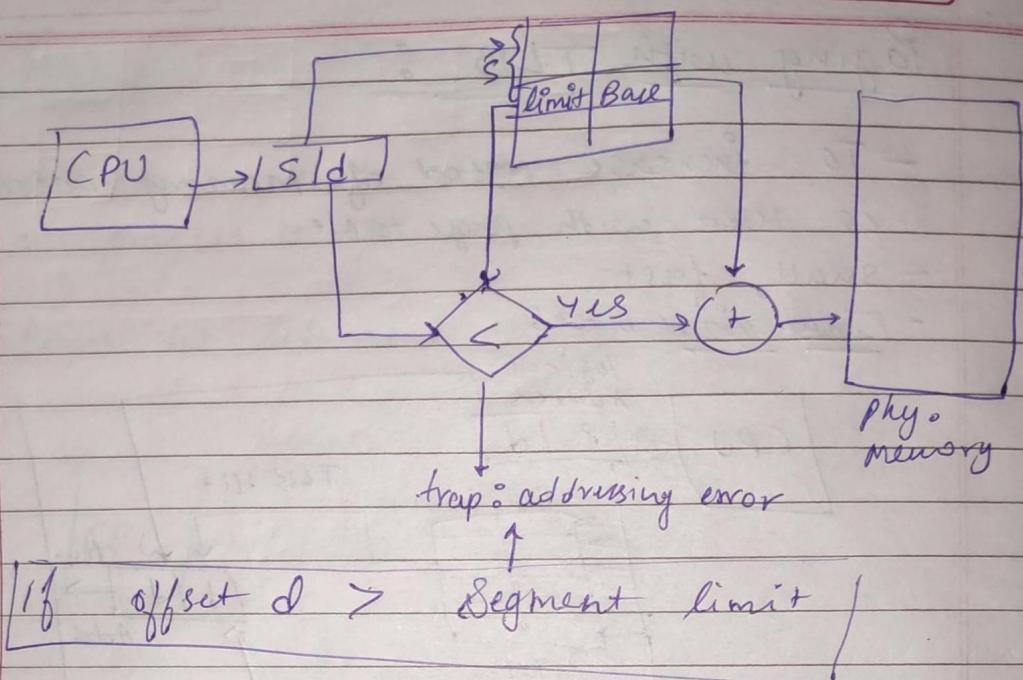


$$\text{Hit Ratio} = \frac{N_{\text{hit}}}{(N_{\text{hit}} + N_{\text{miss}})}$$

- Valid bit  $\rightarrow$  1  $\rightarrow$  Page is in LAS  
0  $\rightarrow$  Page is not in LAS

## Segmentation :

- LAS has variable sized segments. Each segment has a name and a length
- Avoids both ext. and int. frag.
- Takes lesser space in phy. memory.



### Virtual Memory :

- A storage scheme that provides user an illusion of having a very big main memory.
- This is done by treating a part of Secondary memory as main memory.

### Demand Paging :

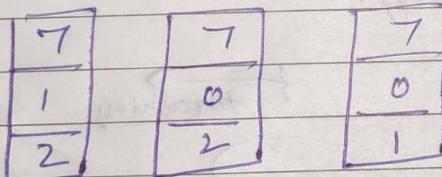
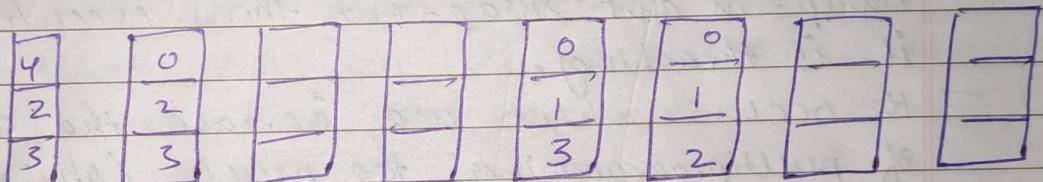
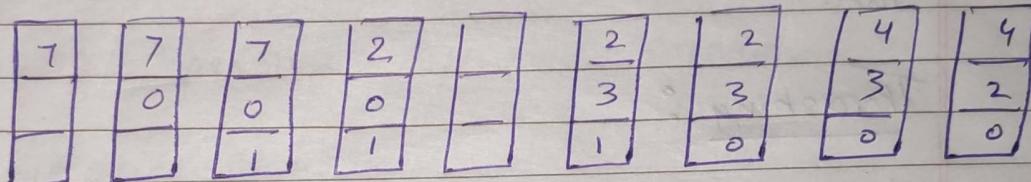
- only those pages are loaded when they are demanded.
- Lazy swapper is used, it never swaps a page into memory unless that page will be needed.

$$\text{Effective Access Time} = (1-p) * \text{main memory access time} + p * \text{page fault time}$$

Q. 7, 0, 1, 2, 0, 3, 0, 4, 2, 3, 0,  
 3, 2, 1, 2, 0, 1, 7, 0, 1

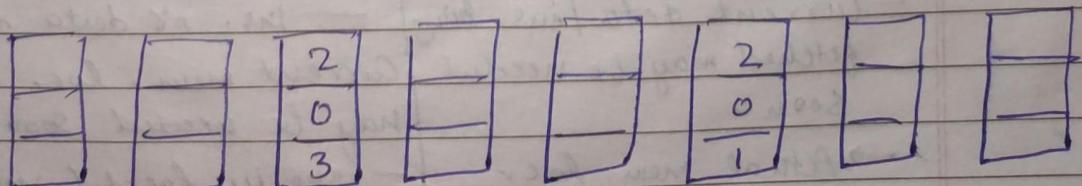
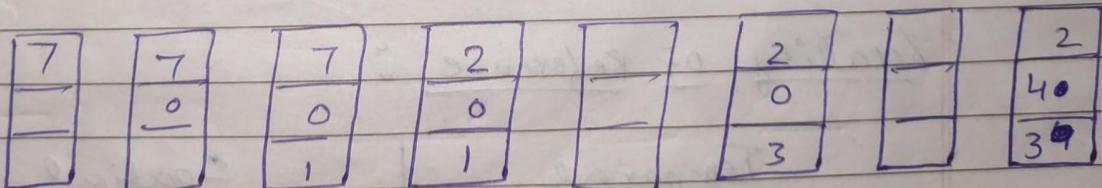
Apply : (3 frames)

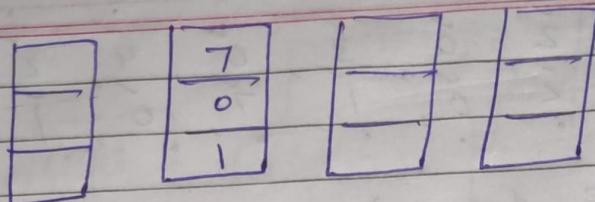
① FIFO



Page Faults = 15

② Optimal Page Replacement





$$\text{Page Faults} = 9$$

Note :- Optimal Page Replacement has lowest page-fault rate.

Thrashing :

- If a process is spending more time in swap-in and swap-out than executing then it is thrashing.
- It occurs when we increase the degree of multiprogramming too much (obviously, more context switching).



Locality of Reference :

Temporal

- Current data/ins. being fetched may be needed soon.

→ Actual mem. loc.

Spatial

- Ins. or data near to current mem. loc. being fetched may be needed soon in future.

- Nearly located mem. loc.

## Other Useless Machine :

### ① Bare Machine

- Comp. with no OS
- Ins. directly on logical HW.
- uses machine language.
- Fast code
- Expensive software creation

### ② Resident Monitor

- Code that runs on Bare Machine
- Works like an OS that controls instructions
- Works like job sequencer

## UNIT - 5

### Disk Scheduling Algorithms :

#### ① FCFS

- Scheduled in the order they arrive in disk queue

#### ② Shortest Seek Time First (SSTF)

- Request is calculated in advance in the queue
- Request near disk arm gets executed first.

#### ③ SCAN

- ELEVATOR Algorithm
- Disk moves into a particular direction
- Then reverses its direction and again services the request arriving in its path.
- Touches the first limit of disk (upper or lower)

#### ④ LOOK

- Improved version of SCAN Algorithm.
- Head does not touch limit, just goes to last request and reverses its direction

#### ⑤ CSCAN

- Direction is preserved
- Both limits are touched.

(6)

CLOCK

— Same as' CSCAN

— No limits are touched

— Only first and last requests are touched  
to preserve direction

Q.

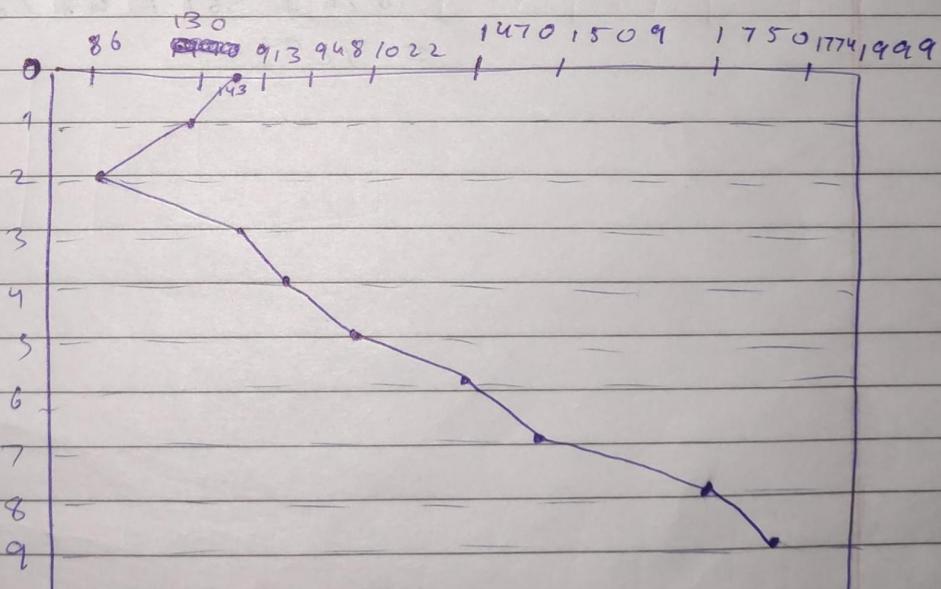
86, 1470, 913, 1774, 948, 1509, 1022,  
1750, 130

Initial Head = 143

Range = 0 to 1999

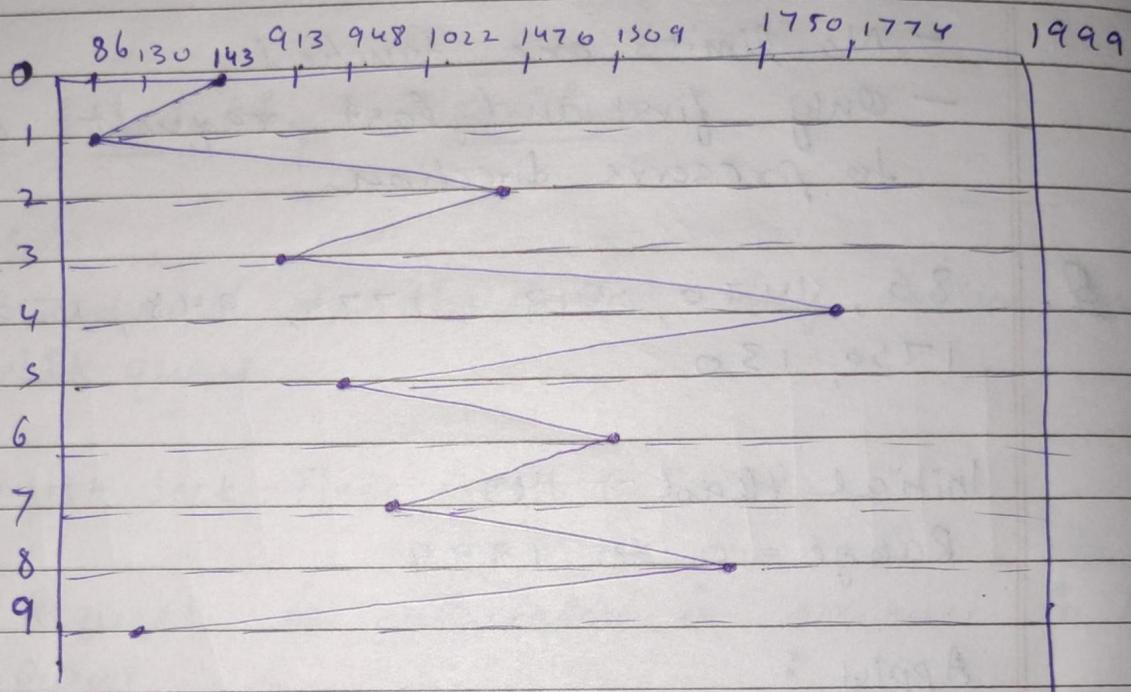
Apply :

(1)

FIFO SSTF

$$\begin{aligned}
 \text{Total Seek Time} &= 13 + 44 + 82 + 35 + 74 + 468 \\
 &\quad + 59 + 241 + 24 \\
 &= 1745
 \end{aligned}$$

② FCFS



$$\begin{aligned}
 \text{Total Seek Time} &= 86 + 130 + 143 + 913 + 948 + 1022 + 1476 + 1309 + 1750 + 1774 + 1999 \\
 &= 1384
 \end{aligned}$$