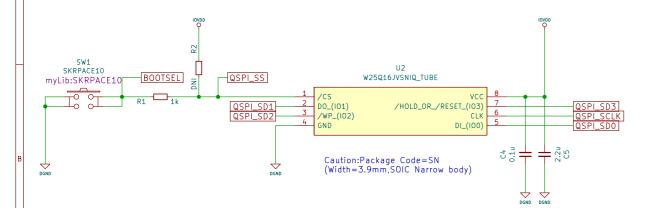
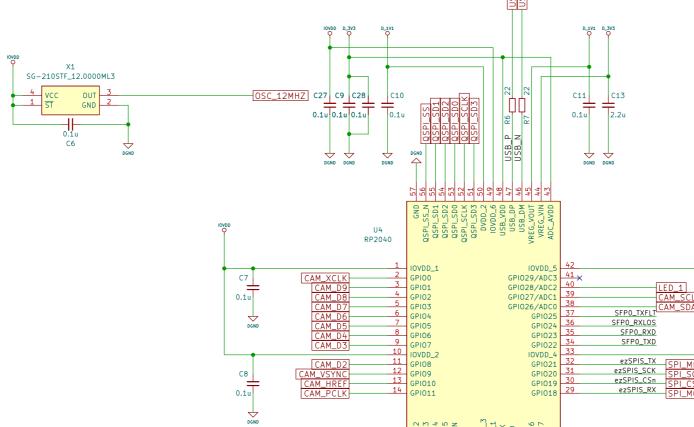
RP2040 eval board Dual Core CPU File: mcu.kicad_sch File: pwr_sup.kicad_sch LOGO MK1 LOGO myLib:kyogen5 S/N:KS-0003-07R4D Kyogen Sound Group. Sheet: / File: RP2040Board.kicad_sch Title: RP2040 with DVP Camera board Size: A2 Date: 2021-12-07 KiCad E.D.A. kicad (6.0.7)

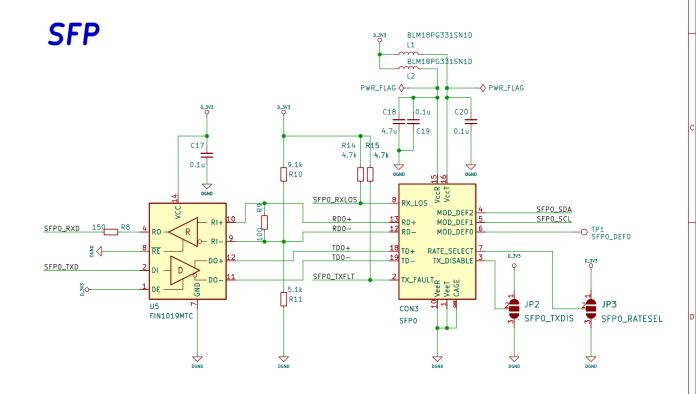
Boot Flash



RP2040 MCU

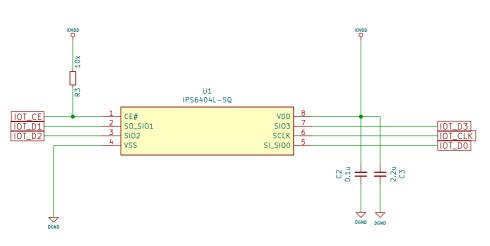


I2C Connection R12 R13 4.7k CAM_SCL CAM_SDA SFP0_SCL SFP0_SDA L2C_SCL I2C_SDA



RATESEL: 3V3 = High, OV = Low TX_DISABLE: 3V3 = Disable, OV = Enable

IoT SRAM

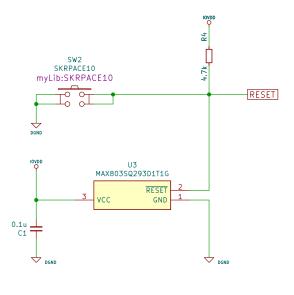


IOVDD Selection

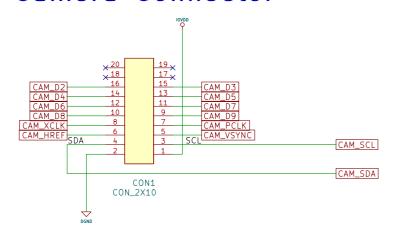


IOVDD:See section.2.9.7.4 on RP2040 DS. IOVDD domain:QSPI, GPIO, SWD, RUN, TESTEN(see Section 5.2)

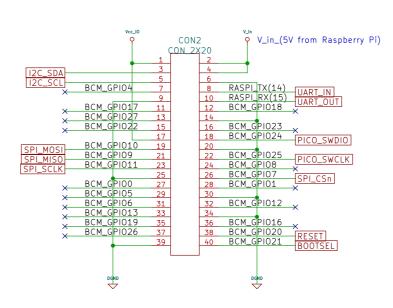
Reset Supervisor

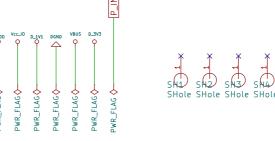


OV5642 DVP Camera Connector



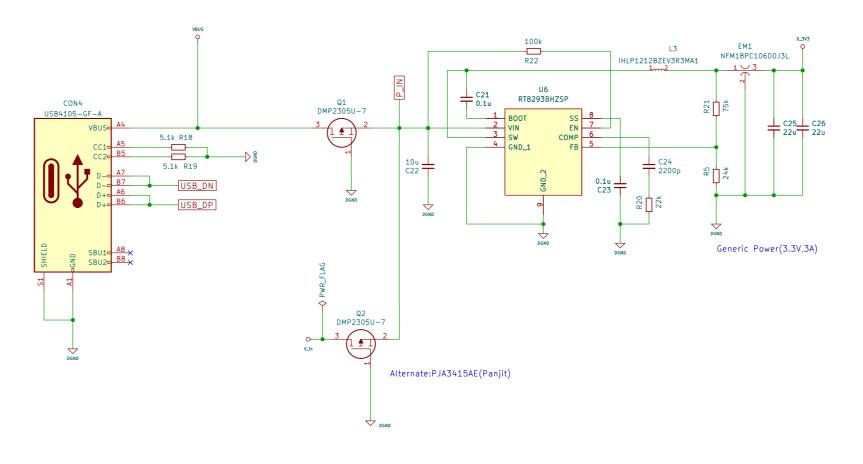
Raspberry Pi Connector





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S/N:KS-0003-07R4D	"
Kyogen Sound Group.	
Sheet: /Dual Core CPU/	
File: mcu.kicad_sch	
Title: RP2040 with DVP Camera board	
Size: A2 Date: 2021-12-07	Rev: 1.00
KiCad E.D.A. kicad (6.0.7)	ld: 2/3

USB and Power Supply



S/N:KS-0003-07R4D

Kyogen Sound Group.

Sheet: /Power Supply/
File: pwr_sup.kicad_sch

Title: RP2040 with DVP Camera board

Size: A2 Date: 2021-12-07 Rev: 1.00

KiCad E.D.A. kicad (6.0.7) Id: 3/3