## **SOFE 3650U: Software Design & Architecture**

## **Project Progress Report - Iteration 1**

Due Date: November 12, 2021

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## **Iteration 1**

- **Step 1: Review Inputs**
- **Step 2: Establish Iteration Goal by Selecting Drivers**
- **Step 3: Choose One or More of the Elements to Refine**
- **Step 4: Choose One or More Design Concepts That Satisfy the Selected Drivers**
- Step 5: Instantiate Architectural Elements, Allocate Responsibilities, and Define Interfaces
- **Step 6: Sketch Views and Record Design Decisions**
- Step 7: Perform Analysis of Current Design and Review Iteration