

TO WHOMSOEVER IT MAY CONCERN

This is to certify that Akash Yadav, a student of B.TECH ECE, 2025 from Babu Sundar Singh Institute Of Technology, Uttar Pradesh has successfully completed his VLSI Design Internship Program from 18/10/2023 to 02/12/2023.

During his internship program with us, he worked on the below mentioned project. AHB2APB bridge design using Verilog HDL.

We wish him all the best in his future endeavors.

For Maven Silicon Softech Pvt Ltd.

Sweety Dharamdasani

Head – Learning & Development

Place: Bengaluru Date:20/12/2023