Profile Information

RV-VLSI ID: 1ADADB263501

Career Objective

- TO WORK IN THE SEMICONDUCTOR INDUSTRY AND EXTEND MY LIMITS OF ACHIEVMENT.

Education Details				
Degree	**Discipline**	**School/College**	**Year of passing**	**Aggregate**
PG Diploma	Advanced Diploma in ASIC Design	RV-VLSI Design Center	2015	-
Master Degree	VLSI & ES	PESIT SOUTH CAMPUS	2014	77
PUC	-	PES PU COLLEGE	2008	92
SSLC	-	LFPS	2006	94

Project Details

Project Title	PHYSICAL DESIGN FLOW IMPLEMENTATION OF TORPEDO SUBSYSTEM.
Institue Name	RV-VLSI
Tools Used	IC COMPILER-SYNOPSYS.
Challenges	UNDERSTANDING THE TCL SCRIPTS AND HOW THE TOOL WORKS IN THE BACKGROUND.

Project Name	MATLAB IMPLEMENTATION OF WIMAX TRANSCEIVER
Institute Name	
Project Description	IMPLEMENTATION OF ALL THE BLOCKS OF TRANSCEIVER.
Challenges	CODING INBUILT FUNCTIONS OF MATLAB.
Tools	MATLAB

Project Name	IMPLEMENTATION OF 64 BIT MULTIPLIER USING VEDIC SUTRA
Institute Name	PESIT SOUTH CAMPUS
Project Description	IMPLEMENT A FASTER MULTIPLIER COMPARED TO CONVENTIONAL ARCHITECTURES.
Challenges	CHOICE OF ADDERS AND EXTENDING THE ALGORITHM TO 64 BIT.
Tools	XILINX MODELSIM