

Profile Information

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Career Objective

- Looking for a Full Custom job in India

Core Competancy

- Good understanding of Linux
- Familiar with the ASIC Design Flow
- Working knowledge of 90nm, 60nm, 45nm and 28nm process technologies
- Good understanding of MOS Device Physics
- Working knowledge of OPC and DFM rules
- Ability to analyse DRC and LVS errors

Education Details				
Degree	Discipline	School/College	Year of passing	Aggregate
PG Diploma	RTL Verification using System Verilog	RV-VLSI Design Center	2015	-
Degree	Electrical and Electronics	RVCE	2015	70
PUC	-	SSMRV	1987	88
SSLC	-	SJBHS	1985	60

Project Details

Project Title	Analysis of TLR
Institute Name	RV-VLSI
Tools Used	Mentor Graphics Calibre, Synopsys ICC, Linux
Challenges	Did not pay attention to record the drawing grid which resulted in a lot of off-grid errors

Project Title	Layout design of 28nm Standard cell library
Institute Name	RV-VLSI
Tools Used	Mentor Pxyxis, Calibre, Linux
Challenges	Power Ground Shorts were the toughest to fix

Project Name	Automatic Voice Recognition
Institute Name	RVCE

Project Description	Used Sample and Hold circuit 555 Timer The project is designed to control the printer and its accessories using a audio signature.
Challenges	Retrieval of the digitised signal was a challenge due to mismatch in the comparison algorithm
Tools	IC 741, S&H 747