

Profile Information

RV-VLSI ID: 14MVD0014

Career Objective

- Intend to build a career with leading corporate of Hi-tech environment with committed & dedicated People, which will help me to explore myself .

Education Details				
Degree	Discipline	School/College	Year of passing	Aggregate
PG Diploma	RTL Design using Verilog (Bridge Course for non VLSI Engineers)	RV-VLSI Design Center	2015	-
Master Degree	Microelectronics & VLSI Design	VIT UNIVERSITY	2015	8.24
PUC	-	Saraswati Shishu Mandir H.S. School, Raipur.	2009	89.80
SSLC	-	Saraswati Shishu Mandir H.S. School, Raipur.	2007	86.33

Project Details

Project Name	Frequency Counter Using AT89C2051 Microcontroller
Institute Name	
Project Description	Interfacing techniques of various modules with the microcontroller. Programming of microcontroller for proper reliable functioning in practical field.
Challenges	PCB DESIGNING, SOLDRING
Tools	AT89C2051 Microcontroller

Project Name	Design and Implementation of AMBA AHB protocol on ASIC
Institute Name	VIT UNIVERSITY
Project Description	Designed and implemented using TSMC 90nm technology. The simulation, synthesis and physical design was carried out. In the synthesis the design was optimized for the low power and speed. Also the physical design was carried out using 6 metal layer pr
Challenges	
Tools	Matlab, Xilinx ISE, Modelsim, Quartus, Cadence, Nclaunch , Atalanta, Silvaco