

Profile Information

RV-VLSI ID: 1ADADB263527

Career Objective

Education Details				
Degree	Discipline	School/College	Year of passing	Aggregate
PG Diploma	Advanced Diploma in ASIC Design - Full Custom	RV-VLSI Design Center	2015	-
Degree	Electricals Engineering	DON BOSCO INSTITUTE OF TECHNOLOGY	2015	72.4
PUC	-	SRI KUMARAN CHILDREN'S HOME COMPOSITE PRE-UNIVERSITY COLLEGE	2009	80.83
SSLC	-	B.E.T Convent	2007	92.4

Project Details

Project Title	9 Track Standard Cell Libraries Layout Design and Physical verification in 90nm CMOS Technology
Institute Name	RV-VLSI Design Center
Tools Used	ICStudio from Mentor Graphics : Pyxis Schematic and Pyxis Layout Editor, Calibre LVS and Calibre DRC.
Challenges	Fixing the LVS Errors

Project Title	Two Stage Op-amp Analog Layout Design and Physical Verification in 180nm CMOS Technology.
Institute Name	RV-VLSI Design Center
Tools Used	ICStudio from Mentor Graphics : Pyxis Schematic and Pyxis Layout, Calibre DRC , Calibre LVS.