Optimal Routing using the In-band Network Telemetry Framework with P4 Switches

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I. Introduction

Rapid progress has been made in programmable data planes in the past few years. The P4 programming language [1] has seen considerable adaptation to make the programmability of switches protocol and hardware independent. It enhances the use of Software Defined Networking and the OpenFlow API to configure forwarding devices and network behavior determination in a "top-down" approach. With the advent of P4, the overall programmability and ease of reconfigurability of the data plane have significantly increased. The implementation of P4 has also led to a variety of new use cases and research areas, such as DDoS Attack Prevention [2], improving Network Resilience paradigms [3], Load Balancing [4], and Traffic Engineering [5].

Within the P4 specification, the In-band Network Telemetry (INT) Framework has emerged to gather real-time information from the data plane. Active and passive network monitoring techniques have been used significantly to monitor network data resource usage. However, active monitoring does not give an accurate report on the network's performance, whereas passive networking is effective but has a high processing overhead that can lead to performance limitations. The INT Framework provides a means of performing network telemetry calculations within the data plane. There has been significant development in designing efficient monitoring systems through INT, such as IntMon [6] and INT Collector [7]. INT has also seen practical use in cases such as event detection [8], queue measurement [9], and improved real-time slice monitoring [10]. It has also made inroads in defining a new blend of Network architectures [11].

P4 and INT can also optimize routing strategies between multiple hosts. P4 switches have also been used as a backbone to create software systems for performance-based ISP routing [12]. P4 and INT have also minimized latency in autonomous networks with AI-based forecasting [13].

II. PROBLEM STATEMENT

The primary motivation for this problem statement is to gain a deep understanding of P4 Programming, specifically configuring an INT Switch Topology for network monitoring purposes. We will examine the different modes of operation offered while using the INT Framework (Fig. 1). With the

telemetry information, we intend to make real-time reconfigurations to the Open Shortest Path First (OSPF) routing protocol in our Controller Node and propagate forwarding table changes to the P4-based Programmable Switches using P4Runtime. The entire topology will be deployed as a slice on the FABRIC Testbed.

The objective is to optimize the dedicated resources and the overall bandwidth, and minimize latency across the entire network slice. The whole slice-level metrics will be measured using the Measurements Framework Library (MFLib) present in the FABRIC Testbed. Using the MFLib API will enable us to have a definitive way to store our metrics in ElasticSearch and easily connect to Grafana for Data Visualization purposes.

III. METHODOLOGY

In this section, we define the project flow to execute the requirements outlined in Section 2.

First and foremost, we describe the topology used for our experiments (illustrated in Figure 2). Host 1 acts as the SDN Controller used to run P4Runtime to configure the switches in the topology. It will also determine the routing protocol configuration to make real-time optimizations in the routing strategy used to transfer data to the receiving destination host (i.e., Host 2). The switches defined are completely programmable BmV2 P4 switches. Each switch performs critical tasks to implement the INT Framework. We describe the different INT Switch Roles as follows:

- INT Source: Creates and inserts INT Headers into the packets it sends. A Flow Watchlist is configured to select the flows in which INT Headers are to be added/modified.
- INT Sink: Extracts the INT Headers and collects the path state contained in the INT Headers. The collected information is sent to the monitoring system for further processing and storage.
- INT Transit Hop: Collects metadata from the data plane by following the INT instructions.

The information collected in the INT Headers will include metrics such as hop latency and buffer queue time. The Network Monitor stores the information collected from the INT Packets. A critical function of the Network Monitor is efficiently detecting important network events while balancing with minimizing computation overheads and adding latency

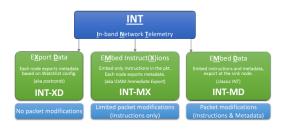


Fig. 1. INT Modes of Operation [14]

to the overall network.

Secondly, using the information processed and stored using the INT Framework, we wish to dynamically modify the initial routing strategy based on hop latency, traffic flow rate changes, etc. This will also include dynamically reconfiguring the forwarding tables in the P4 switches.

Lastly, we will attempt to demonstrate the effects of the routing modifications on the network slice's overall throughput and latency compared to the original approach used.

IV. PROJECT DELIVERABLES

The following are the incremental steps we will follow during the course of the project:

- 1) Walkthrough of P4 tutorials; implementing and deploying a basic P4 program on FABRIC.
- 2) Writing customized P4 programs for implementing the INT Specification. This includes analysis of the three modes of INT operation and setting up a basic topology with INT Sources and Sinks.
- 3) Setting up the proposed topology on FABRIC, implementing the entire telemetry system with minimal network and processing overheads, and visualizing the telemetry gathered when the hosts communicate. This stage will also include writing a codebase using the MFLib API to monitor and log network slice information continuously.
- 4) Devising a dynamic optimal routing strategy based on the telemetry gathered.
- Devising a means of dynamically changing the route forwarding tables based on the new optimal routing strategy and observing the changes.
- 6) Visualize the telemetry and slice information based on overall throughput and latency. We will also attempt to visualize a simulation showcasing the dynamic route switching capability.

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