;Name : Lungade Kiran Sanjay

;Roll no: 39

;Batch: S2

;Subject: MPMCL 6

;Program to generate a square wave of 50Hz frequency on pin P2.3

```
        org 0x0000
                LJMP Start


                org 0x0100


Start: MOV TMOD,#10H          ;timer1

                                              ;mode1
        Again: MOV TL1,#00
                MOV TH1,#0DCH
                SETB TR1


        Back: JNB TF1,Back
                CLR TR1
                CPL P2.3
                CLR TF1
                SJMP Again


                END
```

Registers

Register	Value
Regs	
r0	0x00
r1	0x00
r2	0x00
r3	0x00
r4	0x00
r5	0x00
r6	0x00
r7	0x00
Sys	
a	0x00
b	0x00
sp	0x07
sp_max	0x07
PC $	C:0x010B
auxr1	0x00
dptr	0x0000
states	9402049
sec	10.20187609
psw	0x00

Project | Templates | Registers

39p6.a51

```
1        ;Name : Lungade Kiran Sanjay
2        ;Roll no: 39
3        ;Batch: S2
4        ;Subject: MPMCL 6
5        ;Program to generate a square wave of 50Hz frequency on pin P2.3
6
7            org 0x0000
8                LJMP Start
9
10               org 0x0100
11
12       Start: MOV TMOD,#10H        ;timer1
13                                    ;mode1
14       Again: MOV TL1,#00
15                 MOV TH1,#0DCH
16                 SETB TR1
17
18       Back: JNB TF1,Back
19               CLR TR1
20               CPL P2.3
21               CLR TF1
22               SJMP Again
23
24               END
```

Timer/Counter 1

Timer/Counter 1

Mode

1: 16 Bit Timer/Counter

Timer

TCON: 0x40   TMOD: 0x10

TH1: 0xFB   TL1: 0x01

☑ T1 Pin   ☐ TF1

Control

Status: Run

☑ TR1   ☐ GATE   ☑ INT1#

Logic Analyzer

Setup... | Load... | Save... | Min Time 0 s | Max Time 10.20188 s | Grid 10 ms | Zoom In Out All | Min/Max Auto Undo | Update Screen Stop Clear | Transition Prev Next | Jump to Code Trace | ☑ Signal Info ☐ Show Cycles | ☐ Amplitude ☑ Cursor | ☐ Timestamps Enable

P2

9.937777 s   9.992995 s   0 -> 1   10.0677   10.06958 s, d: 76.58095 ms   0, d: 0   10.20778 s