Kiran S Patil

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An Electronics and Communication third-year undergraduate student seeking to pursue a career in the field of Robotics, Embedded Systems and Deep Learning.

EDUCATION _

| Degree | College/School | University/Board | Passing Year | Percentage |
|-------------------------------------|------------------------------------------------|--------------------------------------------------------------|-----------------|--------------------------------------|
| B.E (Electronics and Communication) | KLS Gogte Institute Of Technology, Belgaum | Autonomous Under Visvesvaraya Technological University | 2019 | 8.23 CGPA (1st-sem to 5th-sem) |
| 12 th | Kendriya Vidyalaya No.2 Cantonment, Belgaum | Central Board of Secondary Education, Delhi | 2015 | 83.80% |
| 10^{th} | Kendriya Vidyalaya No.2 Cantonment, Belgaum | Central Board of Secondary Education, Delhi | 2013 | 8.4 CGPA |

PROJECTS _____

1. Harvester Bot-eYRC 2017

An autonomous robot which identifies (using image processing), plucks and deposits the required fruits on an abstract farm.

2. Path finder

An autonomous robot that can traverse to its given destination even when an obstacle is introduced in its path, by managing to take an best alternative path to its destination.

3. Obstacle detector

A prototype using Arduino Uno and HC-SR04(Ultrasonic sensor), which detects an obstacle/object and returns the distance.

4. Object Identifier

Identification of an object based on its colour and size using image acquisition technique in MATLAB.

TRAINING & INTERNSHIP

• RTL Design using Verilog

Certification course under IEEE blending learning program

RESEARCH PUBLICATIONS _____

1. None

TECHNICAL SKILLS

- **Programming Languages**: C, Matlab, C++, Python, Embedded C, ARM Assembly, HTML, CSS, Verilog HDL.
- Hardware Knowledge :- Atmel ATmega 2560, Arduino, Raspberry Pi, LPC2148.
- Software Knowledege :- OpenCV, Numpy, Cadence Virtuoso, Keil uVision4, Xilinx ISE, Vivado, Multisim and Simulink.