

# KIRAN JOJARE

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## EDUCATION

Master of Science, Electrical and Computer Engineering, University of Colorado, Boulder, USA (GPA: 3.7).	Aug 2022 - Aug 2024
PG Diploma - Embedded System Design, Centre For Development of Advanced Computing, Pune, India (GPA: 4.00).	Aug 2018 - Feb 2019
Bachelor Of Technology, Electronics Engineering, Government College of Engineering, Chandrapur, India (GPA: 9.18).	Jun 2015 - Aug 2018

## SKILLS

- Language Skills:** C, C++, Assembly, Python, M-Script(MATLAB Scripting), Bash Scripting.
- Hardware:** STM32G0x, ESP32, TI2F28xx, STMSPCx, , Renesas RH85, STM34F4x, Jetson Nano, EFR32 Blue Gecko, Beaglebone Black, Raspberry.
- Tools and Technologies:** Code Composer Studio, SPC5-Studio, Espressif, MCU-Express, STMCube, Simplicity Studio, Simulink, MATLAB, Polyspace, System Desk, EB Tresos, CANdb++, CANalyzer, CANoe, Target-Link, Doxygen, Dhrystone, GDB, GCC, Valgrind, Objdump, OCPP.
- Protocols & Peripherals:** SPI, I2C, UART, SCI, PWM, ADC/DAC, CAN, LIN, Bluetooth Low Energy (BLE), TCP/IP, UDP, USB, DoIP, MQTT, DMA.

## EXPERIENCE

<b>Firmware Engineer, Eaton</b> , Peachtree City, Georgia, USA.	Aug 2024 – Present
• Architected and built the <b>Smart Breaker 2.0 droop control</b> feature using event-driven architecture on STM32G0 and ESP32 microcontrollers with <b>FreeRTOS</b> , <b>TDD</b> , and <b>UART-based line communication</b> ; delivered a validated POC deployed in multiple <b>DER systems</b> .	
• Assessed multiple open-source stacks and developed <b>in-house OCPP 1.6 stack</b> for chargers; owned the lifecycle from POC to deployment.	
• Led the <b>Customer Success team</b> on post-production firmware issues, ensuring stability, field issue triaging, and root cause analysis.	
<b>Firmware Engineering Intern, Danfoss Editron</b> , Longmont, Colorado, USA.	Jan 2024 – Aug 2024
• Collaboratively developed brushless DC motor controller firmware using <b>TI 280x DSPs</b> for torque and speed control, with CAN and RS232.	
• Utilized DAQ-based <b>fault injection C code</b> to simulate safety-critical faults in ECU, reducing Dyno testing time by 30% using the CAN library.	
• Implemented a <b>C-state machine</b> for IGBT Gate Driver diagnostics on <b>UCC5870Q1 Gate Driver IC</b> using SPI firmware modules, and <b>HIL Testing</b> .	
• Utilized <b>PWM</b> on the TI TMS320F2802 ECU as a DAC to monitor and verify the conversion of PWM duty cycles into analog voltage signals	
<b>Embedded Platform Software Intern, Canoo</b> , Torrance, California, USA.	May 2023 - Aug 2023
• Improved CAN Library to include <b>29-bit ID CAN</b> signal for <b>STM32SPC56 ECU</b> , enhancing compatibility for Canoo's Vehicle Platform team.	
• Restructured OTA functionality on STMSPC58 MCUs for Canoo's ECU, using <b>seed-key exchange algorithm</b> and leveraging CAN and <b>DoIP</b> .	
• Initiated <b>CAPL</b> integration within CANoe for CAN signal analysis using <b>UDS</b> , contributing to advanced diagnostics and optimization.	
<b>Student Firmware Engineer, University of Colorado Boulder</b> , Boulder, USA.	Aug 2022 - Apr 2023
• Coordinated firmware development for the SWARM-EX Cube Satellite program, enhancing Aptina MT9 series camera modules and facilitating communication between <b>Pocket Beagle</b> and <b>dsPIC33</b> adopting <b>I2C &amp; UART</b> , showcasing expertise in C firmware.	

<b>Senior Engineer, ZF Friedrichshafen AG</b> , Hyderabad, India.	Jun 2021 - Jul 2022
• Engineered Cubi-X vehicle motion firmware on <b>NXP S32K3 MCU</b> , incorporating an Actuator Control Subsystem state machine via <b>Embedded C and Simulink</b> , while achieving <b>100% ISO 26262</b> compliance for bolstered motion control safety (ASIL-D and ASIL-C).	
• Integrated <b>Microcontroller Abstraction Layer (MCAL)</b> within the <b>AUTOSAR</b> architecture of NXP S32K3 MCU for advanced diagnostic communication feature, enabling efficient fault detection and reporting in-vehicle systems, in compliance with AUTOSAR standards.	
• Streamlined reliable firmware using C/C++ & Simulink, following <b>MISRA C, J-MAAC</b> standards, and AUTOSAR guidelines for 6 Cubi-X features.	

<b>Embedded Software Engineer, KPIT Technologies</b> , Pune, India.	Mar 2019 - May 2021
• Created ECM features for Cummins, overseeing full SDLC; achieved a <b>15%</b> reduction in MCDC software issues, enhancing system reliability.	
• Utilized embedded C/C++ firmware modules employing <b>RH850 – F1H/F1K MCUs</b> . Leveraged <b>RTWEC</b> resulting in efficient coding standards.	
• Executed NOx sensor's <b>RTOS</b> embedded firmware for RH850 MCU leveraging C, Simulink/Stateflow & PID based Control System in MATLAB.	

## PROJECTS

<b>Low Power Gesture-Controlled Classroom Using BLE and OTA with EFR32 Blue Gecko</b>	Aug 2023 - Dec 2023
• Implemented a <b>client-server architecture</b> on EFR32 Blue Gecko boards using <b>C-based state machines</b> , integrating <b>BLE GATT services</b> , <b>OTA update mechanisms</b> , and real-time gesture ( <b>I2C</b> ) and PIR ( <b>ADC/PWM</b> ) sensor data handling via HAL drivers.	
• Strategically achieved <b>32uA power consumption</b> in optimized low-power mode, utilizing power management techniques and sleep modes.	
<b>USB Based Communication between STM32F4F4 and AT89C51 Microcontrollers</b>	Dec 2022 - May 2023
• Constructed firmware for <b>USB communication</b> between an <b>STM32F411E-Discovery board</b> and an <b>AT89C51</b> microcontroller.	
• Integrated a GT-521F32 fingerprint sensor with the STM32F4 microcontroller, implementing <b>USB to TTL converters</b> for seamless data transfer of fingerprint sensor from STM32F411E to the AT89C51 microcontroller. Executed firmware on the AT89C51 to display it on OLED using <b>SDCC</b> .	
<b>Concurrent Containers Using Open MP C++ Programming Model</b>	Sep 2022 - Jan 2023
• Resolved high-performance <b>thread-safe data structures</b> , like SGL Stack/Queue, M&S Queue, and Trieber Stack, achieving a <b>20% increase</b> in concurrency efficiency through the use of advanced techniques like <b>elimination and flat combining</b> .	
• Utilized <b>OpenMP</b> and <b>C/C++</b> to execute optimization, increasing L1 Cache Hit Rate by 15% and improving Branch Pred Hit Rate by 25%.	
<b>Smart Distributed Control System for Excavators via FreeRTOS</b>	Aug 2022 - Dec 2020
• Built a FreeRTOS-based distributed control system with PWM-controlled servo motors and ADC joystick input, using a <b>Shared Clock scheduling algorithm</b> to sync Beaglebone Black and STM32F407VG, improving efficiency by 30% and precision by 25%.	