

Aidan Kirk, WORK LOG

Milestone 1 work

Friday, March 31, 2023:

Met with team: 1 hour

- Arranged the group meeting today. We talked about the overview of what we want to do, and decided on load store (tentatively)

Sunday, April 2, 2023:

- Today, we all worked together to try to understand the stack architecture a little bit more. We hit some roadblocks, so we are going to ask the professor about a quick overview of stack architecture after class tomorrow. I added the “assembly to machine language” and “High-level code to assembly” sections to the document and helped with the others a little. [2 hours]

Monday, April 3, 2023:

- Today, since our group could not meet, I got a head start on the process of writing an assembler. Got basic file read/write for translating assembly to machine code. [3 hours]

Tuesday, April 4, 2023:

- Today, we finished the bulk of the milestone. Dalton and I wrote the RelPrime and GCD program in our assembly language. Decided on our procedure calling conventions and touched up on mistakes we made in the last two meetings. [4 hours]

Wednesday, April 5, 2023

- I had to make some last-minute changes to M1, such as adding the pushi instruction and adding the tables underneath the “basic code to assembly” section. [1 hour]

Milestone 2 work

Friday, April 7 – April 14, 2023

- Finished the assembler (discounting any changes that might be needed if we change instructions) during spring break and started on a compiler in my free time. [10 hours]

Monday, April 17, 2023

- Group meeting. I wrote all of the RTL for our language and made some minor changes to our instruction set. [2.5 hours]

Wednesday, April 19, 2023

- Meeting in class with Dalton. [1.5 hours] I originally wrote RTL for single cycle, so today we converted this into multicycle RTL. We also changed the design document to accommodate for the feedback from M1.