# Kirshanthan Sundararajah

School of Electrical and Computer Engineering Purdue University 465 Northwestern Avenue West Lafayette, IN 47907 □ ksundar@purdue.edu
□ 765-775-0153
⋈ kirshanthans.github.io
⋈ www.linkedin.com/in/kirshanthan

## **EDUCATION**

Ph.D., Purdue University, West Lafayette, IN.

Major: Electrical and Computer Engineering

Aug 2015 - May 2022

B.Sc.Eng.(Hons), University of Moratuwa, Katubedda, Sri Lanka. Major: Electronics and Telecommunication Engineering Jul 2009 - Mar 2014

# PROFESSIONAL EXPERIENCE

• Graduate Research Assistant @Purdue University PLCL Group

Aug 2015 - Present

- PolyRec: Framework for composing and verifying transformations for nested recursion and loops.
- DARM: Framework for melding similar control-flow graphs.
- **Grafter**: Framework for fusing general recursive traversals over heterogeneous trees.
- Treelogy: Benchmark suite for tree traversals.
- **HACCLE**: An ecosystem for *Secure Multi-Party Computations*.
- Software Engineering Intern @Nvidia GPU Compiler Group

Sep 2020 - Dec 2020

- Diesel Compiler: Warp specialization and pipelining for GPU kernels.
- Software Engineering Intern @Reservoir Labs

June 2020 - Aug 2020

R-Stream Compiler Group

- ParSEC Backend: A task-based runtime backend for R-Stream polyhedral compiler.
- **Research Intern** @Microsoft Research RiSE Group

June 2018 - Sep 2018

- Parallelizing Word2Vec: Parallelizing and scaling Word2Vec training to execute on many cores.
- Associate Electronic Engineer @Zone24x7 Inc. Signs24x7 Group

May 2012 - Oct 2012

- **Image Compression Algorithm**: Implementation of memory efficient image compression algorithm, supposed to perform decompression on an *STM32 microcontroller* based system.
- Clock Synchronization Algorithm: Implementation of real-time clock synchronization algorithm, deployed on an *ARM microprocessor* runs *embedded Linux*.
- Hardware Abstraction Layer: Implementation of *Hardware Abstraction Layer (HAL)* for radio communication protocol stack of *Electronic Paper Display(EPD)*, driven by an *STM32 microcontroller*.

### **PUBLICATIONS**

- Y Bao, K Sundararajah, R Malik, Q Ye, C Wagner, N Jaber, F Wang, M H Ameri, D Lu, A Seto, B Delaware, R Samanta, A Kate, C Garman, J Blocki, P Letourneau, B Meister, J Springer, T Rompf, and M Kulkarni "HACCLE: Metaprogramming for Secure Multi-Party Computation" in *International Conference on Generative Programming: Concepts and Experiences*, GPCE 2021. [ACM DL]
- **K Sundararajah** and M Kulkarni "Composable, Sound Transformations of Nested Recursion and Loops" in *Programming Languages, Design and Implementation*, PLDI 2019. [ACM DL]
- L Sakka, **K Sundararajah**, R R Newton, and M Kulkarni "Sound, Fine-Grained Traversal Fusion for Heterogeneous Trees" in *Programming Languages*, *Design and Implementation*, PLDI 2019. [ACM DL]
- L Sakka, **K Sundararajah** and M Kulkarni "TreeFuser: A Framework for Analyzing and Fusing General Recursive Tree Traversals" in *Object-Oriented Programming, Systems, Languages, and Applications*, OOPSLA 2017. [ACM DL]
- N Hegde, J Liu, **K Sundararajah**, and M Kulkarni "Treelogy: A Benchmark Suite for Tree Traversals" in *IEEE International Symposium on Performance Analysis of Systems and Software*, ISPASS 2017. [IEEE Xplore]
- K Sundararajah, L Sakka, and M Kulkarni "Locality Transformations for Nested Recursive Iteration Spaces" in *Architectural Support for Programming Languages and Operating Systems*, ASPLOS 2017. [ACM DL]
- **K Sundararajah** and S Jayasena, "Model-based Input-adaptive Vectorization" in *Moratuwa Engineering Research Conference*, MERCon 2016. [IEEE Xplore]
- K Sundararajah, L Logeswaran, P N D Panagoda, L P Wijesinghe, D V S X De Silva, and A A Pasqual, "Layered Depth Image Based HEVC Multi-view Codec" in *Advances in Visual Computing: Proceedings of the International Symposium on Visual Computing*, ISVC 2014. [Springer]

## **ACHIEVEMENTS**

#### Awards

- Bilsland Dissertation Fellowship 2021-2022, Purdue University.
- *Electrical and Computer Engineering Fellowship 2015-2016*, Purdue University.
- V. K. Samaranayake Research Assistantship 2014-2015, University of Moratuwa.
- Mahapola Merit Scholarship 2009-2014, University of Moratuwa.

### Grants

- ACM Travel Grant to Attend PLDI 2019.
- ACM Travel Grant to Attend SPLASH 2018.
- ACM Travel Grant to Attend ASPLOS 2017.
- ACM Travel Grant to Attend PLDI 2016.
- ACM Travel Grant to Attend CGO 2015.

# Competitions

- Silver Medal in ACM Student Research Competition at SPLASH 2018.
- Placed  $25^{th}$ ,  $34^{th}$ ,  $29^{th}$  and  $45^{th}$  correspondingly in *IEEExtreme 7.0, 6.0, 5.0 and 4.0*.
- Placed 4<sup>th</sup> in Sri Lanka Robot Competition (SLRC) 2012.
- Champions of Inter-University Statistics Quiz Competition 2010, University of Colombo, Sri Lanka.
- Participated at International Mathematics Olympiad Competition(IMO) 2009, Bremen, Germany

# **TEACHING EXPERIENCE**

• ECE 368 Data Structures [Instructor] @Purdue University

Summer 2021

• ECE 295 Introduction to Data Science [TA] @Purdue University

Fall 2020

• ECE 295 Introduction to Data Science [TA] @Purdue University

Summer 2019

• CS 1032 Programming Fundamentals [TA] @University of Moratuwa

Mar 2014 - Jun 2015

# **SERVICE**

- Member of Artifact Evaluation Committee for PLDI 2020.
- Organizer of PurPL weekly seminar and PurPL Fest 2019.
- Program Committee Member of ECOOP 2019 Doctoral Symposium.
- Collaborative reviewer for POPL 2019.
- Student volunteer for SPLASH 2021, PLDI 2019, SPLASH 2018, and PLDI 2016.