You can do this assignment in a Group of at most 3 students. Due: Oct. 8, 2023

Marks [100+15]

P1. Cost-effective branch predictors [50 marks]

In this exercise you are required evaluate different branch predictors using the Champsim simulator [1]. You will evaluate (i) a G-share predictor, (ii) a perceptron predictor, and (iii) any variant of the TAGE predictor. The Champsim code already implements the first two predictors. You are free to use any open-source code available for the TAGE predictor (with 4 tables) and integrate it with the Champsim. To make the comparison fair, you will consider equal hardware cost, roughly 128KB (or 128KB $\pm$ 2KB) for each branch predictor. Each predictor uses a BTB of 2048 entries which is not included in the storage budget.

a. Evaluate 3 SPEC programs and report MPKI (Mispredictions per Kilo Instructions) and IPC for the different predictors. Each group will be given a distinct set of 3 benchmark traces which should be used in the evaluation.
b. Explore a different set of history lengths and different table sizes for the TAGE predictor, but keeping the storage budget for the predictor to 128KB, to see if the MPKI and/or IPC can be improved further.
c. Explore a hybrid perception and TAGE predictors with a total storage of 128KB (for the two predictors). Assume you have an additional storage budget of 1KB for the meta predictor. The relative storage allocated to the individual predictors (perceptron and TAGE) can be varied as 30:70, 50:50, or 70:30.
d. Based on the above experiments and the knowledge you have gain, can you design a hybrid predictor (involving any of these 3 predictors) that perform the best for your benchmark set? Report the rationale for the improved design and its observed performance. This problem is **optional** and **bonus marks** [**15**] will be given for those who come up with a successful design!

Report your findings in a short report (~4-6 pages). Your report should include the simulation methodology used and a detailed performance analysis. Make sure you simulate at least 250 Million instructions in the detailed mode after sufficient fast-forwarding and warmup.

[1] N. Gober, et. al., "The Championship Simulator: Architectural Simulation for Education and Competition", https://arxiv.org/pdf/2210.14324.pdf

[2] Champsim: https://github.com/ChampSim/ChampSim

[3] SPEC traces: https://dpc3.compas.cs.stonybrook.edu/champsim-traces/speccpu/

P2. Obtaining CPI Stack for Programs using Hardware Performance Counters and Linear Regression [50 marks]

In this exercise you are required to obtain the CPI (Cycles Per Instruction) stack for programs using hardware performance monitoring counters. CPI stack divides the

total CPI of an application into components that reflect the time spent in various events, such L1-I Cache misses, L1-D cache misses, L2/L3 Cache misses, I-TLB and D-TLB misses, branch misprediction, etc. (See [1] for details regarding CPI Stack). The counts of various miss events occuring during program execution can be obtained using hardware Performane Monitoring Counters (PMC) on modern architectures. (See e.g., [2] for details regarding PMCs available on Intel Skylake Processor). Performance counter values for running application can be obtained using performance monitoring tools such as PAPI [3] or `perf` [4].

You are required to run a set of programs on your laptop/desktop and collect performance counter values for various events. You are required to develop a simple linear regression model (using simple linear terms) for CPI for each application. Since we are interested in the CPI stack where the contribution due to different miss events are supposed to be additive, the regression model should have only non-negative coefficients. The regression model (with additive terms) essentially gives the CPI stack. For each program-input pair, there would be a separate regression model which gives the CPI stack for that program.

To build the regression model, one requires a training data set consisting of at least a few hundred to may be a few thousands data points for a given program-input pair. One crude solution is to measure the PMC values over several intervals (could be fixed intervals of say 100M instructions/cycles or 10mSec of execution), so that for a single program we can have many data points. The interval size given above is only indicative. It (the interval size) should be chosen appropriately so that there are enough data points for single program-input pair and the intervals are sufficient large to give meaningful counter values for building the CPI stack. Note that the model would be somewhat approximate due to various reasons. Also, validating the models may be more involved (using simulators) and is not be a part of the exercise.

You are required to develop the CPI stack (or separate linear regression model) for a set of 4 benchmark programs. Each group will be given a distinct set benchmarks which should be used in the evaluation. In each case choose the appropriate input for the program. You can experiment with different interval sizes and different sets of features (miss events) in building your regression model. Report the RMSE, R^2, adjusted R^2 values, Residuals, F-statistic and p-value to assess the quality of the model generated of the model. Comparing the CPI stacks across the programs, you can report your observations for different programs. You are required to submit a report (not exceeding 6-pages) summarizing your findings.

A few remarks/points to be noted are listed below:

(a) As the performance counters available on a processor differ across generation of processors, you can go through the processor manual to know the list of available counters specific to your processor generation.

(b) In order to read these performance counters, performance monitoring tools like perf and PAPI can used in Linux environment. Perf tool presents a simple command line interface and can be installed as a linux utility tool using the command "apt-get install linux-tools-common linux-tools-generic linux-tools-`uname -r`"

install linux-tools corresponding to your exact Linux Kernel version. You can read more about perf at https://perf.wiki.kernel.org/index.php/Tutorial

(c) PAPI provides APIs that can be inserted in the source code to query the performance monitoring counters.
You can find more information about PAPI and installation at https://icl.utk.edu/papi/

(d) When you run the application on your laptop/desktop and gather performance counter values, make sure that no other processes (like browsers, video players, other C programs etc.) are running on your system/or on the specific core as they could influence the counter values. You can also pin your process to a specific core and run the above tools to read counters on that core. Also ensure you run all the experiments in the same system while collecting the performance counters

(e) You can choose applications which run for atleast 10 seconds so that there are at least 1,000 data points generated. Note that these numbers (interval size and the number of data points) are indicative. You can choose appropriate values, appropriate regions of interest in the program, but document these in your report.

(f) You should use as many miss events as required for the model. Note that some of the miss events may not figure as significant terms in the linear model for some benchmark-input pair . It is receommended that you measure at least 7 miss events for developing the model. In building the model you can consider only linear terms and no need to consider interaction terms (interaction of two events and their counter values).

(g) In building the regression model the feture values in the train data could be normalized so that individual feature values are always between 0 and 1.

(i) In building the model, you should take care to avoid over-fitting the data. You could use some part of the data (chosen randomly) to perform cross-validation.

(j) You can use available R-packages (or other open source software) for building the regression model.

[1] A Top-Down Approach to Architecting CPI Component Performance Counters
Article in IEEE Micro · February 2007
[2] Intel® 64 and IA-32 Architectures Software Developer's Manual - Volume 3B: System Programming Guide, Part 2
[3]PAPI User's Guide. Available at:
http://icl.cs.utk.edu/projects/papi/files/documentation/PAPI_USER_GUIDE_23.htm
[4] Linux Kernel Profiling with perf. Available at:

https://perf.wiki.kernel.org/index.php/Tutorial